

CICS TS V5 Performance Improvements that You Definitely Don't Know About.

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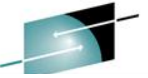


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Agenda

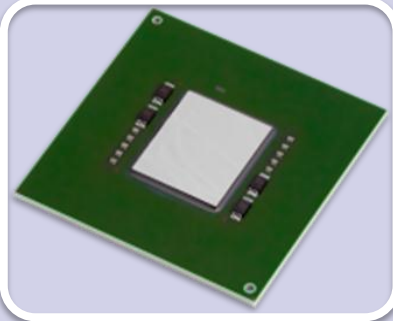
- What do we mean by “Performance”?
- CICS TS V5.3 open beta – Performance improvements
- Improvements in CICS TS V5.2 and V5.1 you should know about
- What can you do to improve performance?

What do we mean by “Performance”?

What do we mean by “Performance”?



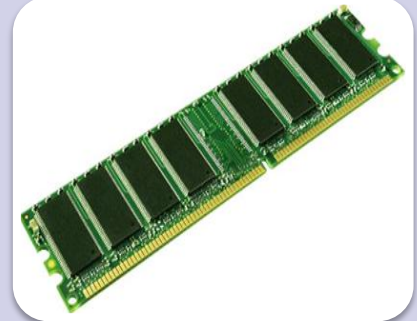
Response
Time



CPU
Usage



Throughput



Memory
Usage

CICS TS V5.3 open beta – Hills

- Design Thinking and Performance
 - Hill 2 - A capacity planner can demonstrate, in routing and data owning regions, a CPU reduction of 10 to 20%.
- Reduce the overheads of connecting to your Systems of Record
- Customer and Sponsor User feedback used to determine areas to work on
 - Popular and growing communications
 - Understanding what is going on

CICS TS V5.3 open beta – Performance Improvements

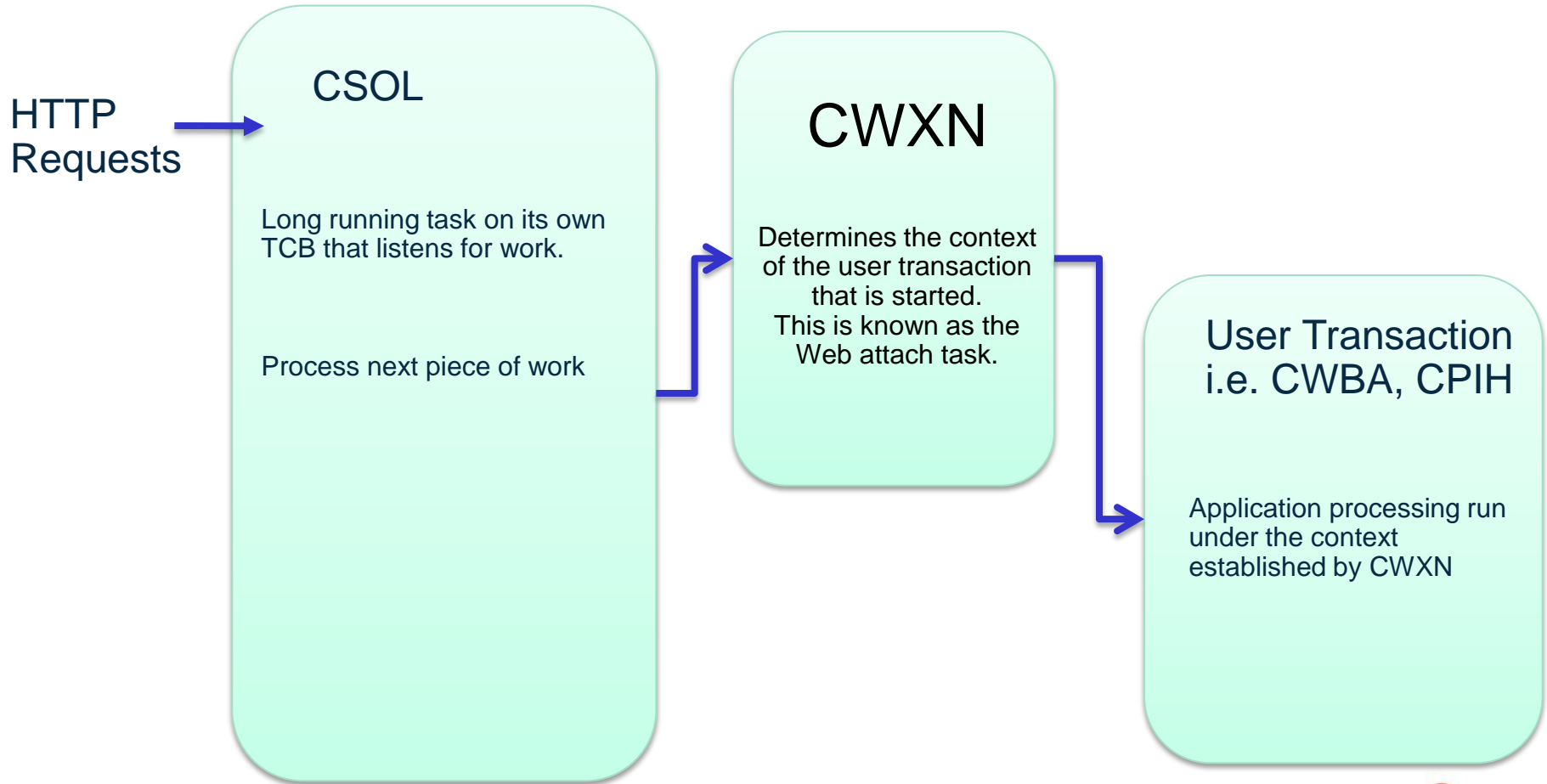
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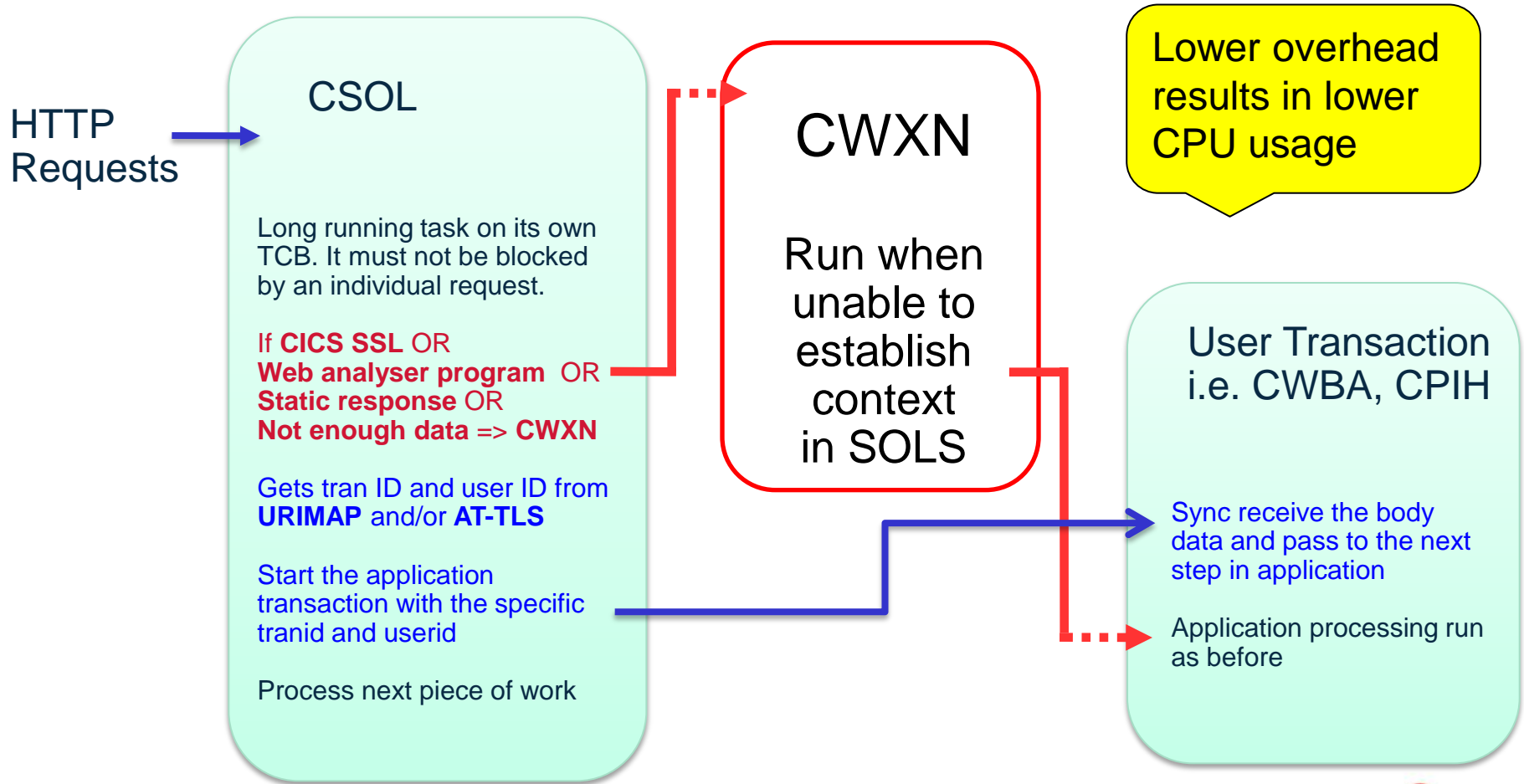
CICS TS V5.3 open beta – Performance Improvements

- HTTP efficiency, including for web services
- SSL/TLS improvements
- Other areas of improvement
- Some numbers from a CICS TS V5.3 open beta development level

HTTP Pre open beta

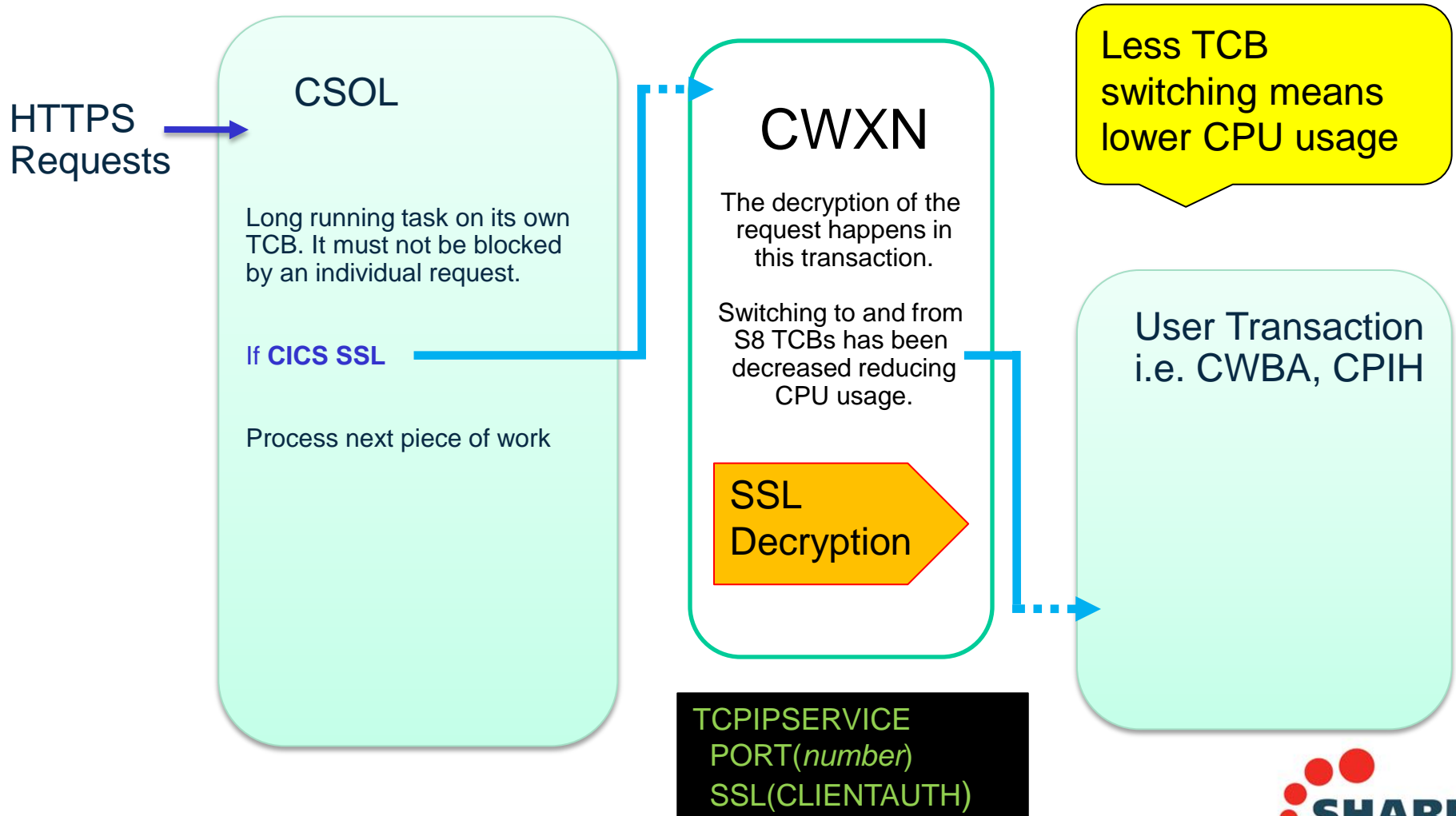


HTTP in CICS TS V5.3 open beta



Lower overhead results in lower CPU usage

SSL in CICS TS V5.3 open beta



AT-TLS Aware CICS TS V5.3 open beta

z/OS Communications Server

HTTPS Requests



AT-TLS Support

CICS TS V5.3 open beta

CSOL

Long running task on its own TCB. It must not be blocked by an individual request.

If CICS-SSL OR
Web analyser program OR
Static response OR
 Not enough data => CWXN

Can get **user ID** from **AT-TLS**

Start the application transaction with the specific tranid and userid

Process next piece of work

User Transaction
 i.e. CWBA, CPIH

EXTRACT CERTIFICATE
 commands still work

CWXN

Run when unable to establish context in SOLS

HandShakeRole=servwithclientauth
 ClientAuthType=FULL

TCPIP SERVICE
 PORT(*number*)
 SSL(ATTLISAWARE)

Liberty z/OS Connect feature in CICS

- z/OS Connect provides a way to have RESTful APIs with JSON payloads between CICS, mobile devices, and cloud environments
- The performance of this feature within the CICS TS V5.3 open beta has been improved.
 - Also for CICS TS V5.2 via APAR PI32451

Enhanced Metrics

- Metrics have been added to global CICS statistics, including **transaction CPU** time measurements captured without needing CICS monitoring to be active - allows greater insight into CPU resource usage of CICS TS V5.3 open beta regions without the overhead of collecting and processing SMF 110 monitoring records.
- *Transaction tracking identifies relationships between application tasks as they flow across CICS systems, and has been extended to transactions started by the CICS-WebSphere MQ bridge. This expands the scope of transactions that can use **transaction tracking**, to help with problem determination, reporting and auditing.*

Other Performance Enhancements

- Trace Facility
 - Use of z9 hardware instructions, including store clock fast
- Monitoring Facility
 - Excluding fields is now less efficient than collecting all of them
 - Use of z9 hardware instructions, including store clock fast
 - Some level 1 trace moved to level 2
 - Reduced internal lock contention
- MRO connections with high session counts
 - Improved the algorithm to make it more efficient
- Cache alignment of control blocks, the use of prefetch, and tuning of other internal algorithms also contribute to general CPU usage reductions.

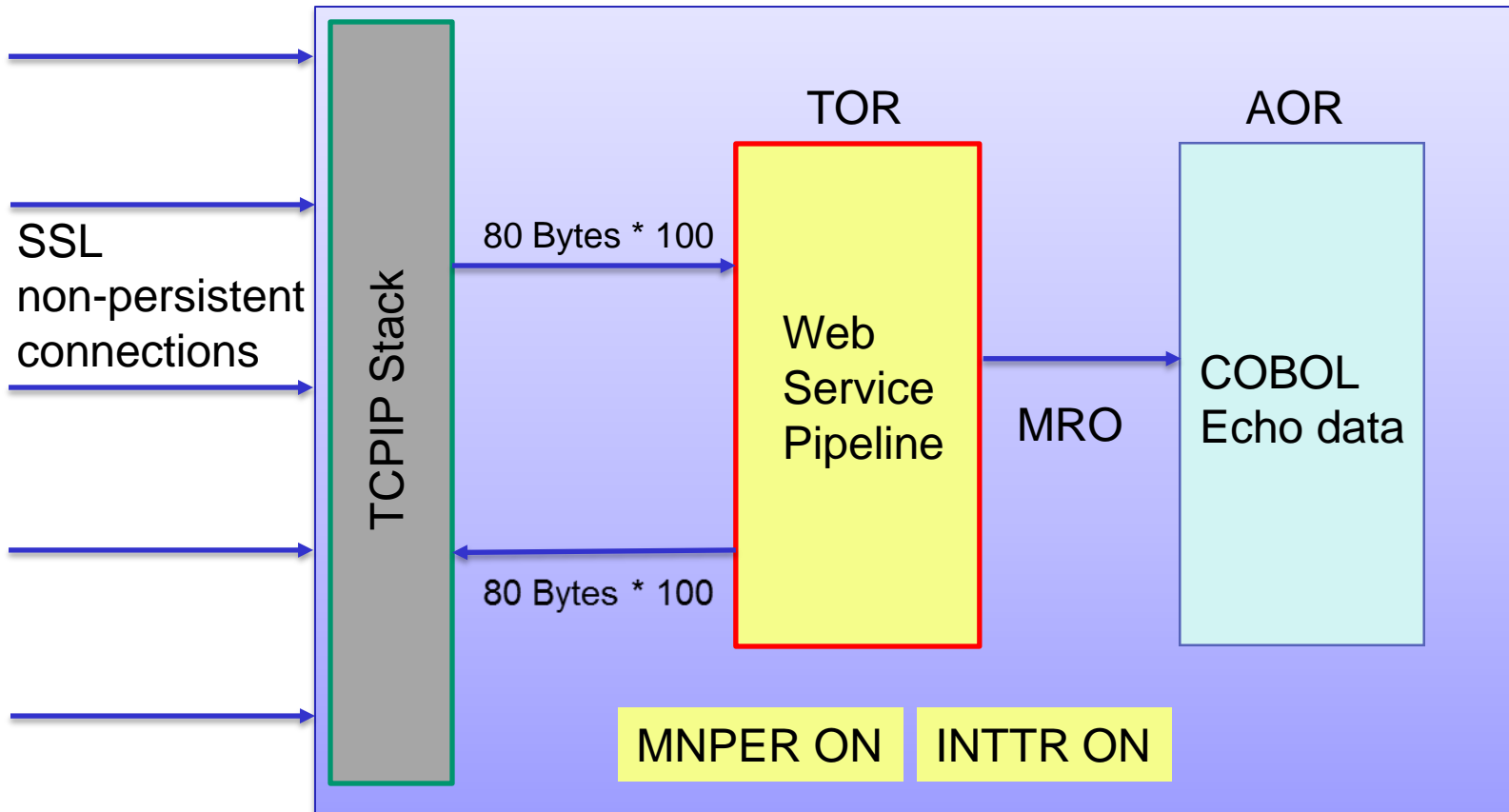
Caution!

All numbers in this section have been created using developmental code. Your experiences using the CICS TS V5.3 open beta will vary.

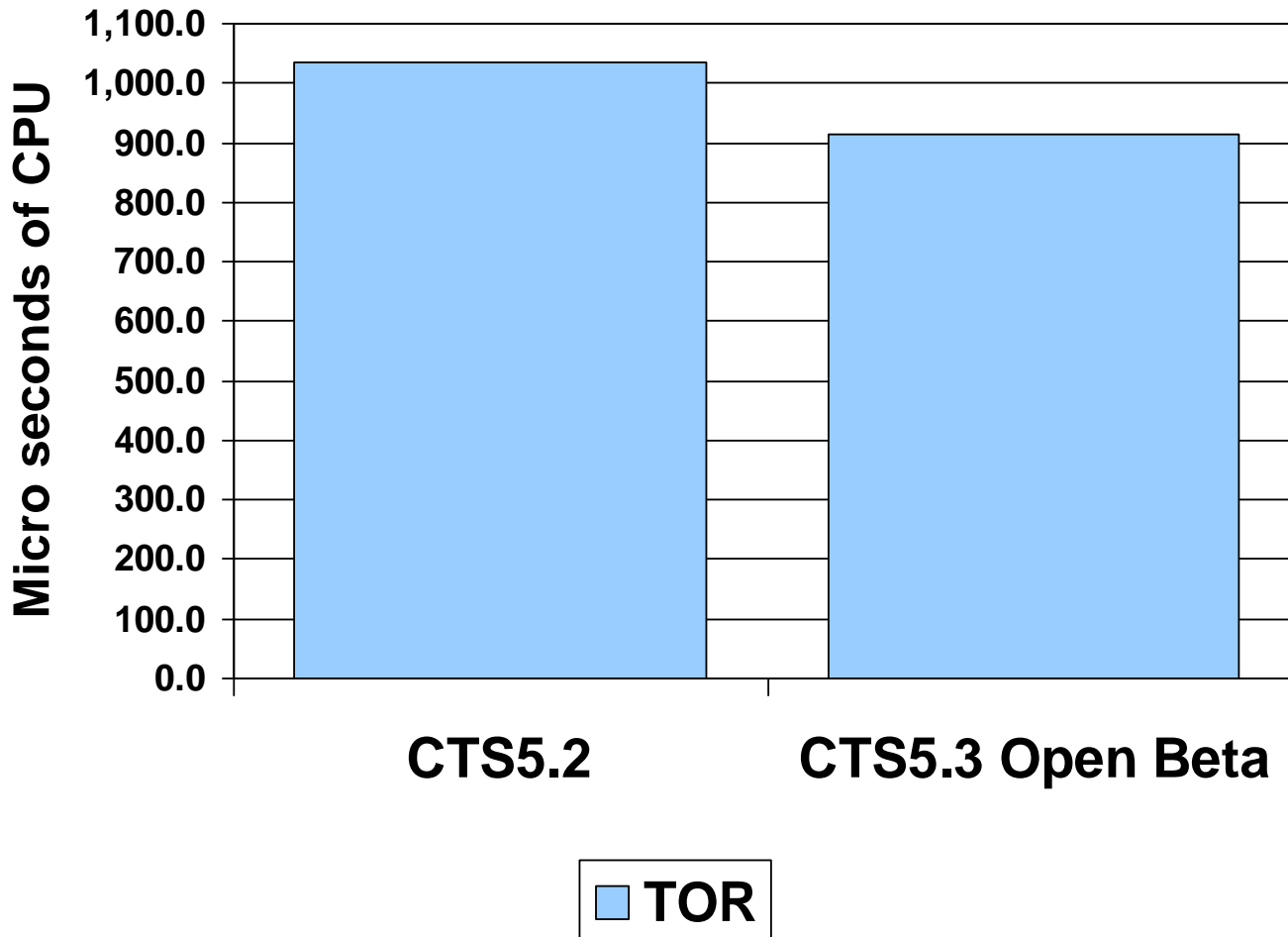
Some numbers from a CICS TS V5.3 open beta development level region

The Workload

Machine used was an EC12



CPU per Transaction comparison - 11.6% reduction in TOR



MNPER ON

INTTR ON

TCB and SRB CPU times – 5 minute interval

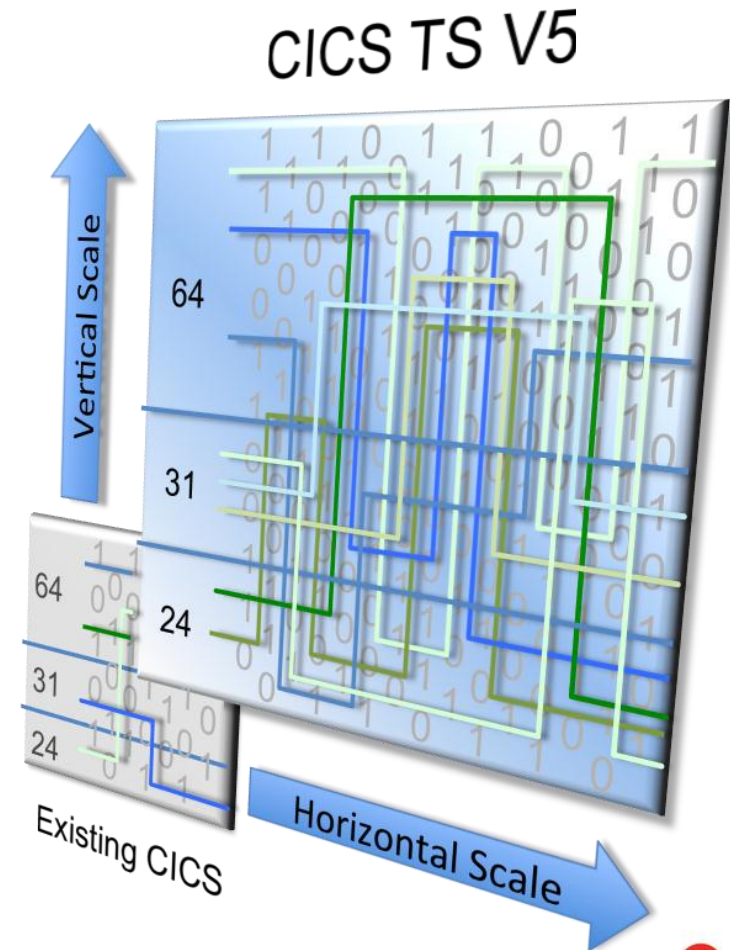
	CICS TS V5.2	CICS TS V5.3 open beta	
Transactions	600181	600243	
TCB	299.97	260.42	-13%
SRB	13.32	12.16	
QR TCB	58.99	47.55	-19%
SL TCB	21.28	20.25	
SO TCB	46.30	22.45	-51%
S8 TCB	118.85	120.32	+1%
L8 TCB	54.50	49.80	-8%

I/O now done on S8, used to be on SO, eliminates change modes

Performance Improvements in CICS TS V5.2 and V5.1 you should know about

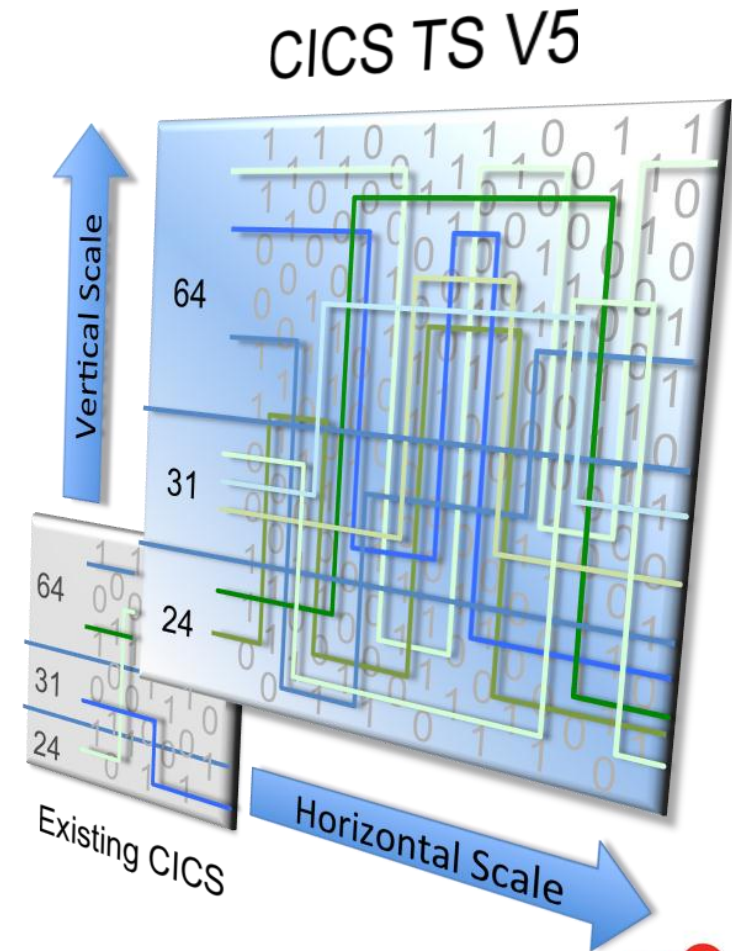
Throughput Enhancements – V5.2 and V5.1

- Threadsafe API and SPI
 - (V5.1) EXEC CICS SET TASK
 - (V5.1) INQUIRE and SET TRACEDEST / TRACEFLAG / TRACETYPE
 - (V5.2) INVOKE APPLICATION
 - (V5.2) VERIFY TOKEN
- Threadsafe TD (V5.1)
 - TD GLUEs must be threadsafe
- Max tasks increase (V5.1)
 - New default 500, min 10 and max 2000
 - NB: It is okay to hit max tasks!



Throughput Enhancements – V5.2 and V5.1

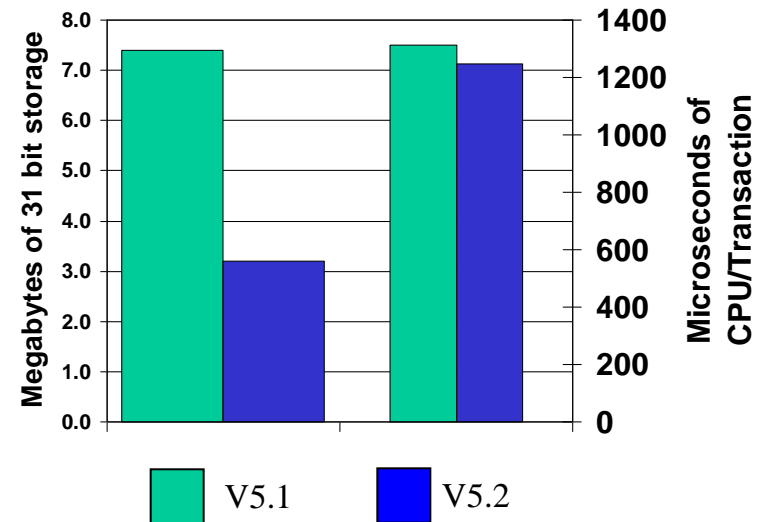
- Multi-cast event emission
 - Use an EP adapter set
 - Allows more efficient event handling
- Increase the potential of program load capacity (V5.1)
 - Reduced contention for the single CICS RO TCB
 - New statistics added



Virtual Storage Constraint Relief – V5.2 and V5.1

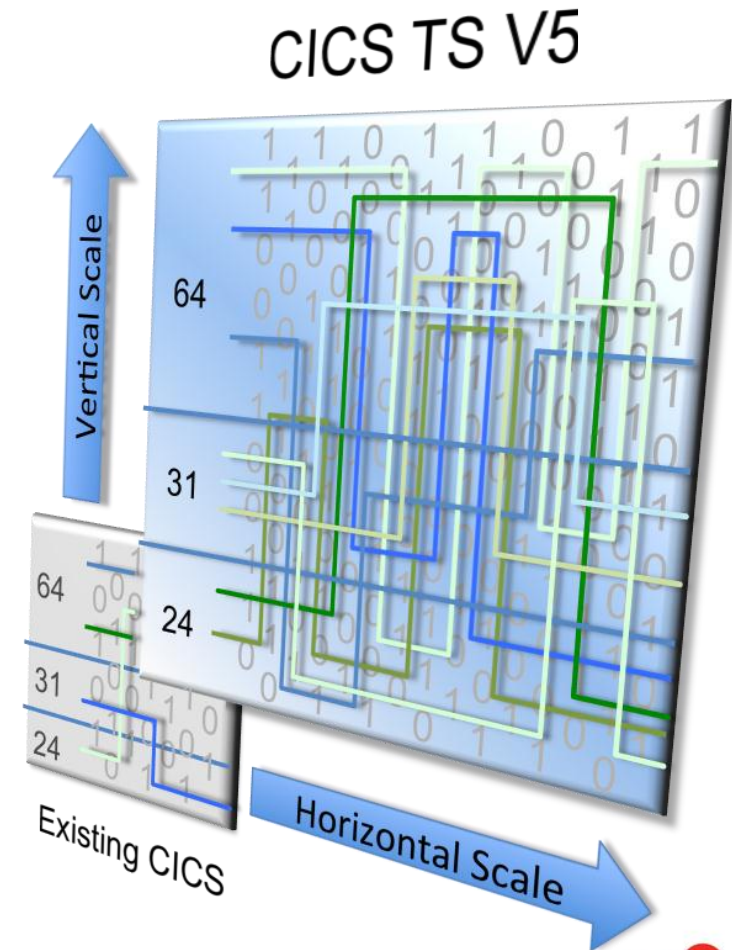
- Reduced 31bit memory usage for Web services (V5.2)
 - The SOAP node parsing is done entirely in 64 bit storage
- 64 bit application storage (via assembler) (V5.1)
 - Non-LE ONLY
 - EXEC CICS GETMAIN64 and FREEMAIN64
 - EXEC CICS PUT64 CONTAINER and GET64 CONTAINER

1 Megabyte Web service



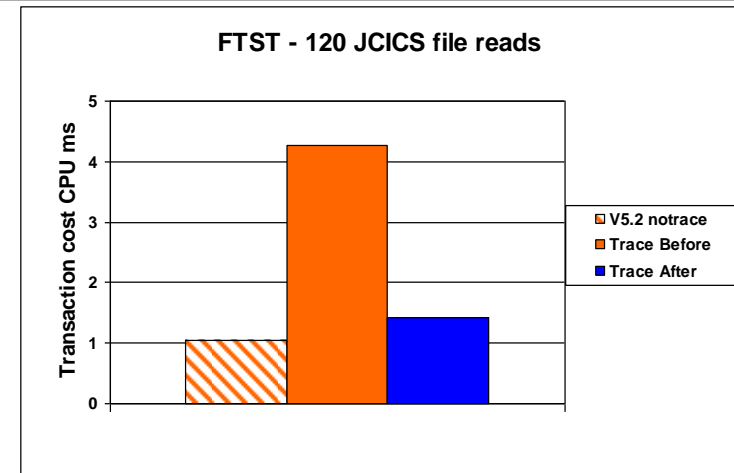
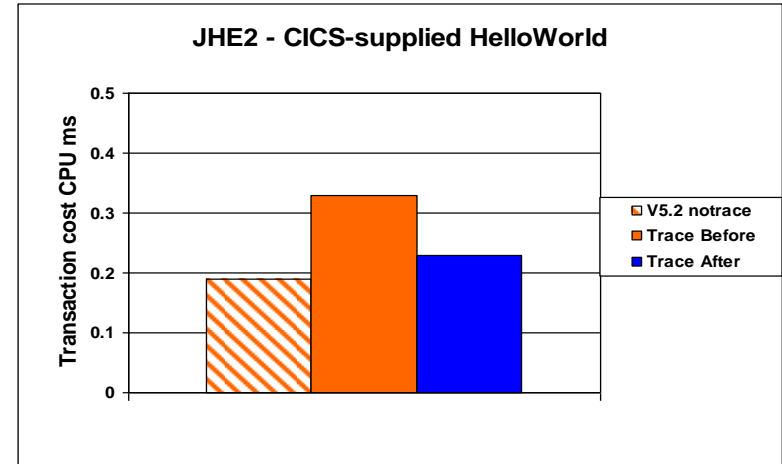
Virtual Storage Constraint Relief – V5.2 and V5.1

- Domains exploiting 64 bit
 - (V5.1) Storage Manager, Loader, Console Queue, new function
 - (V5.2) Pipeline, new function
- 24 bit storage use significantly reduced (V5.1)
 - Supplied transactions now TASKDATALOC(ANY)
 - Internal control blocks moved



CPU Usage – V5.2 and V5.1

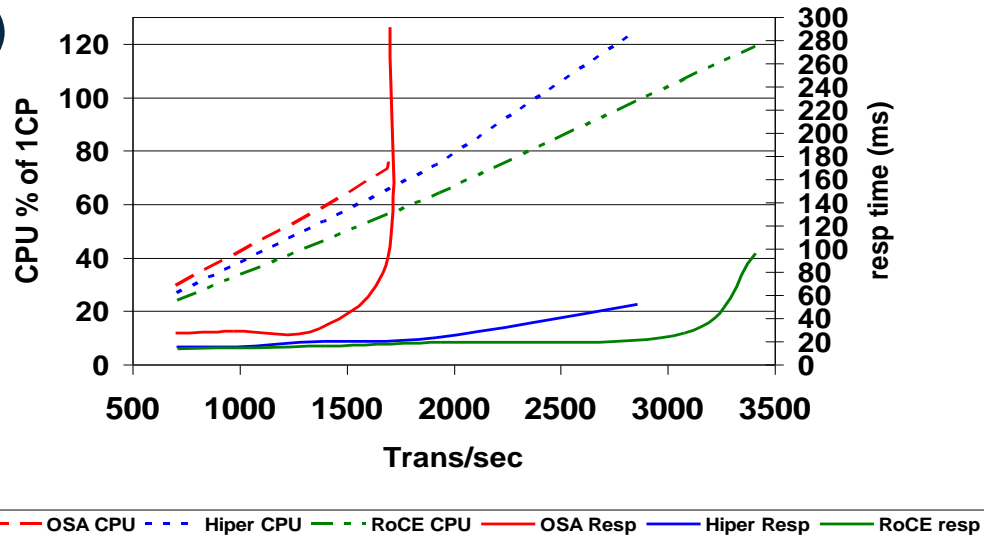
- Java Trace (V5.2)
 - Content of default trace changed
 - Better performance for JCICS
 - Measurements in tables from pre-GA code
- Java calls to DB2 reduced TCB switching (V5.1)
 - Requests stay on T8 TCBs
- Improved defaults for some system configuration parameters



CPU Usage – V5.2 and V5.1

- TCB Switches (V5.2)
 - Remove 2 TCB switches per web service request when invoking transactions defined with DYNAMIC(NO), by optimizing when the routing program is called
- Also apply to other releases (All)
 - RoCE (SMC-R) gives reduced CPU and better scaling
 - Use of Flash with CICS V5.2, z/OS V2.1 and zEC12 gives benefits when paging & when taking system dumps

SMC-R vs other connectivity options



Other items in CICS TS V5.2

- Task association data parameter list improvements
 - Better performance when looking at these fields
- Reinstate ability to control MAXxxxTCBS (with option for system to set value)
 - For very heavily loaded systems having very large numbers of open TCBS can have detrimental impact on performance
- CICS PA improved visualization and expertise
 - Visualisation in the PA plugin allows easy comparison between areas, e.g. storage use and transactions
- Better understanding of QR dispatch queue
 - Additional dispatcher and transaction manager metrics are collected to aid understanding of threadsafe application usage

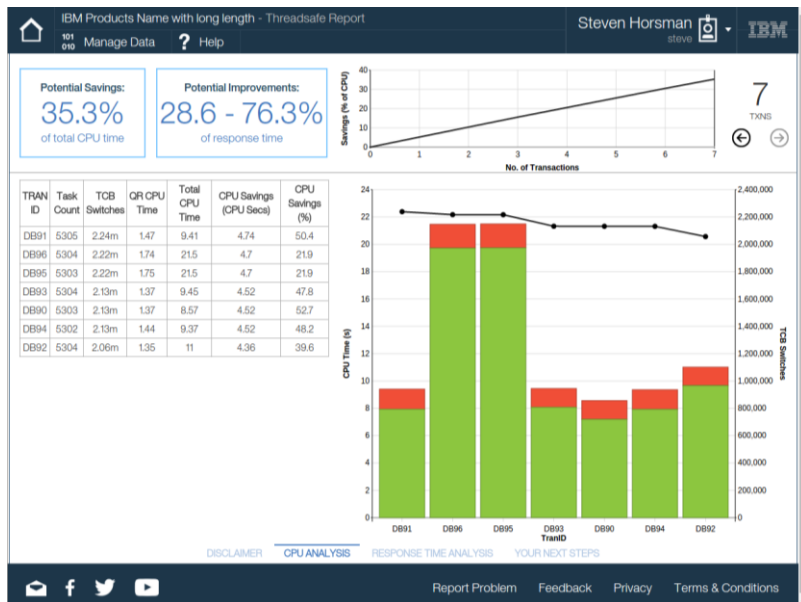
What can you do to improve performance?

What can you do to improve performance?

- Analyse your systems
- Make appropriate programs threadsafe
- Recompile with newer compiler versions
- Exploit zIIP offload
- Education – Red Paper series
- Upgrade machine
- Add accelerator cards
- Consolidate regions
- Change settings to be appropriate for workload
- Use policies to protect regions
- Get help from IBM

CICS Operational Insights open beta

- New Cloud based Service to identify opportunities to tune your CICS
- Understand your operational characteristics to target improvements
- Upload a snapshot of operational data and get a Threadsafe Assessment (more insights to follow)
- Threadsafe can help you get more efficient and save money



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CICS and z13

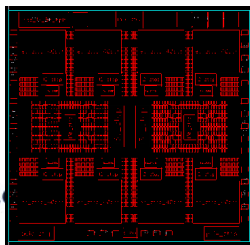
IBM z13: built for the needs of digital business and designed for the mobile generation



z13 Feature	Potential CICS benefit
SMT on zIIP exploited by Java	CICS OSGi Java, CICS Liberty JVM server, CICS Mobile, CICS TG
CPACF and SIMD exploited by Java 8	CICS TS Statement of Direction for Java 8 support
Cryptographic improvements: Crypto Express5S & CPACF	SSL processing: CICS TS and CICS TG
Large Memory	Storage-constrained workloads e.g. using channels and containers, shared data tables, Java heap
SIMD exploitation and other improvements in COBOL V5.2, PL/I V4.5, C/C++ V2.1.1	CICS applications recompiled for z13



- Transaction Processing
- Data Serving
- Mixed Workloads
- Operational Efficiency
- Trusted and Secure Computing
- Reliable, Available, Resilient
- Virtually Limitless



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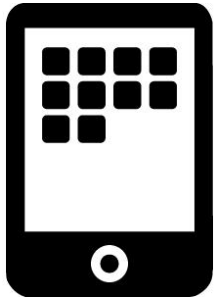
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Questions

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