



# The Inner Workings and Hidden Mechanisms of FICON

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March 13, 2014 Session Number 14375



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# **OF MAINFRAME DOMINANCE**

# Happy Birthday System z!

- Mainframes are in use in 90% of the *Fortune* 1000 and a large percentage of midmarket companies"
  - IBM and the Computer & Communications Industry Association
- ...more than 70 percent of all corporate data resides on mainframes. Legacy systems represent *trillions of dollars* in assets...
  - z/Journal
- About 25 % of IBM's annual revenue comes from the sale of mainframes and associated products like storage systems, software and services!
  - NY Times



### Now – Let's Look Inside FICON I/O Processing



- There have been many, significant improvements in how I/O is handled by the mainframe.
- Fibre CONnection, introduced in 2000, has undergone a vast number of improvements since then.
- It has been necessary to match the improvements of FICON on the mainframe with improvements of FICON flowing along a Fibre Channel storage network.
- This is an overview of some of those storage networking improvements.

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#### Complete your session evaluations online

### **Bottleneck Detection**

Identify Slow Draining Devices and Storage Network Congestion

- The bottleneck detection feature identifies ports where the offered load is greater than the achieved egress throughput
- Bottleneck detection enables a user to:
  - Prevent degradation of throughput in the fabric
  - Reduce the time it takes to troubleshoot network problems





#### **Bottleneck Detection**

#### Quickly Identify and Resolve FICON Performance Degradation

- SHARE Technology - Connections - Results
- Monitor for latency in the I/O fabric with Bottleneck Detection and receive notification when problems are detected
- Monitor resource contention, congestion, and other issues impacting user application performance and FICON performance
- Alert the user when thresholds are exceeded and display data on a Dashboard



# Significant Change In Data Encoding

8b/10b compared to 64b/66b





64b/66b: Two check bits are added after every 8 Bytes – 2% overhead
At the end of every 32, eight byte groups, we have collected 32 hi-order ck bits
This is a 32 bit check sum to enable Forward Error Correction to clean up links

- 1/2/4 and 8Gbps will always use 8b/10b data encoding
- 10Gbps and 16Gbps will always use 64b/66b data encoding

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# Multi-speed Encoders Along With A

#### Significant Change In Data Encoding

- To improve the efficiency of the FC protocols, 10G and 16G optics use only 64b/66b coding which is 98% efficient
  - Encoding is done by the ASIC and depends on which optic is being utilized in that port
  - 8b/10b coding is used for 1/2/4/8G FC and is 80% efficient
  - 16G signals cannot use the 8b/10b encoders
- To be backward compatible with 4/8G FC the 16G FC ASICs must support both 8b/10b and 64b/66b coder/decoders (codec) on each link
  - 1Gbps and 2Gbps are not supported at 16Gbps
- During speed negotiation, the transmitter and receiver switch back and forth between the speeds (and the corresponding codecs) until the fastest speed is reached for a given link





For 4/8 Gbps FC



#### Multi-speed Encoders – Example

This example assumes the connection of 2 switches



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#### **Forward Error Correction**

- ASIC-based functionality that is enabled by by default and allows the hardware to fix bit errors in a 10G or 16Gbps data stream
  - Works on Frames and on Primitives
  - The high-order bits collected from the 64b/66b data encoding help correct transmission bit errors
  - Corrects up to 11 bit errors per each 2,112 <u>bits</u> in a payload transmission
    - 11 bit corrections per 264 bytes of payload
- Requires hardware that is capable of performing this function for its E\_Ports
- Significantly enhances reliability of frame transmissions across an I/O network







### **Storage Network Super Highways**



• Even when ISL links are being used very well, **Head of Line Blocking** on an ISL link can create congestion and performance problems much like toll booths on a Super Highway can cause congestion and slow travel.







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#### Head of Line Blocking What Is the Problem?









# Virtual Channels (VCs)

Buffer Credit Pool segments established on each ISL link



- Virtual Channels is a unique feature available on every Brocade switch/Director.
- VC technology logically partitions the buffer credits on each ISL link into many different BC pools (virtual channels) and then prioritizes traffic to optimize performance and prevent head of line blocking.
- In addition, each VC has its own queues.
- Of course an ISL is still just one fibre link so only a single lane (a single frame) of I/O traffic is passing across it, in each direction, physically at a time:
  - A VC is really just a smaller allotment segment of the total buffer credit pool that becomes dedicated to servicing a specific VC number.
  - Segmenting the single physical buffer credit pool into multiple pools allows frames to avoid HoLB and keeps the ISL link optimized



# Virtual Channels help create a SAN Super Highway

Buffer Credit (BC) Pool segments provide multiple lanes of traffic on 1 link



The Port BC pool is logically segmented into groups of virtual BCs (virtual channels) and then ingress ports can be assigned to a virtual channel segment if an ISL is required to be used.

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# Testing ISL Links Before Deployment

Reduce the time it takes to successfully deploy a fabric

![](_page_15_Picture_2.jpeg)

- Diagnostic Port will check optics and cables integrity
  - D\_Port is a special port type, configured by the user to run diagnostics
  - Does not carry any FC control or data traffic
- Supported only on ISL and ICL ports

- Full support for 16G SFPs
- Partial support for 10G SFPs
- Provides the following capabilities:
  - Performs electrical loopback (16G)
  - Performs optical loopback (16G)
  - Measures link distance (10G, 16G)
  - Does link saturation testing (10G, 16G)

![](_page_15_Picture_14.jpeg)

### **I/O Flow Generator**

Better testing of ISL links before they are deployed

- Once D\_Port has been executed, users might want to consider using I/O Generator to create a saturation flow across the ISL link to test for a stable link
- Generates traffic at line rate across ISL links for testing purposes.
- For System z customers, the expected use for this feature is to validate inter-switch links (ISLs), especially through DWDM and over leased lines.
- Previously, the only feature to generate traffic to validate links was the Diagnostic Port (D\_Port) function but D\_Port was not designed to actually stress those links.
- Flow Generator is a complimentary tool to stress links at full line rate to make sure that they are up to the task of delivering I/O successfully throughout the fabric.

![](_page_16_Picture_7.jpeg)

![](_page_16_Picture_8.jpeg)

## I/O Flow Monitoring

#### Reduce the time it takes to resolve congestion/backpressure issues

- Flow Monitoring provides performance analysis based on a specific CHPID-to-Link address path.
  - Multiple Flow Monitors can be created.
  - All Flows to a given port can be created automatically so performance at a specific port can be analyzed by channel path.
- The ability to automatically configure a Flow Monitor for all paths to a given port can be a valuable troubleshooting tool to determine where traffic to a port originates.
- For example, an HCD mistake that results in two CHPIDs from different CECs contending for the same tape port is easily found by setting up a Flow Monitor on the tape port to automatically monitor all flows.
- Using switch management software, a single mouse click brings the user right to the port information with the RNID data that articulates the CEC S/N, CHPID, and CSS.

![](_page_17_Picture_9.jpeg)

![](_page_17_Picture_10.jpeg)

![](_page_17_Picture_11.jpeg)

## **Inter-Switch Links and Inter-Chassis Links**

![](_page_18_Picture_2.jpeg)

![](_page_18_Figure_3.jpeg)

"Hop of no concern"

![](_page_18_Figure_5.jpeg)

Do not create an official hop

But

Do connect domains together

![](_page_18_Figure_9.jpeg)

Creates a FICON hop

![](_page_18_Picture_11.jpeg)

Do create an official hop And Do connect domains

![](_page_18_Picture_13.jpeg)

together

# **Unique Fabric Scalability**

**FICON Triangle Configuration** 

- Chassis expansion scalability through an ability to provision up to 768 ports of 16 Gbps bandwidth, within a local fabric through the use of ICLs:
  - Three 256 port chassis can be connected together providing 768 ports with from 128 – 2048 Gbps of ICL bandwidth (three 256p chassis = 768 ports)
- Chassis expansion scalability through an ability to provision up to 1,536 ports of 16 Gbps bandwidth, in a cascaded FICON fabric through the use of ICLs and ISLs:
  - Three 256 port chassis can be interconnected with ICLs and then ISL connected to another pod of three 256 port chassis (6 \* 256 = 1,536p)
  - The example to the right shows a 1,024 FICON port fabric

![](_page_19_Picture_7.jpeg)

Using MPO cables, can be up to 100 m so these chassis can be in widely dispersed cabinets.

![](_page_19_Picture_9.jpeg)

![](_page_19_Picture_10.jpeg)

![](_page_19_Picture_11.jpeg)

# **Port De/Re-commissioning for Mainframes**

Improving FICON ISL Management

- Coordinate event with external applications
  - Switch operating system moves routes off of the target ISL before that ISL is disabled
- Mechanism to remove an ISL non-disruptively
  - Block/Disable an ISL port after moving the traffic flow to other routes so that removing it will be non-disruptive
  - Requires Lossless DLS to be enabled on both end
- Will become attached to automated processes like Port Fencing in the future

![](_page_20_Figure_8.jpeg)

# Port De/Re-commissioning for Mainframes

These LPARs use

the port and will free it up.

Improving FICON N\_Port Management

- Mechanism to remove a CHPID or Storage port non-disruptively
  - Block/Disable a device port after allowing each LPAR to quiescing the path/device so that removing it will be non-disruptive
- Coordinate event with external applications
  - Application or system manager
    - Moves workload off of a target port before that port is disabled
- Will become attached to automated processes like Port Fencing in the future
- Each LPAR would run a CIMOM agent

![](_page_21_Figure_9.jpeg)

# **Improved ISL Security and Efficiency**

ISL data traffic Compression and/or Encryption In-Flight

#### Secure Transfers

- Encrypts data on 4/8/16 Gbps ISLs hosted on Gen 5, 16 Gbps blades
  - Switch-to-switch encryption, not at-rest encryption
  - For Fibre Channel long-distance links
- Uses AES-GCM algorithm for both authentication and encryption
- Uses 256-bit encryption key

#### Maximum Network Efficiency

- Disk or tape traffic gets compressed on ISL and gets uncompressed at the receiving switch
- Provides up to 2:1 compression and uses Brocade LZO algorithm
- Provides up to 128 Gbps of compressed bandwidth per blade
- Requires no license and can be used in conjunction with in-flight encryption

![](_page_22_Figure_13.jpeg)

**Qualified Configurations:** 

- Use neither capability
- Use both capabilities concurrently
- Use only compression
- Use only encryption

![](_page_22_Picture_19.jpeg)

# **Storage Networking Dashboards**

Proactive management of Storage Networking

![](_page_23_Picture_2.jpeg)

- Customers are demanding Ease-of-Use as they deploy new technologies into their data centers.
- A variety of FC fabric event monitoring and displayable metrics can now provide customers with many advantages:
  - Quick check of the SAN's status
  - Quick check of the IP status
  - Early and better fault detection
  - Proactively detect deteriorating SFPs
  - Avoid down time and replace in a scheduled maintenance window.
  - Apply predefined parameters across all fabrics within an enterprise with a single mouse click

![](_page_23_Figure_11.jpeg)

Health and Performance Dashboards provide a color coded "quick look" at many storage network metrics

![](_page_23_Picture_13.jpeg)

![](_page_24_Figure_0.jpeg)

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# **SAN Health Check-up Tool**

- Five Minute Process
  - One Minute to install
  - Four Minutes to audit fabrics
- Encrypted upload to secure servers located at Brocade HQ
- Detail report returned within a couple of hours via email

#### **Report Content:**

#### • Visio Diagram

- Detailed diagrams of all fabrics and end devices
- Excel File
  - Reports on traffic spikes and over-subscription ratios
  - Verifies Zoning and switch setting consistency
  - Flags any potential SFP and cable errors
  - Provides detailed licensing and capacity planning information

# Brocade SAN Health

![](_page_25_Picture_15.jpeg)

#### SAN Health Diagnostics Capture

This program captures diagnostic and performance data from SAN switches to aid in the generation of a SAN Health report containing inventory, topology and performance information.

Only commands that retrieve information from the switch are used.

No switch configuration settings are changed.

#### Version 3.2.2

Supported products and firmware levels <u>SAN Health Support Matrix</u>
Contact the SAN Health admin team on <u>SHAdmin@brocade.com</u>
For more information visit the SAN Health home page <u>www.brocade.com/sanhealth</u>
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# • Brocade • Cisco

McDATA

![](_page_25_Figure_24.jpeg)

### Let Me End This Session By Proudly Presenting The Industry's ONLY FICON Certification

![](_page_26_Picture_1.jpeg)

![](_page_26_Picture_2.jpeg)

![](_page_27_Picture_0.jpeg)

![](_page_27_Picture_1.jpeg)

# Fundamentals of Brocade Mainframe Networking Seminar taught over 2 full days or 3 relaxed days

- We have been holding classes since mid-2008
- This is good for mainframers who desire to become professionally certified as FICON subject matter experts
- This seminar teaches advanced concepts and is not well suited for professionals with less than 1 year of experience

Total number of attendees at these seminars since 2008: ~550 Total number of Brocade FICON Certifications awarded: 250+

(There is so little FICON training available that some people attend just to get current on FICON and not for certification)

![](_page_27_Picture_8.jpeg)

# **Brocade Certified Architect for FICON (BCAF)**

Brocade can provide this 2 or 3 day training for YOU!

#### Fundamentals of Brocade Mainframe Networking Seminar

(the preparatory class for the BCAF certification)

#### Day 1 (8:30am to 5pm):

- Course Introduction
- Mainframe and FICON Overview
- Brocade Switching Technologies
- Design and Migration
- FICON cascading and data transmission technologies

#### Day 2 (8:30am to 5pm):

- Managing cascaded FICON environments
- FCIP, Data Replication and Business Continuity Networks
- FICON Implementation
- Managing and Maintaining a FICON Environment
  - Class minimum size is 8 people
  - No charge if taught in the USA
  - \$2,500 fee for travel and living if taught overseas
  - Contact David Lytle if interested: dlytle@brocade.com

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Can be done In 3 easier days!

![](_page_28_Picture_21.jpeg)

![](_page_28_Picture_22.jpeg)

![](_page_29_Picture_0.jpeg)

# **Some Mainframe Social Media Sites**

![](_page_29_Picture_2.jpeg)

![](_page_29_Picture_3.jpeg)

Please check out the Brocade Mainframe Solutions blog today: http://community.brocade.com/t5/Mainframe-Solutions/bg-p/MainframeSolutions

Find useful information on the Brocade Fibre Channel Communities Page today: <u>http://community.brocade.com/t5/Fibre-Channel-SAN/bd-p/fibre</u>

#### Almost 300,000 hits!

Join us in discussing issues at the Mainframe Discussion Community today: <a href="http://community.brocade.com/t5/Mainframe-FICON/bd-p/mainframesolutionsforum">http://community.brocade.com/t5/Mainframe-FICON/bd-p/mainframesolutionsforum</a>

![](_page_29_Picture_8.jpeg)

![](_page_30_Picture_0.jpeg)

![](_page_30_Picture_1.jpeg)

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