

## What I hope to cover.....

- What are dispatchable units of work on z/OS
- How WLM manages dispatchable units of work
- The role of HiperDispatch
- Dispatching work to zIIP and zAAP engines
- RMF Work Unit Analysis Report

## z/OS Dispatchable Units

- There are different types of Dispatchable Units (DU's) in z/OS
  - Preemptible Task (TCB)
  - Non Preemptible Service Request (SRB)
  - Preemptible Enclave Service Request (enclave SRB)
    - Independent a new transaction
    - Dependent extend existing address space
    - Work-dependent extend existing independent enclave





transaction program request







WLM Dispatch	ing Pri	ority Usage	
Γ	255	SYSTEM	
	254	SYSSTC	
Γ	253	Small Consumer	
	252	Priorities for dynamic policy adjustment	
	208		
	207		
	202	Not used	
	201	Discretionary work Mean Time to wit algorithm	
	192		



BLWLTRPCT	Percentage of the CPU capacity of the LPAR to
	be used for promotion
	• Specified in units of 0.1% • Default is $5 (-0.5\%)$
	• Default is $5 (=0.5\%)$ • Maximum is $200 (=20\%)$
	• Would only be spent when enough units of
	work exist which need promotion
BLWLINTHD	Specifies threshold time interval for which a
	blocked address space of enclave must wait
	Minimum is 5 seconds. Maximum is 65535
	seconds
	•Default is 20 seconds





SERV	ICE TIME	APP	L &	PRO	MOTED	STC	RAGE
.PU	805.69/	LP	92.24	BLK	1.489	AVG	1195.43
DCT	13.850	AAPUP	0.00	ENU	0.046		182122.4
	9.990	IIPUP	0.00	LCK	0.000	SHAKED	230.39
III ІСТ	0.070	AAD	0 00	CHD	0.000	DACE 1	
	0.000		0.00	JUF	0.000	CTNCLC	N ANIES-
	0.000	111	0.00			DIOCK	0.0
	0.000					SUNDER	0.0
						HSP	0.0
/ED-							





## **HiperDispatch Introduction**

- Design Objective
  - Keep work as much as possible local to a physical processor to optimize the usage of the processor caches
  - As a result systems with high number of physical processors provide a much better scalability
- Function: HiperDispatch

   Interaction between z/OS and the PR/SM Hypervisor to optimize work unit and logical processor placement to physical processors
   Consists of 2 parts
  - Consists of 2 parts
    - In z/OS (sometimes referred as Dispatcher Affinity)
    - In PR/SM (sometimes referred as Vertical CPU Management)

	IKM,
HiperDispatch Mode	
PR/SM	
<ul> <li>Supplies topology information/updates to z/OS</li> </ul>	
<ul> <li>Ties high priority logicals to physicals (gives 100% share)</li> </ul>	
<ul> <li>Distributes remaining share to medium priority logicals</li> </ul>	
<ul> <li>Distributes any additional service to unparked <i>low priority</i> logicals</li> </ul>	
z/OS	
<ul> <li>Ties tasks to small subsets of logical processors</li> </ul>	
<ul> <li>Dispatches work to high priority subset of logicals</li> </ul>	
<ul> <li>Parks <i>low priority</i> processors that are not need or will not get service</li> </ul>	

Parked

#### HiperDispatch: z/OS Part LPAR z/OS obtains the logical to z/OS physical processor mapping in Hiperdispatch mode Node Node - Whether a logical processor Parked has high, medium or low share - On which book the logical processor is located • z/OS creates dispatch nodes PR/SM - The idea is to have 4 high Logical to Physical share CPs in one node Mapping - Each node has TCBs and Hardware SRBs assigned to the node •6• - 語・ Books •日• •日• - Optimizes the execution of -日work units on z/OS



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# RMF CPU Activity Report

		s/03 V1R11		SYSTEM I	D 22		START	09/11/200	9-02.30.00	INTERVAL 000.30.00
				RPT VERS	ION VIRII	RMF	END	09/11/200	9-03.00.00	CYCLE 0.100 SECOND
CPU	2097	MODEL 737	H/W MODEL	E56 SEQUE	ENCE CODE	000000000	00699F	F HIPE	RDISPATCH=Y	ES
C	PU		TIME	ŧ		LOG PF	200	I/0 I	INTERRUPTS	
NUM	TYPE	ONLINE	LPAR BUSY	MVS BUSY	PARKED	SHARE	\$	RATE	VIA TPI	
0	CP	100.00	96.60	96.74	0.00	100.0	HIGH	1593	2.64	
1	CP	100.00	97.51	97.69	0.00	100.0	HIGH	1607	2.73	
2	CP	100.00	96.02	96.23	0.00	96.0	MED	5.12	29.30	
3	CP	100.00	39.26	80.81	51.23	0.0	LOW	0.00	0.00	
4	CP	100.00	48.71	79.90	38.77	0.0	LOW	0.00	0.00	
5	CP	100.00	41.06	79.34	48.01	0.0	LOW	0.00	0.00	
6	CP	100.00	12.42	78.35	84.11	0.0	LOW	0.00	0.00	
7	CP	100.00	0.00		100.00	0.0	LOW	0.00	0.00	
8	CP	100.00	0.00		100.00	0.0	LOW	0.00	0.00	
9	CP	100.00	33.05	80.34	58.68	0.0	LOW	199.6	1.01	
TOTA	L/AVERA	AGE	46.46	89.73		296.0		3405	2.62	
A	AAP	100.00	57.35	88.68	0.00	32.0	MED			
в	AAP	100.00	46.71	92.85	17.56	0.0	LOW			
С	AAP	100.00	45.27	90.82	17.79	0.0	LOW			
D	AAP	100.00	53.81	85.00	0.00	0.0	LOW			
TOTA	L/AVERA	AGE	50.78	89.09		32.0				
E	IIP	100.00	0.26	0.26	0.00	16.2	MED			
F	IIP	100.00	0.01	0.01	0.00	0.0	LOW			
TOTA	L/AVERA	AGE	0.13	0.13		16.2				

	IDM						
HiperDispatch and LPAR							
1 PARTITION DATĂ REPORT z/OS VIR10 SYSTEM ID LPARI DATE 04/29/2011 INTERVAL 14.59.998 CONVERTED TO z/OS VIR12 RMF TIME 19.28.00 CYCLE 1.000 SECONDS	page 3						
MVS PARTITION NAME         LPAR1         NUMBER OF PHYSICAL PROCESSORS         55         GROUP NAME           IMAGE CAPACITY         3165         CP         53         LIMIT           NUMBER OF CONFIGURED PARTITIONS         4         11P         2         AVAILABLE           NGL CONFLORMENT         NO         DISPATCH         NO         DISPATCH	N/A N/A N/A						
	TENTAGES TESSORS TIVE TOTAL .96 18.40 8.72 24.06 0.00 0.00 1.44						
TOTAL         05.18.52.927         05.35.48.155         2.21         41           Total LPAR weight = 1000         LPAR1 494/1000 = .494 * 53 CPs = 26.18 CPs         LPAR2 446/1000 = .446 * 53 CPs = 23.64 CPs         LPAR2 446/1000 = .446 * 53 CPs = 23.64 CPs	68 43.90						
LPAR1 = 25 VH and 2 VM at 59% share (27 logicals unparked) LPAR2 = 23 VH and 1 VM at 64% share (24 logicals unparked) 51 logicals unparked	]						
Need to deactivate unused LPARs to reallocate their weight to VH and VM logicals							
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W	Warning Track Statistics										
*	<ul> <li>RMF keeps track of the number of times PR/SM issued a warning-track interruption to a logical processor and z/OS was able/unable to return the logical processor within the grace period.</li> <li>RMF measures the amount of time in microseconds that a processor was yielded to PR/SM due to Warning-track processing.</li> </ul>										
	SMF reco	rd typ	e 70 su	btype 1	1 (CPU Activ	vity) – CPU data section					
	Offset	Name	•	Length	h Format	Description	0				
	80 x50	SMF70WTS 4			Binary	The number of times PR/SM issued a warning-track interruption to a logical processor and z/OS was able to return the logical processor within the grace period.	A31803				
	84 x54	SMF	SMF70WTU 4		Binary The number of times PR/SM issued a warning-track interruption to a logical processor and z/OS was unable to return the logical processor within the grace period.						
	88 x58 SMF70WTI 4 Binary Amount of time in microseconds that a logical processor was yielded to PR/SM due to Warning Track processing.										
	RMF Po	stproc	cessor C	vervie	w Condition	s					
	Name Qualifier De			r	Description						
	WTRKCP cpu-id (WTRKAAP) (WTRKIIP)				The percentage processor and	The percentage of times PR/SM issued a warning-track interruption to a processor and z/OS was able to return it to PR/SM within the grace period.					
	WTRKTC (WTRKTA (WTRKTI	P (AP) (P)	cpu-id		Time in micro to Warning Tr	seconds that a purpose processor was yielded to PR/SM due ack processing.					





Current IB	M Exploitation	n of zAAPs and zIIPs
Specialty CP	Eligible	Major Users
zAAP	Any Java Execution	Websphere CICS Native apps XMLSS
ZIIP	Enclave SRBs	DRDA over TCPIP DB2 Parallel Query DB2 Utilities Load, Reorg, Rebuild DB2 V9 z/OS remote native SQL procedures TCPIP - IPSEC XMLSS zIIP Assisted HiperSockets Multiple Write Virtual Tape Facility Mainframe (VTFM) Software z/OS Global Mirror (XRC), System Data Mover (SDM) z/OS CIM Server



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Specia	lty CP	work	runr	ning i	n a	WL	M Serv	vice	e Clas	S	
REPORT BY: F	OLICY=WLMP	ol wof	KLOAD=BF	T_WKL	SERVI	CE CLA	SS=BATSPEC	RE	SOURCE GR	OUP=BAT	MAXRG
TRANSACTIONS	TRANS-T	IME HHH.MM	.ss.ttt	DASD	I/0	SE	RVICE	SERVI	CE TIMES	APP	L %
AVG 0.9	8 ACTUAL		6.520	SSCHRT	11.5	IOC	8326	CPU	24.7	CP	0.97
MPL 0.9	8 EXECUTI	ON	6.128	RESP	7.0	CPU	662386	SRB	0.0	AAPCP	0.01
ENDED 1	0 QUEUED		391	CONN	6.9	MSO	0	RCT	0.0	IIPCP	0.00
END/S 0.1	7 R/S AFF	IN	0	DISC	0.0	SRB	965	IIT	0.0		
#SWAPS	0 INELIGI	BLE	0	Q+PEND	0.1	TOT	671677	HST	0.0	AAP	40.27
EXCTD	0 CONVERS	ION	0	IOSQ	0.0	/SEC	11195	AAP	24.2	IIP	0.00
AVG ENC 0.0	0 STD DEV		0					IIP	0.0		
GOAL: EXECU RES	TION VELOC	ITY 35.0% EX PERF	VELC	CITY MIG	RATION SING%	: I/ 	O MGMT 99	.2% <u>EXE</u>	INIT MG	MT 92.2 <u>Lays %</u>	% 
SYSTEM		VEL% INDX	ADRSP	CPU AA	P IIP	I/0	TOT CPU				
SYSD RMF report is at	N/A 1 minute interval	99.2 0.4	4 1.0	0.8 45.	9 0.0	3.9	0.4 0.4				



zAAPs on zIIPs	
<ul> <li>When a <u>processor</u> has no zAAPs installed but the LPAR has zIIP(s), treat zAAP-eligible work instead as zIIP-eligible work</li> <li>Exception: Under z/VM can define a guest without zAAPs, on a processor with zAAPs, allow testing</li> </ul>	to
<ul> <li>IEASYSxx system parm indicates whether zAAP on zIIP is enabled</li> <li>ZZ=<u>NO</u>   YES for z/OS 1.9 and z/OS 1.10</li> <li>ZAAPZIIP=NO   <u>YES</u> for z/OS 1.11 (also supports alias of ZZ)</li> <li>The enablement state of zAAP on zIIP cannot be changed after IPL</li> </ul>	
<ul> <li>Timing fields within SMF will show offload time under zIIP if a zIIP is installed</li> <li>No method to determine what portion of zIIP time originated due to a zAAP request</li> <li>Accounting and Capacity planning may need updating to use zIIP field</li> </ul>	
New operator command in z/OS R12 to display ZIIPZAAP eligibility	
New planning White Paper WP101642	



	IBM
DB2 Parallelism, WLM, and zIIPs	
-DP2 Derelleliem and -IIDa	
Controlled by a CPU threshold. Once the threshold is met all child are zIIP eligible	tasks
Parents are not zIIP eligible	
Parent and child CPU time contribute to the CPU Threshold	
Can see any kind of work, CICS, IMS, TSO, batch using zIIP reso	urces
DB2 will use new Work-Dependent Enclaves for Child tasks	
APAR OA26104 for releases 1.8 and beyond	
Without new Work Dependent Enclave support parallel enclaves r be classified using subsystem DB2	nust
-Unclassified work would wind up in SYSOTHER	













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