

zEnterprise Hybrid Smarter-Planet Application Architecture and Smarter-Server Design

Monte Bauman
IBM Columbus
mbauman@us.ibm.com

Monday, August 08, 2011

9557

Topics

- Requirements
- Design
 - zEnterprise
 - z10 vs. z196
 - zManager
- Use Cases
- Conclusion
- Next Steps

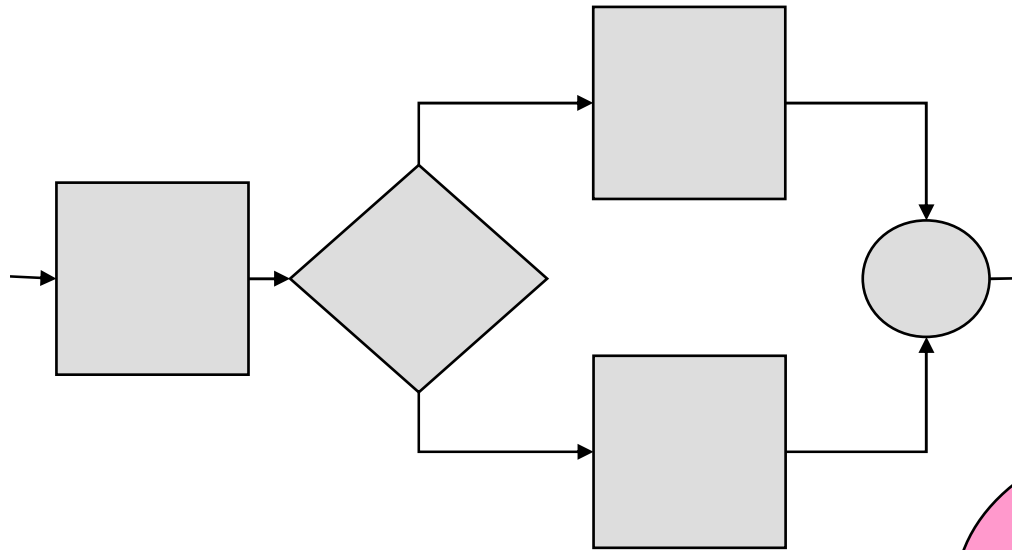
Requirements



Topics

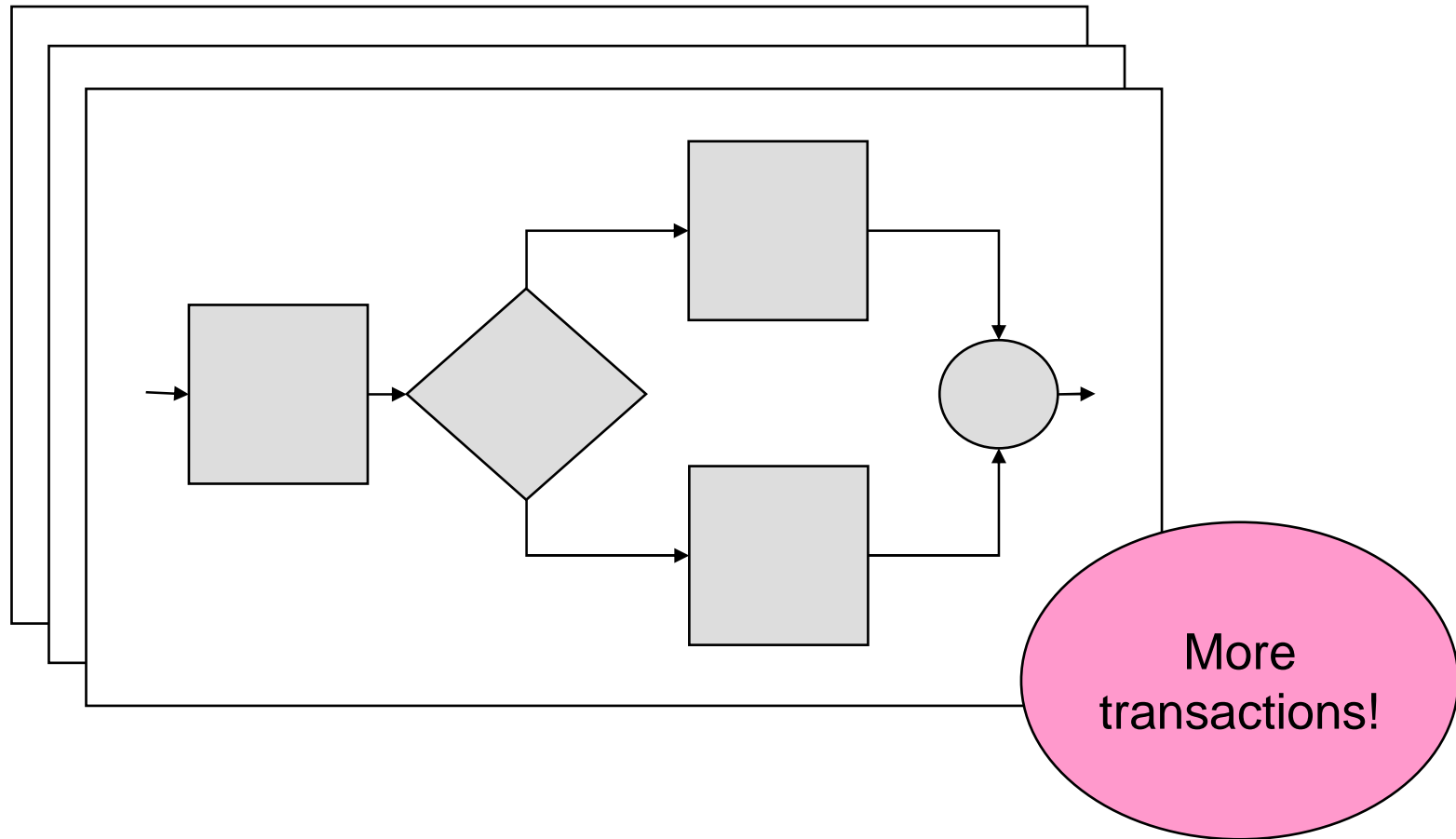


It's About the Business... (and the Business Process that we automate)



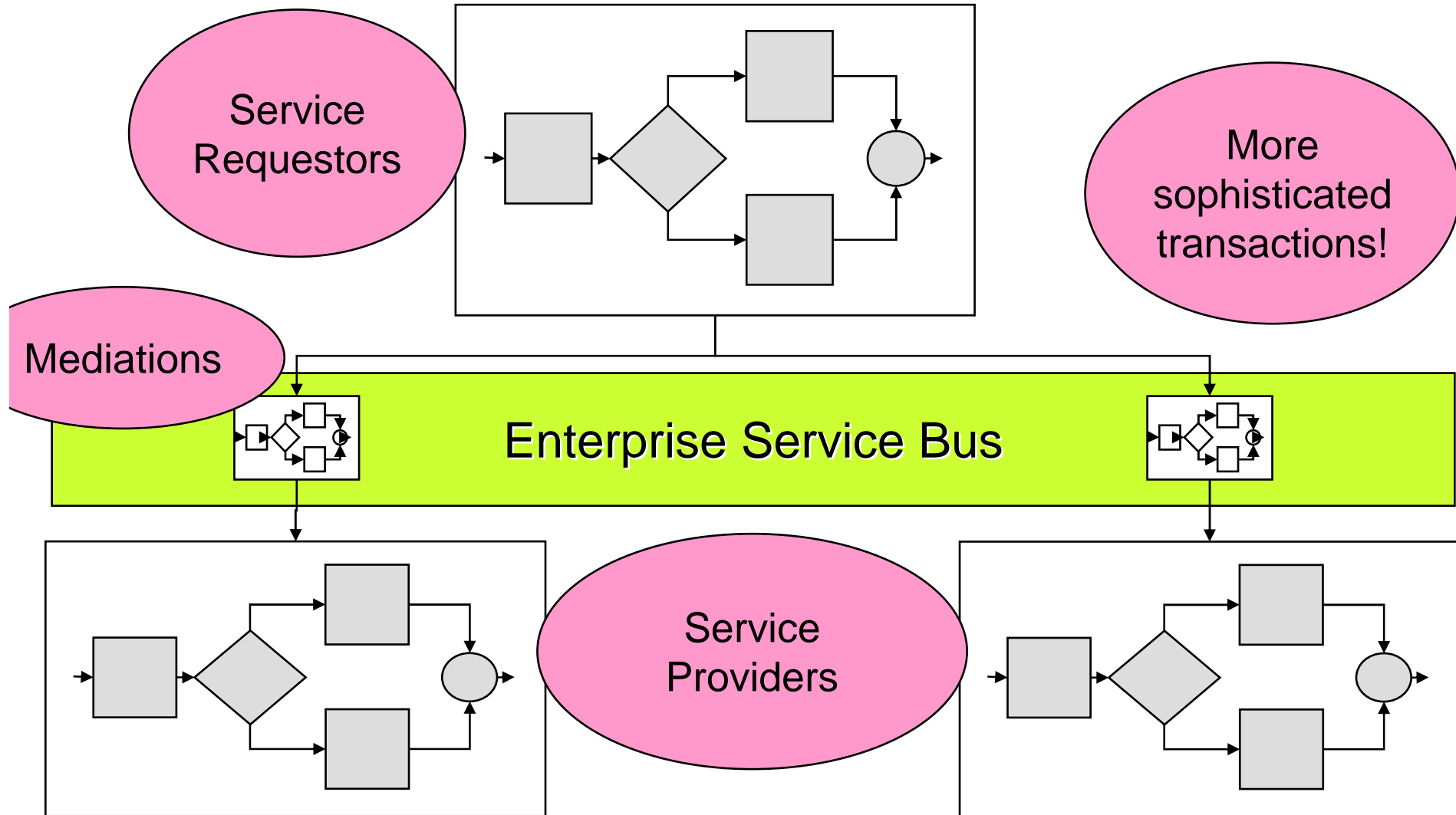
It is ALL about the transaction!

It's About the Business Process... (indeed ... lots of business processes)

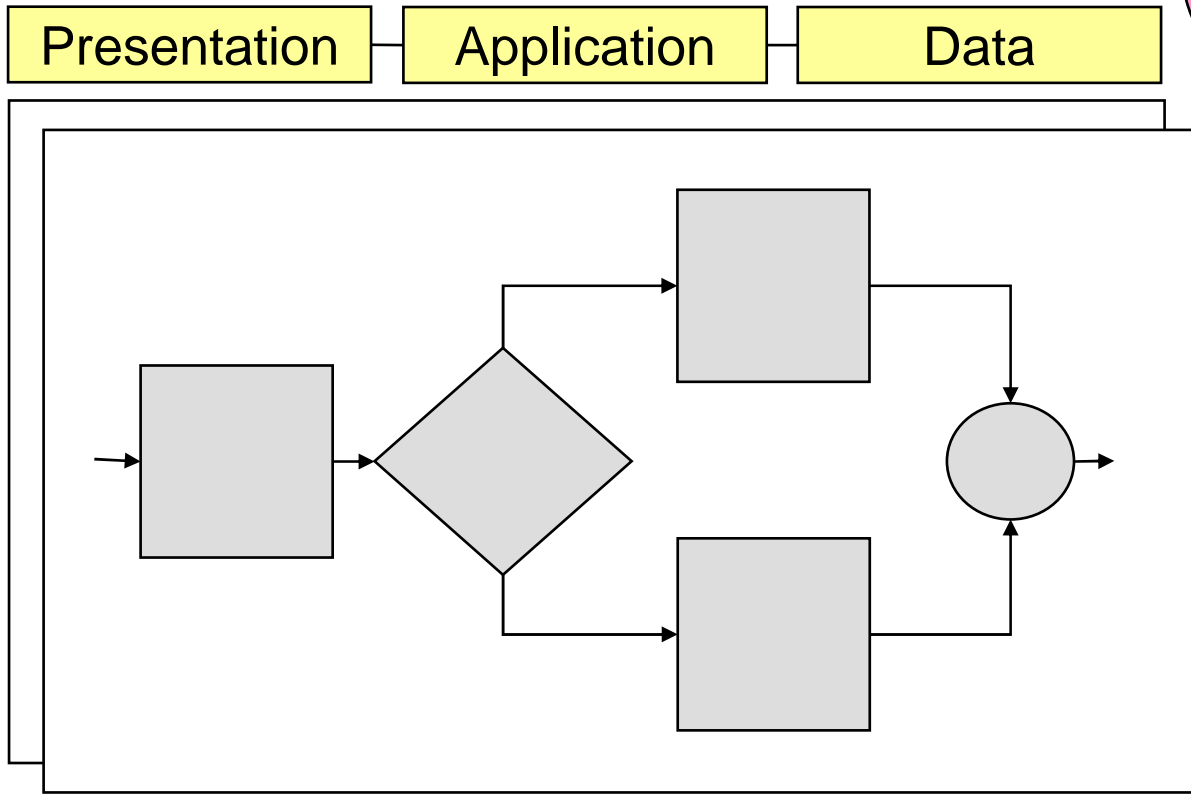
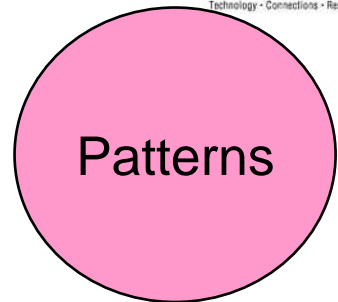


It's About the Business Process

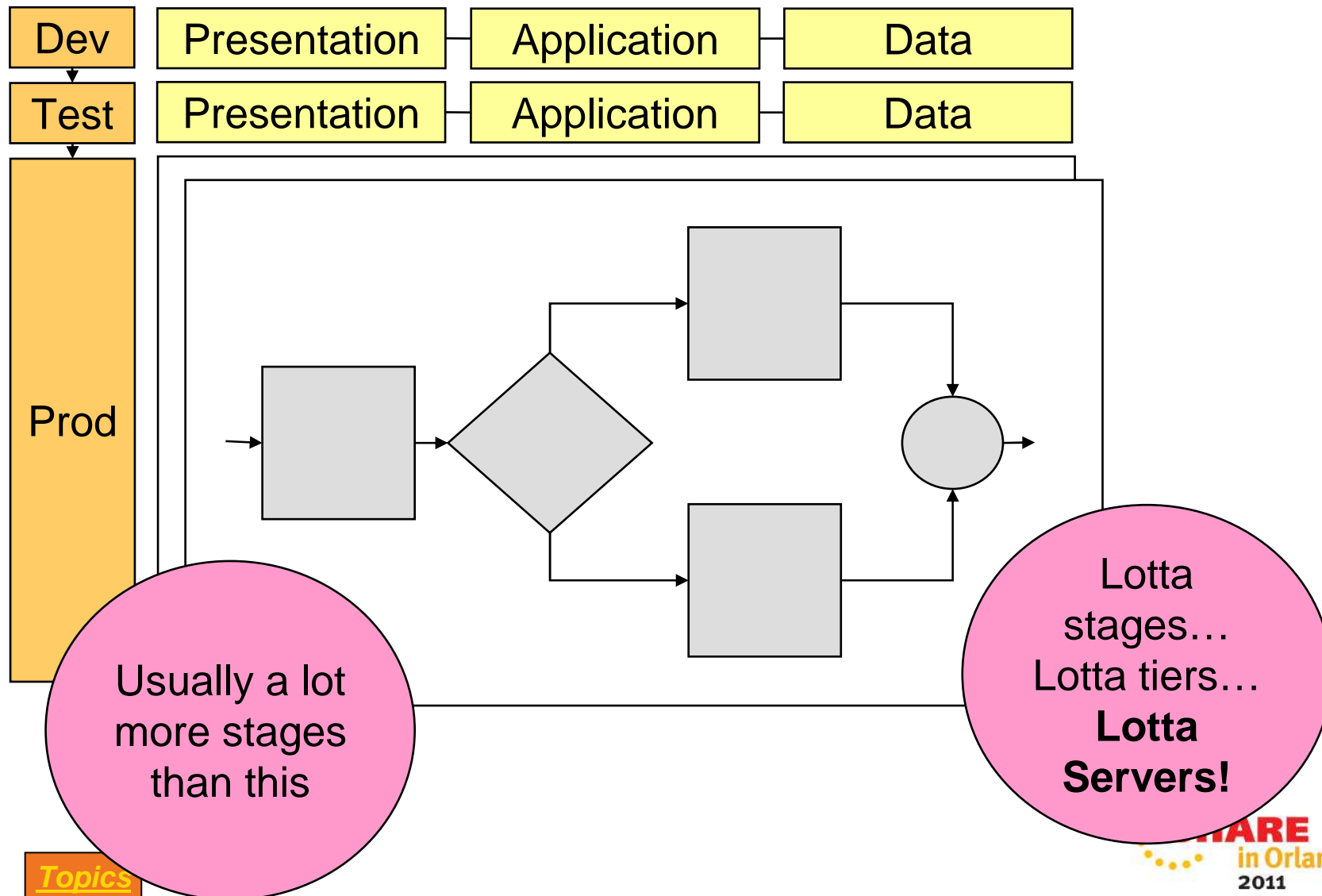
(indeed ... lots of inter-related (SOA-ish) business processes)



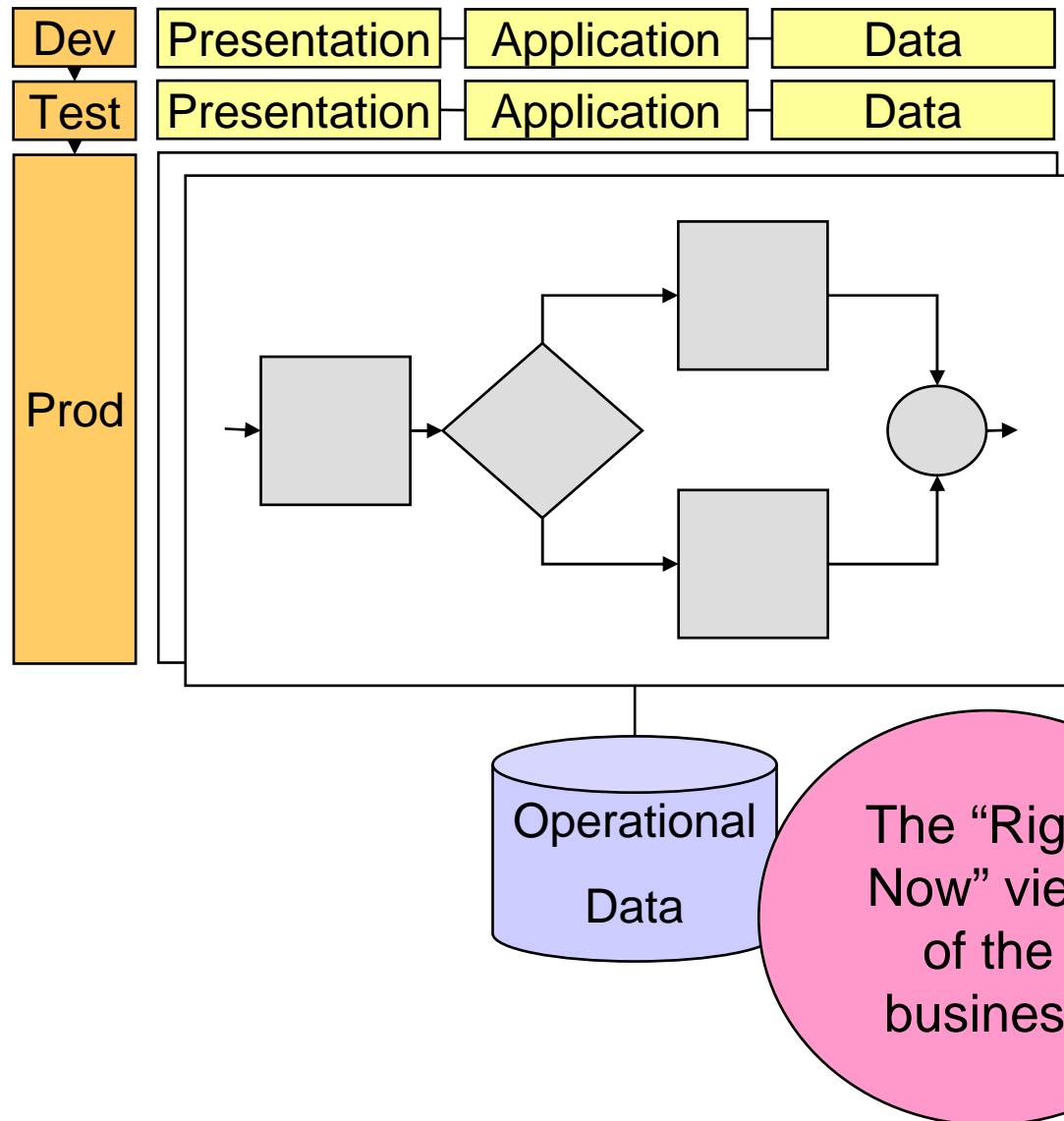
It's About Automating/Enabling the Business Process (using repeatable architectural patterns)



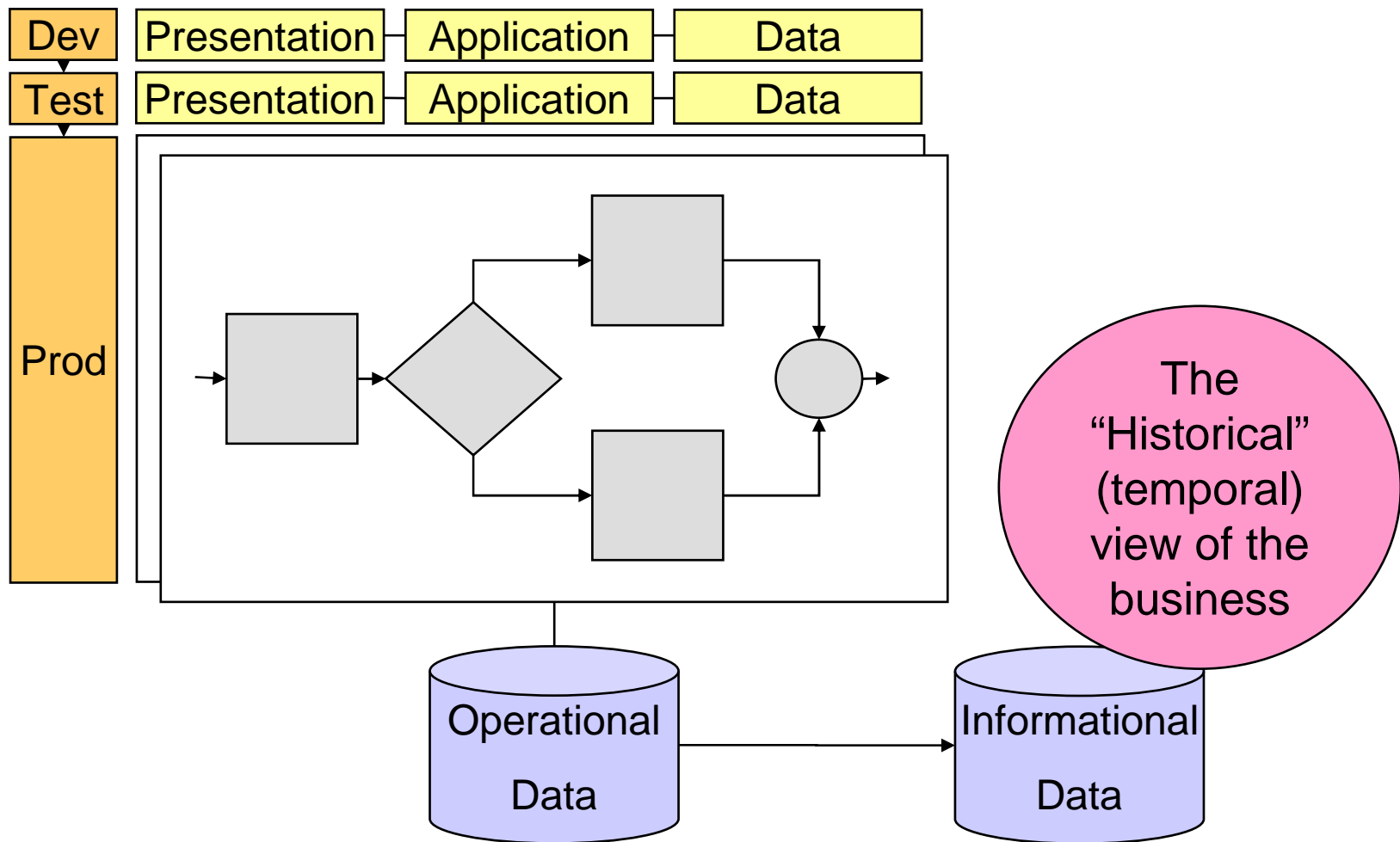
It's About Building/Deploying the Business Process (the application development lifecycle)



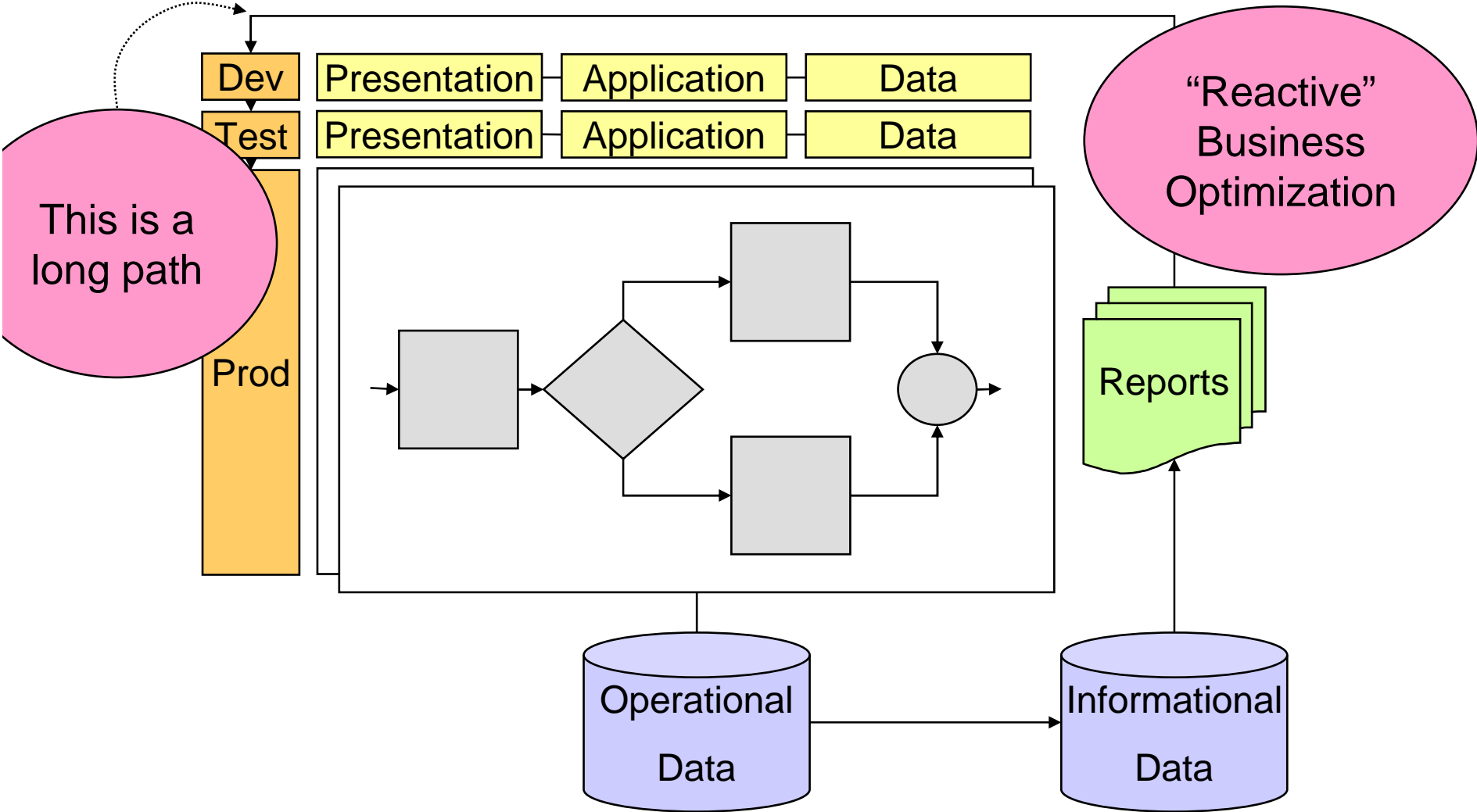
It's About the Data



It's About the Data ... It's About the Information (in the data) (Business Intelligence is about turning data into information)

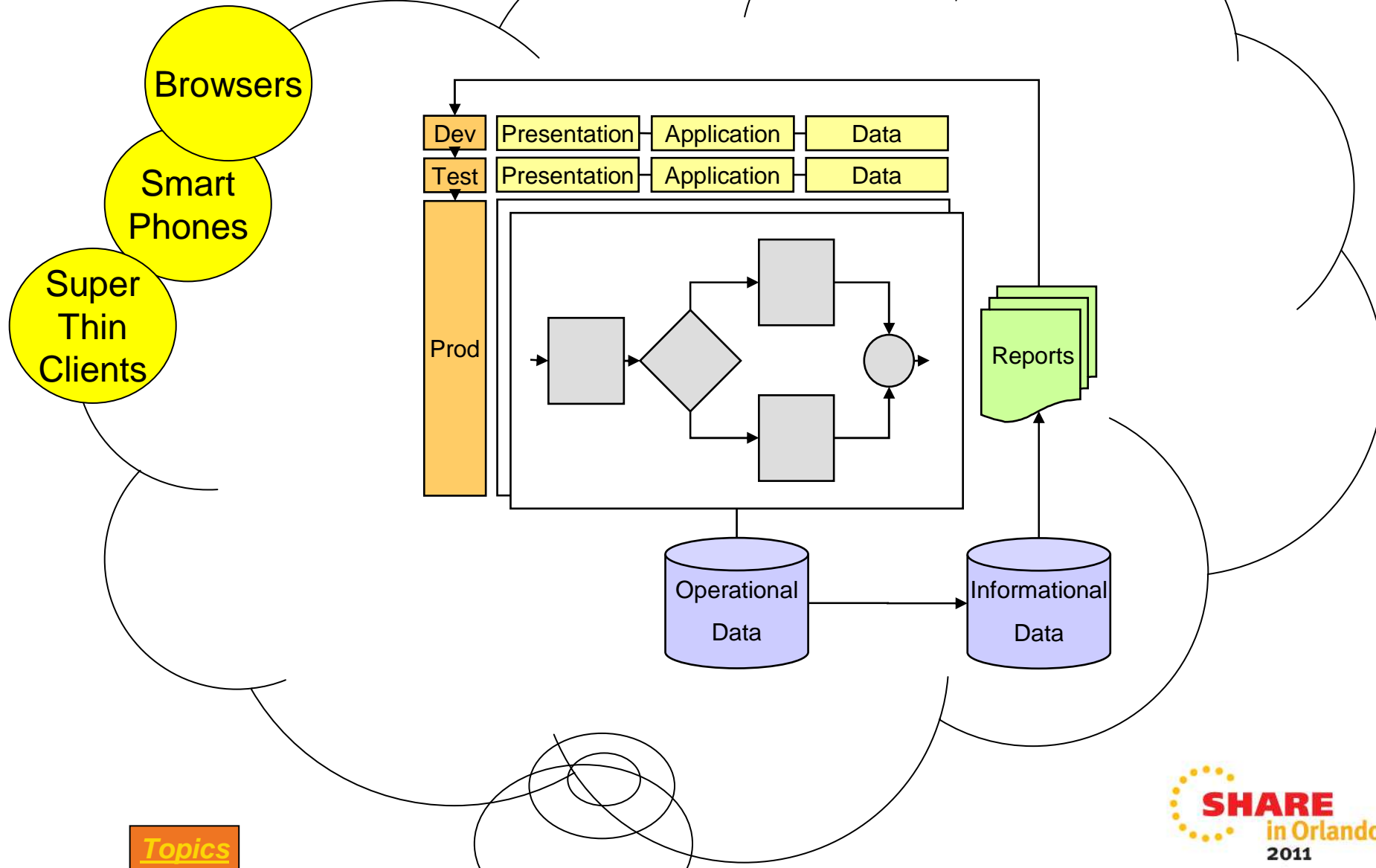


It's About Making the Business (Process) Better

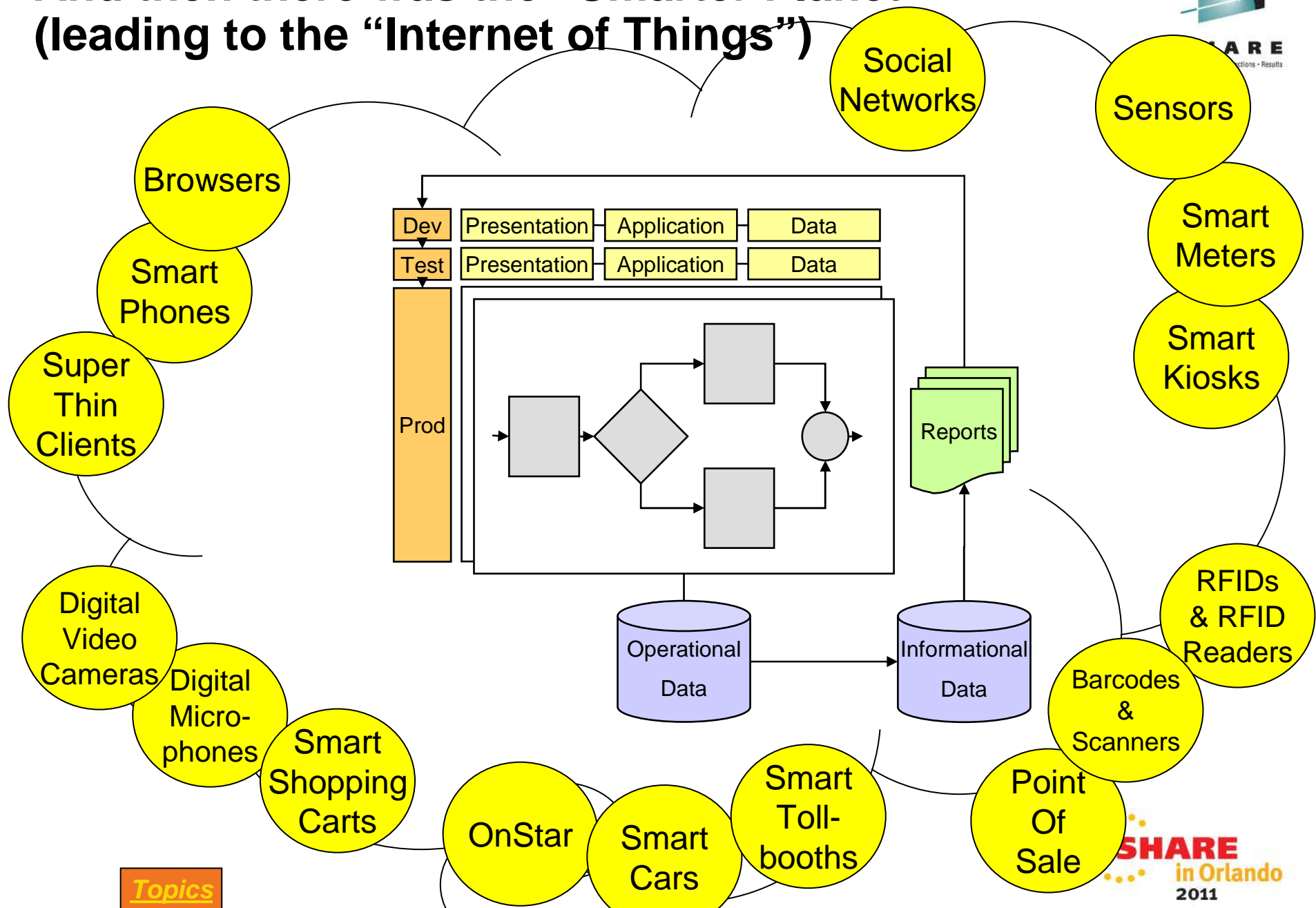


Topics

And then there was the “Smarter Planet” (starting with the “Connections”)

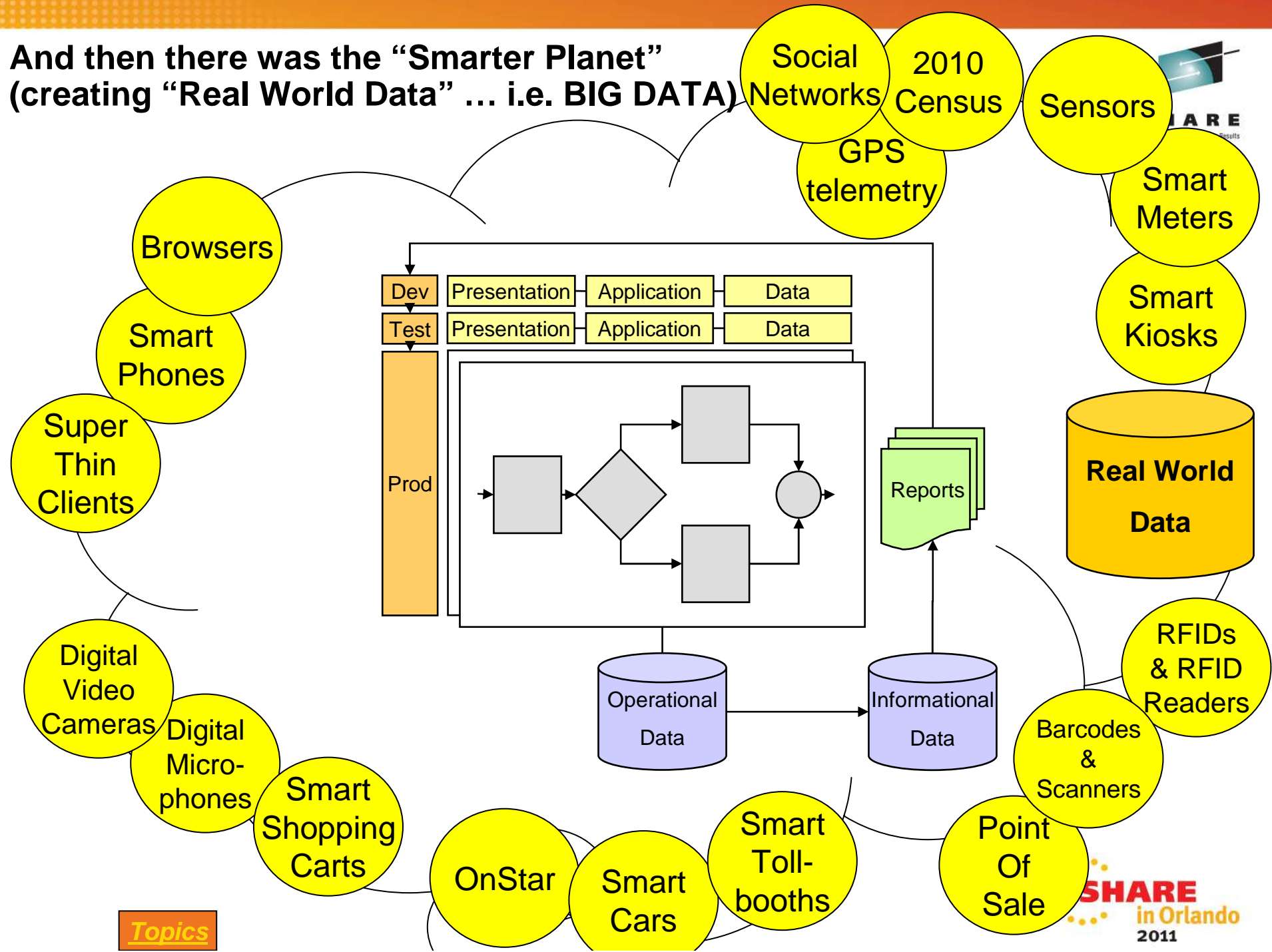


And then there was the “Smarter Planet” (leading to the “Internet of Things”)



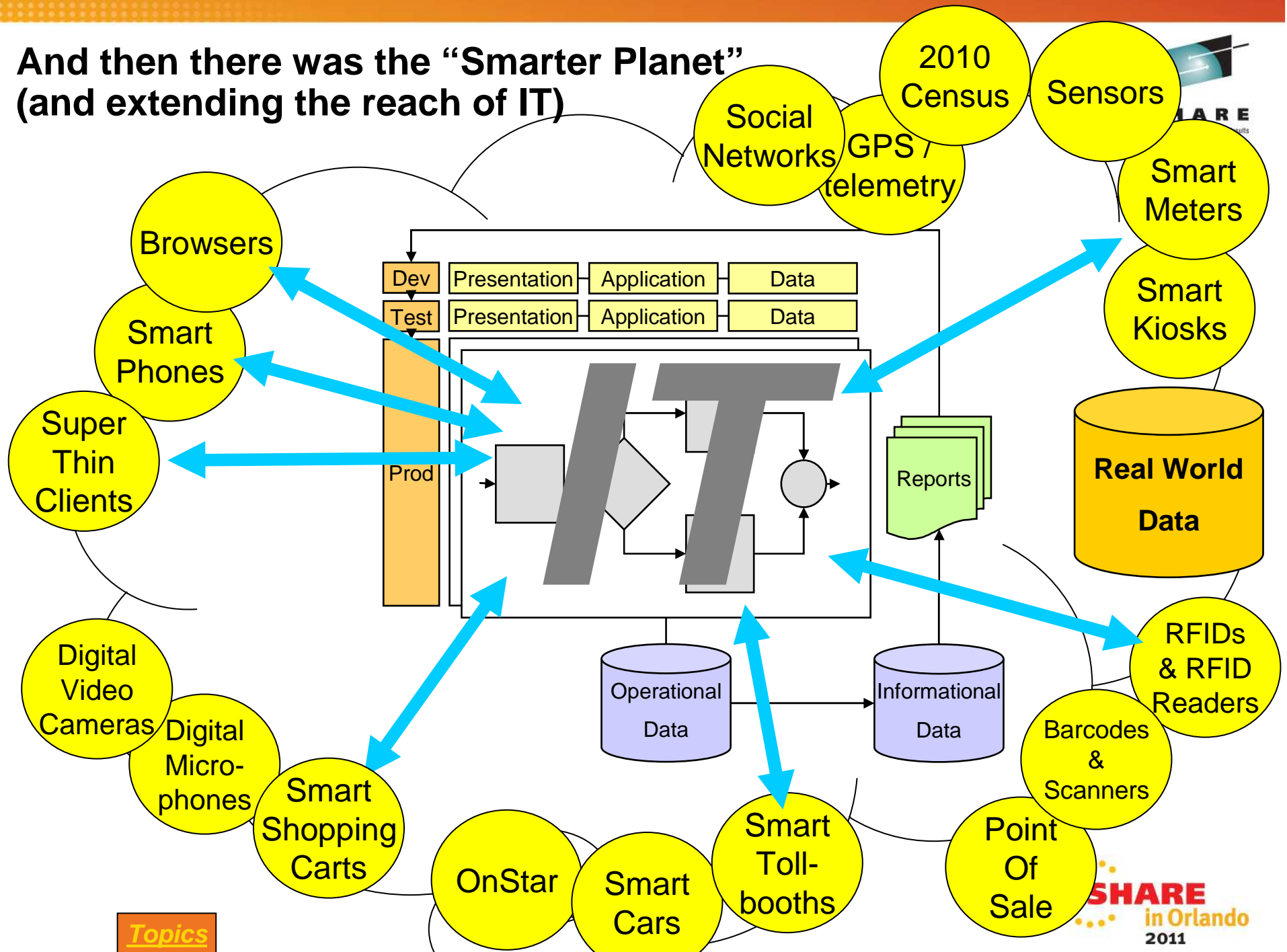
Topics

And then there was the "Smarter Planet" (creating "Real World Data" ... i.e. BIG DATA)



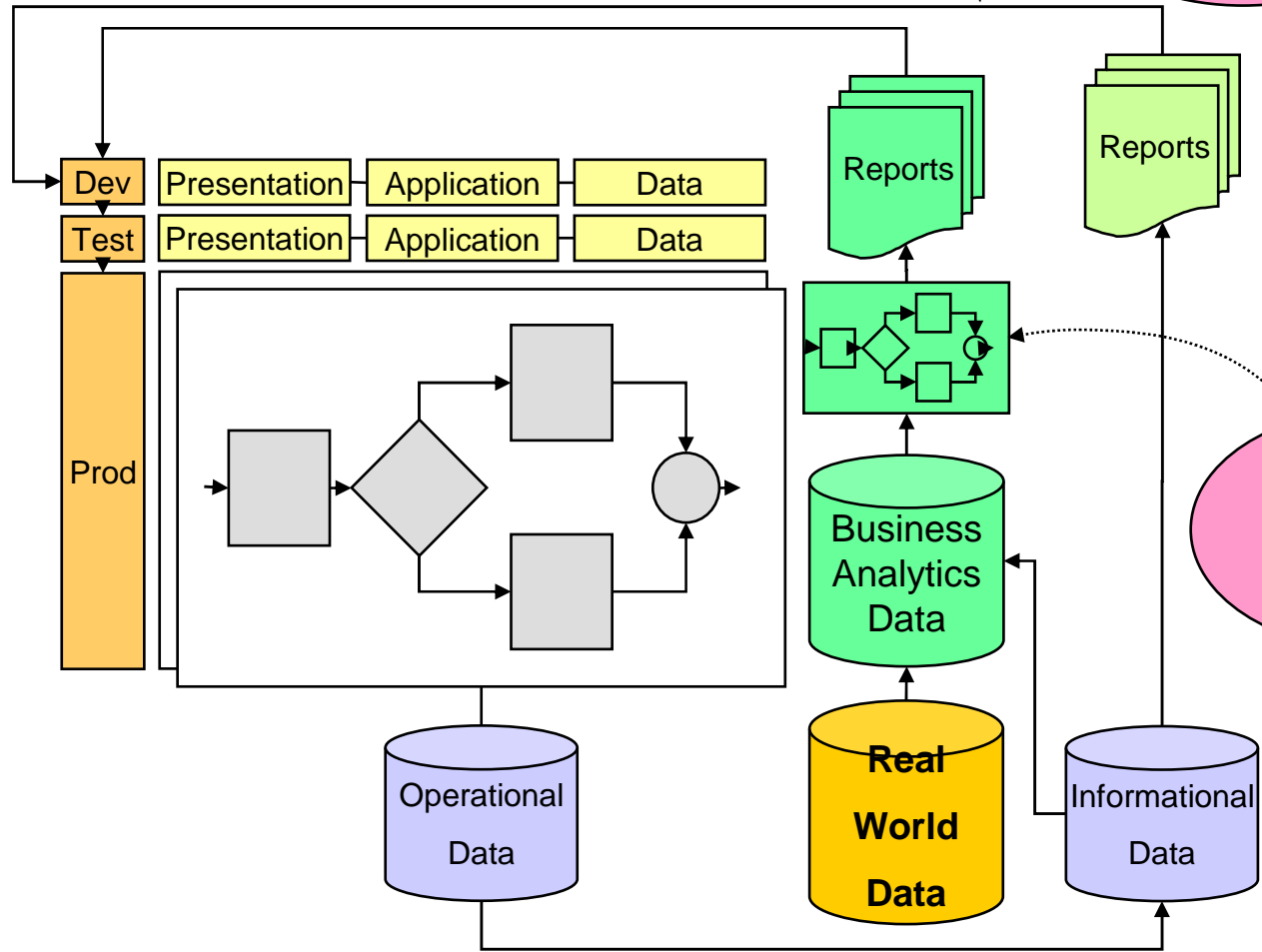
Topics

And then there was the "Smarter Planet" (and extending the reach of IT)

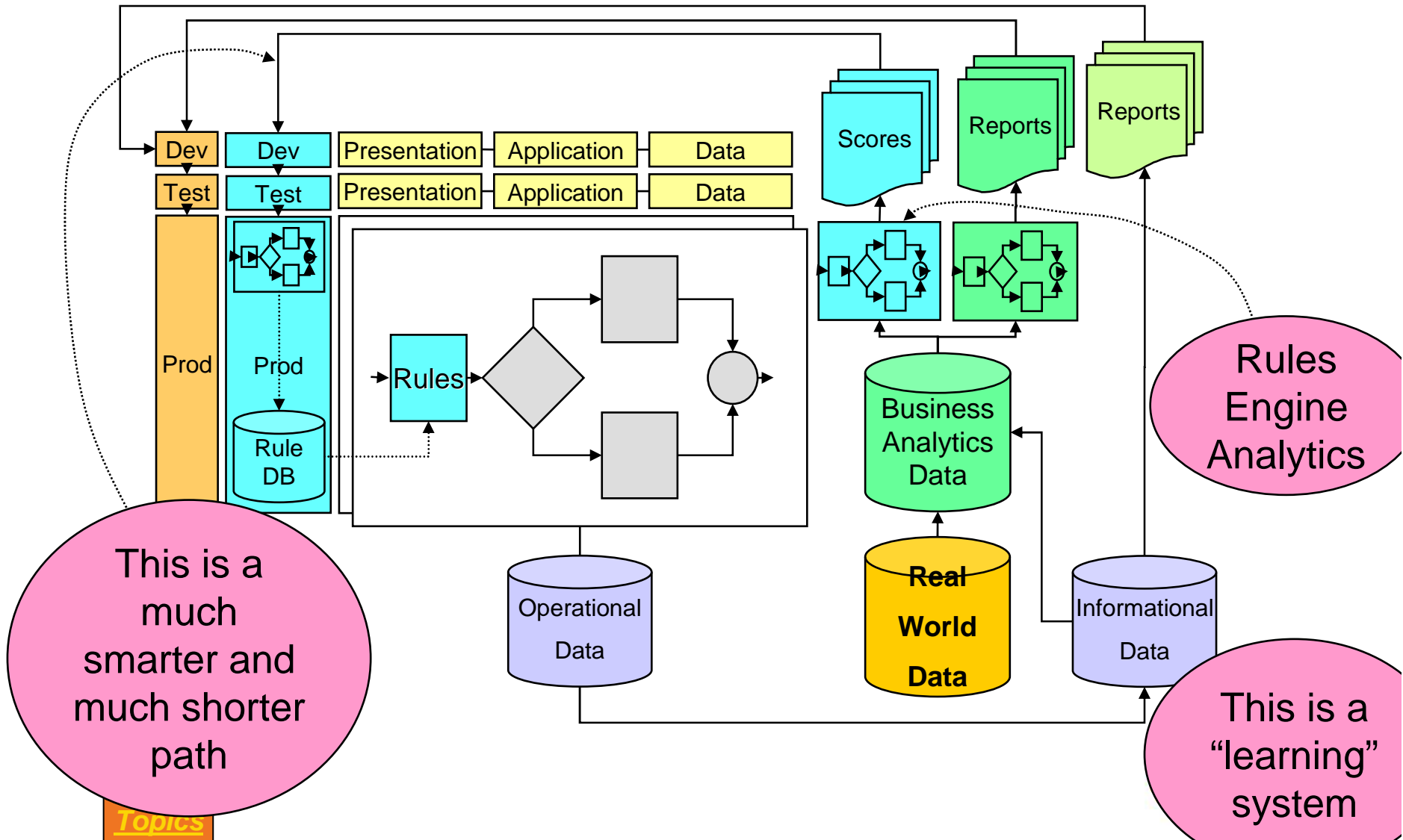


And then there was the “Smarter Application”
(offering a “Smarter” way to evolve the business)

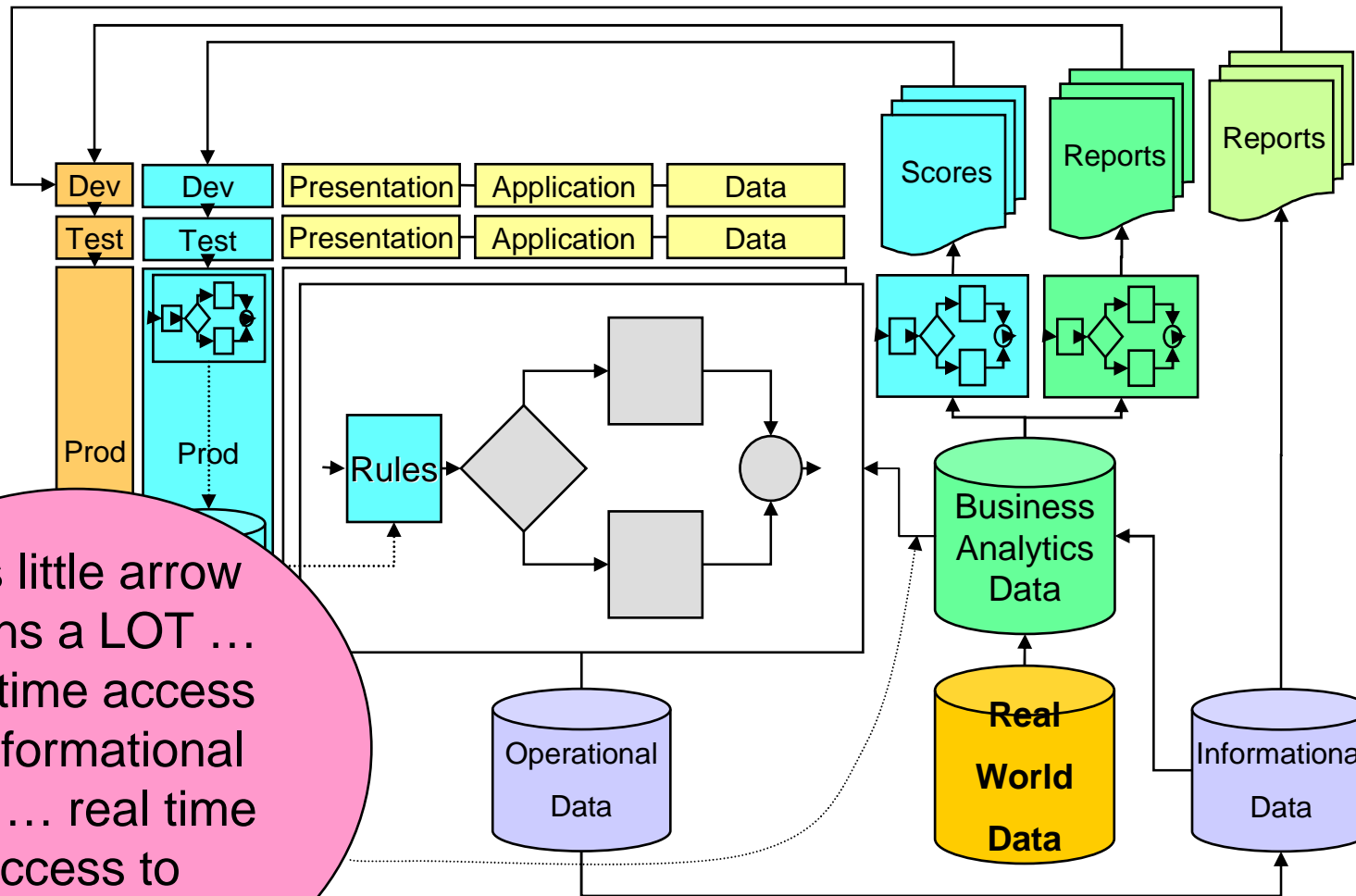
This is a much smarter path



And then there was the “Smarter Application” (offering a “Smarter” and “Faster” way to evolve the business)

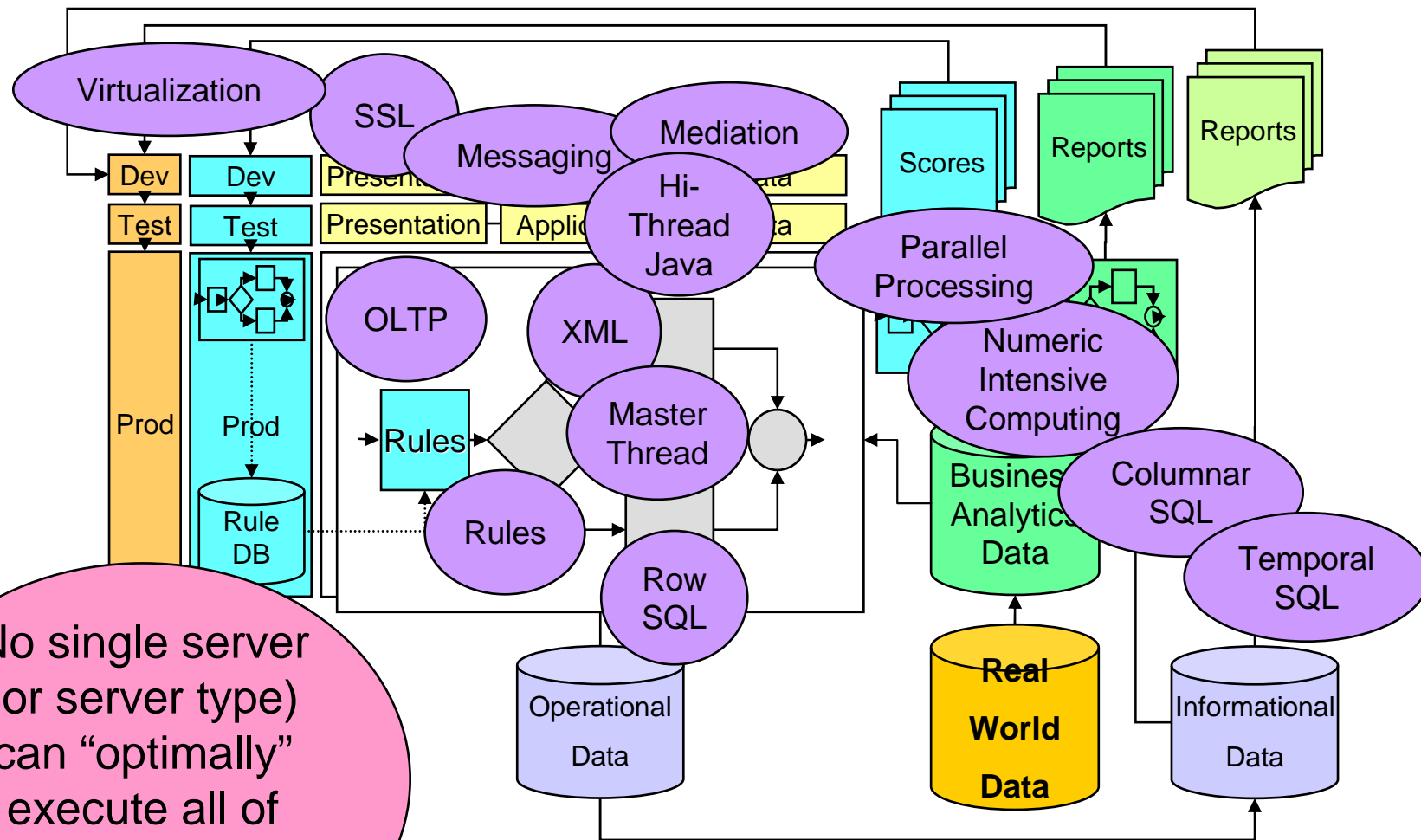


And then there was the “Smarter Application” (offering a “Smarter” TRANSACTION!!!!)



This little arrow means a LOT ... real-time access to informational data ... real time access to analytics!

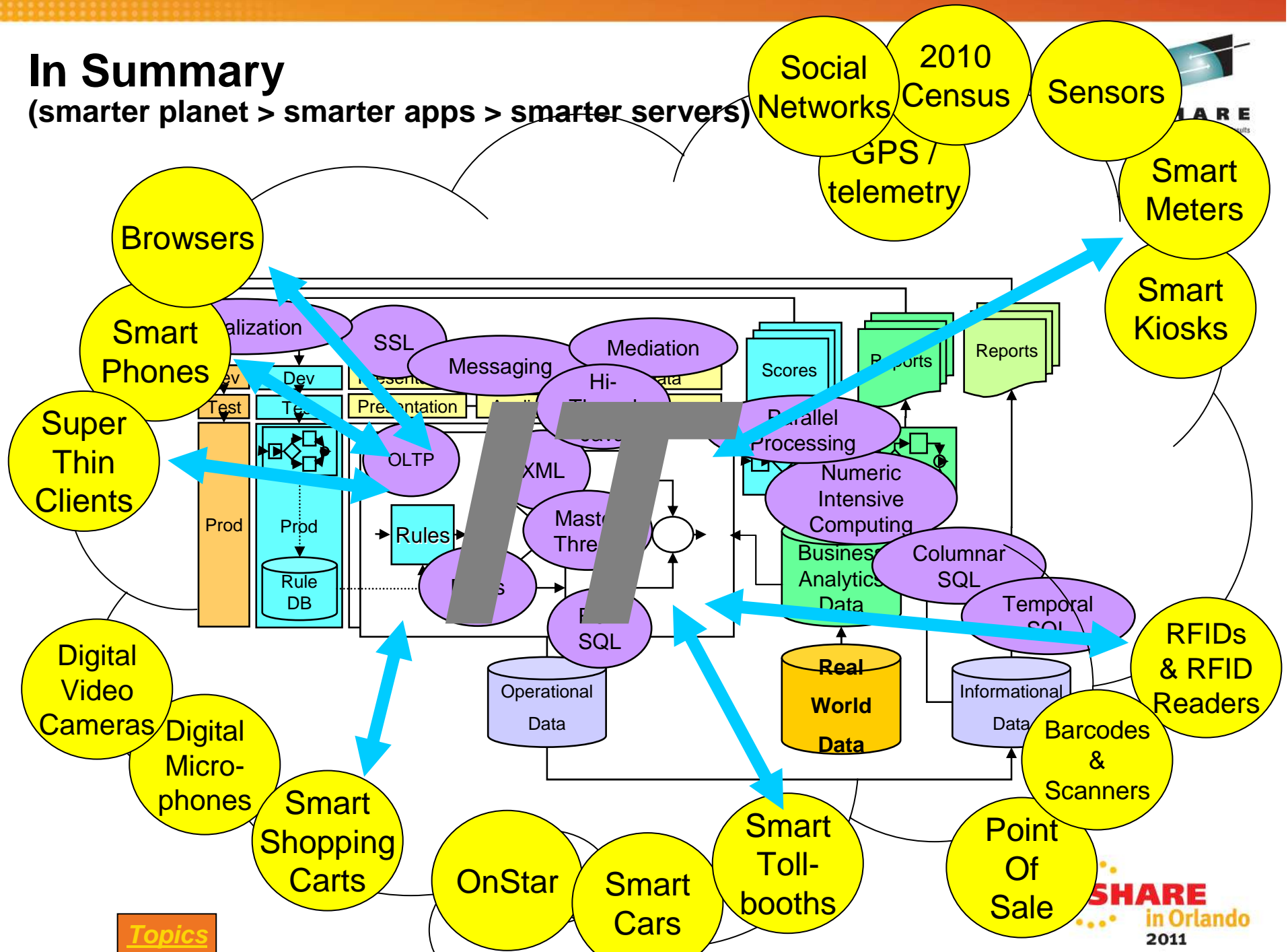
And then there was the “Smarter Application” (driving requirements for “Smarter” servers)



No single server
(or server type)
can “optimally”
execute all of
these compute
models

In Summary

(smarter planet > smarter apps > smarter servers)



Topics

End of Section



Topics



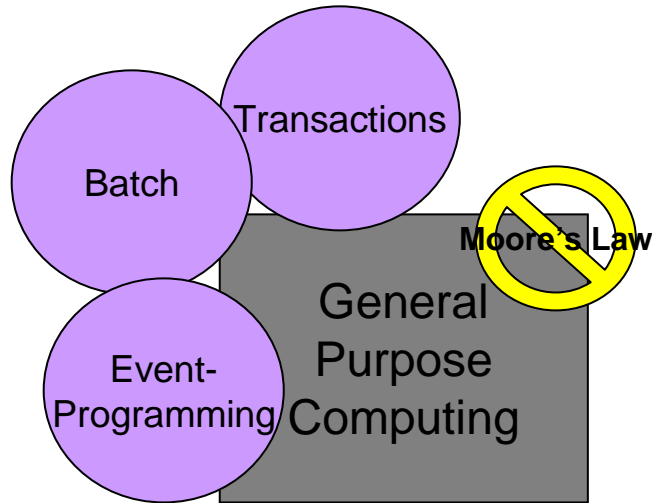
Design Principles



Topics



General Purpose Computing



General Purpose Computing...

Continues to be the cornerstone of business process automation.

New event-based programming models (a natural fit with real-world extension of IT)

If Moore's Law is not broke ... it is at least getting bent!

What's your definition?
(2X density improvement every 18mo)
(2X speed-up every 2yr)
(2X price/performance every 2yr)

CMOS technology progress is slowing.

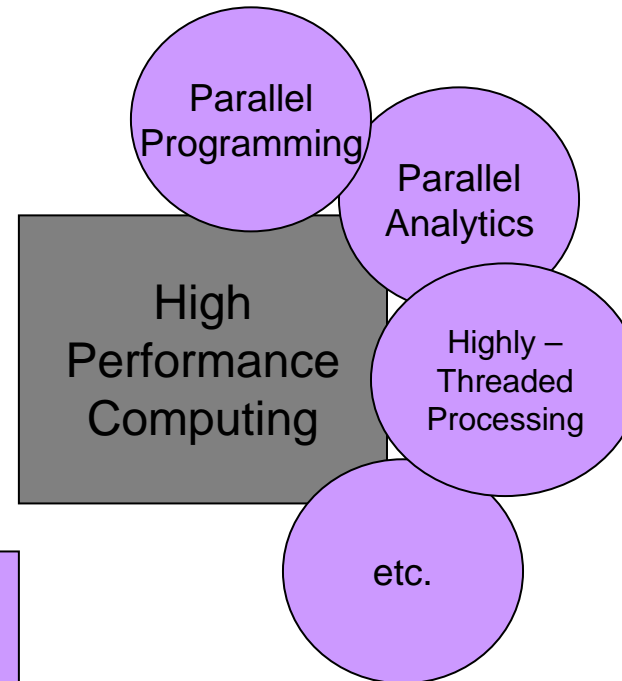
Logic density growth is slowing.

Frequency growth is slowing.
(some may say "reversing")

Cost/performance improvement is???

Many technology providers are taking non-traditional approaches to afford continued price/performance progress.

High Performance Computing



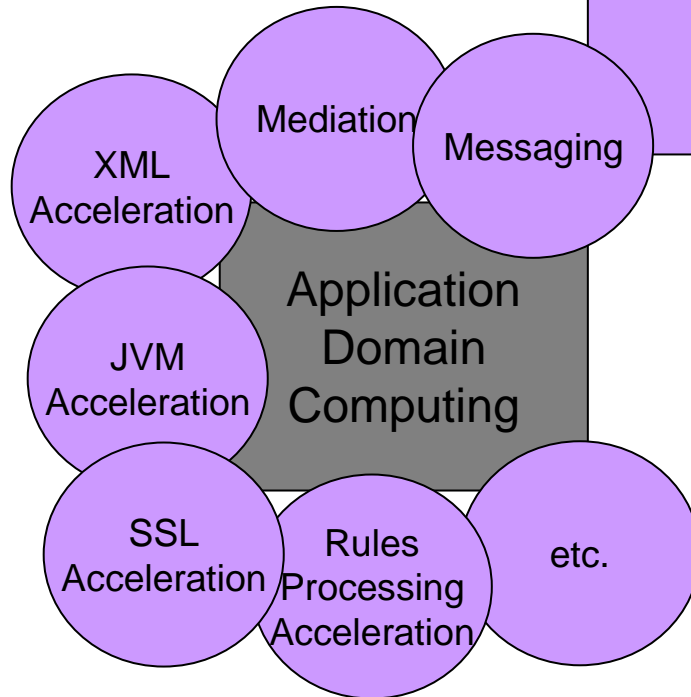
High Performance Computing...

Parallel algorithms
Vector processing
Analytic parallel-data algorithms
Predictive analytics
(patterns and correlations used to predict outcomes in real time)

Application Domain Computing



Application Domain Computing...
Specialized hardware & algorithms
Single-purpose
Appliance form factor

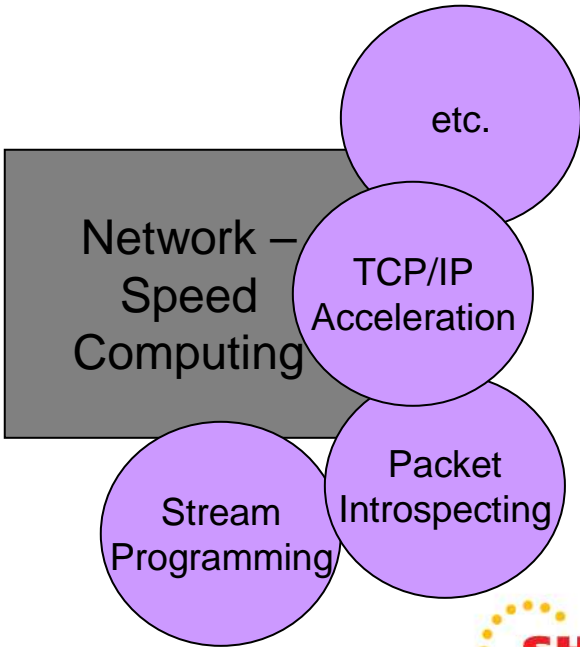


Network-Speed Computing

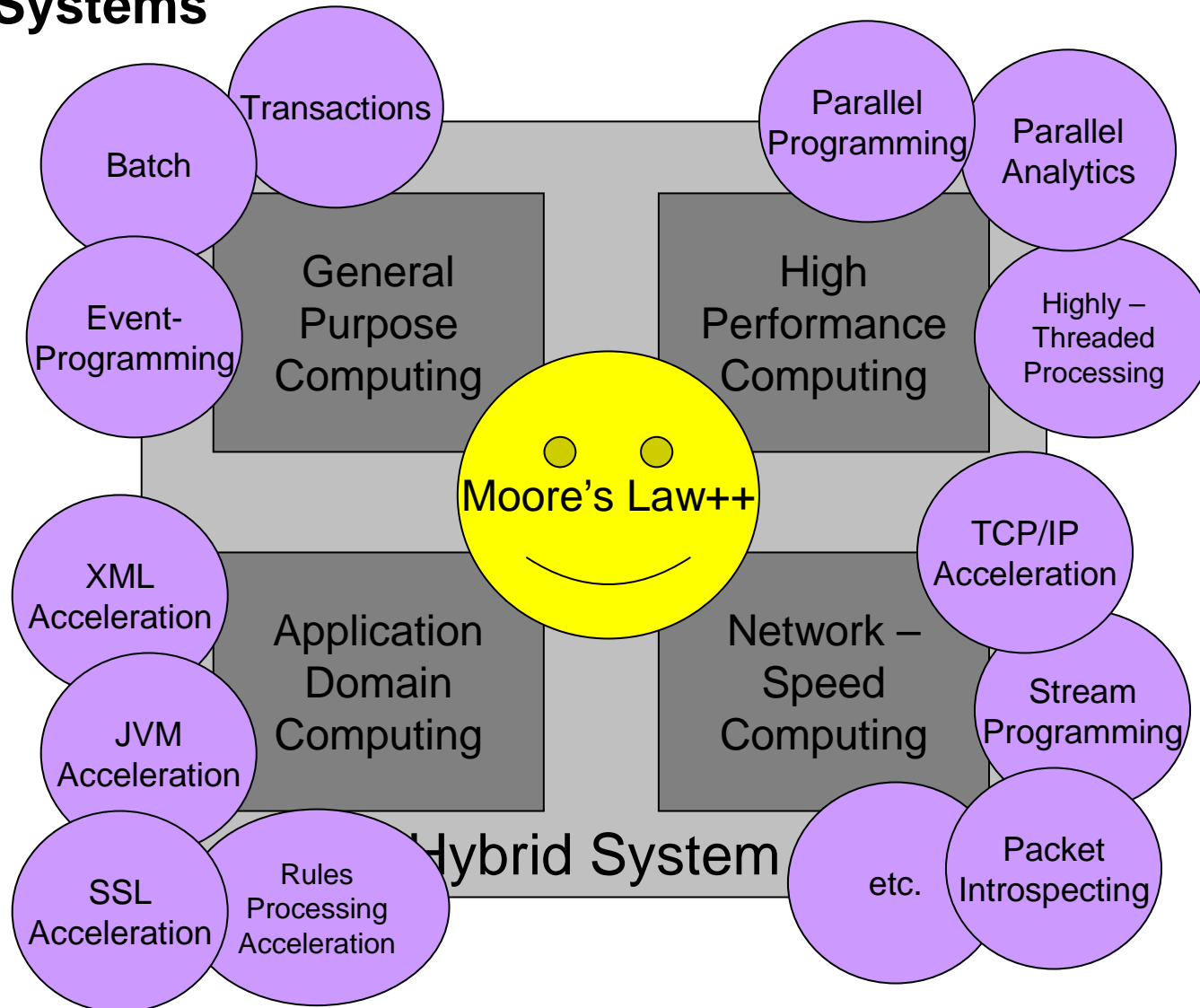


Network-Speed Computing...

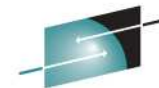
- Real-world data feeds
- Processing into actionable form
- Processing into reduced/stored form
- Processing into patterns
- Processing into events (triggers)



Hybrid Systems



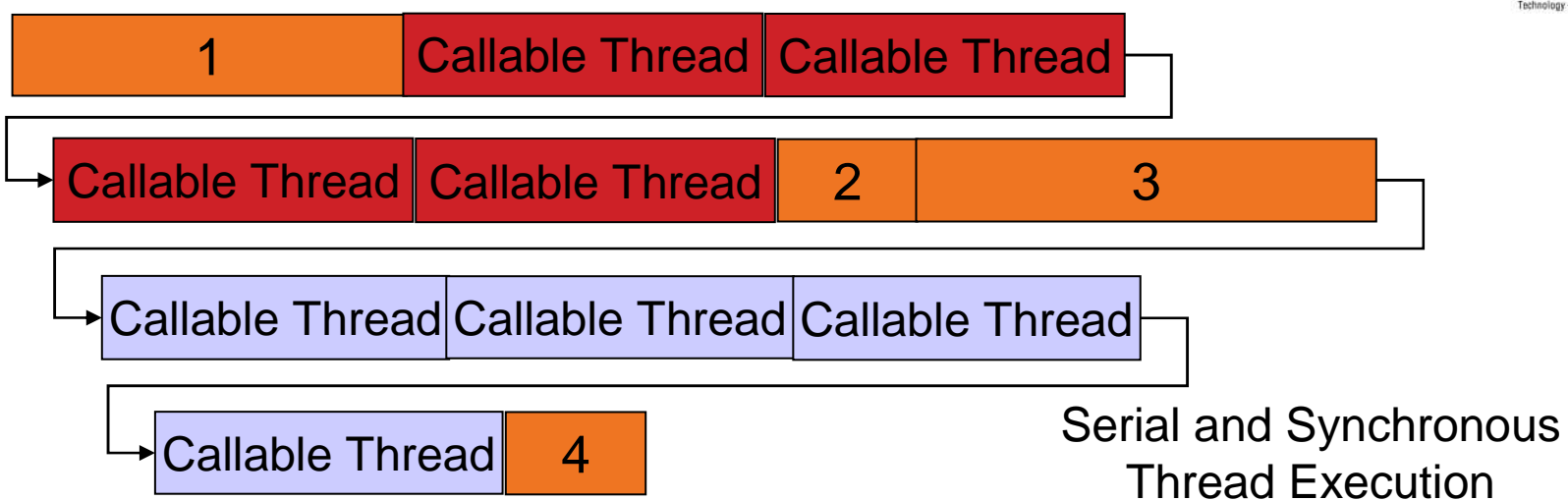
Designed not just to meet Moore's Law, but in the aggregate, to far exceed it!



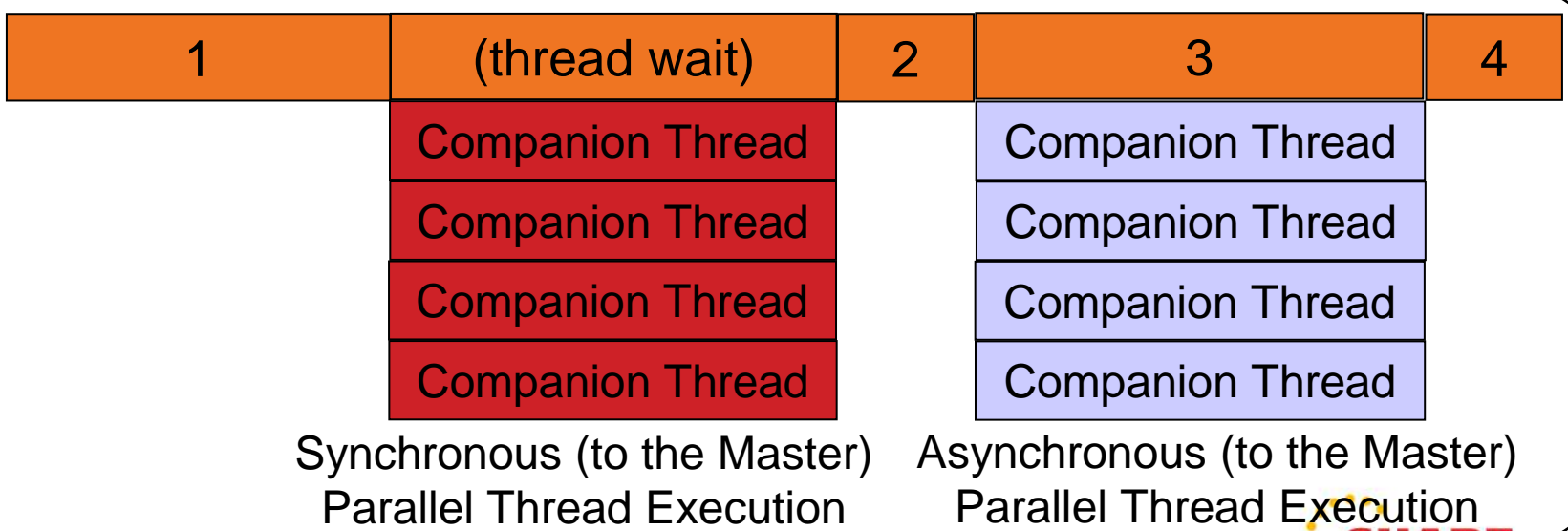
SHARE
Technology - Connections - Results

Hybrid Computing – a tail of two threading styles

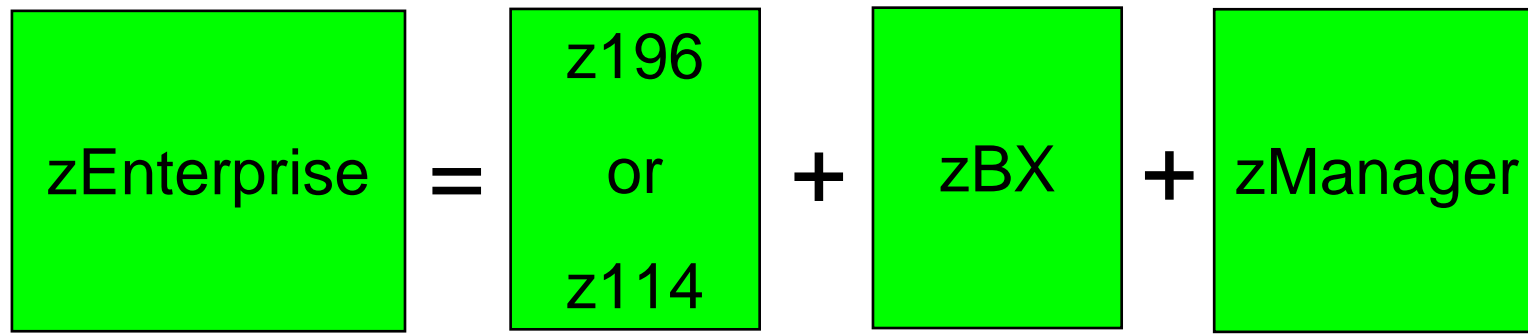
Single Thread



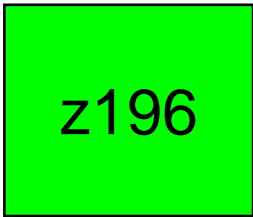
Hybrid Computing



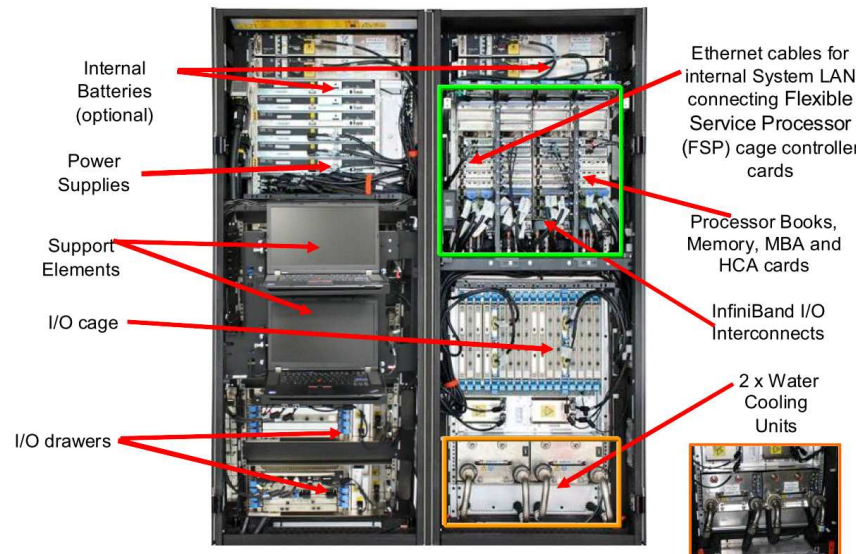
zEnterprise A “Smarter Server” Design



The z196

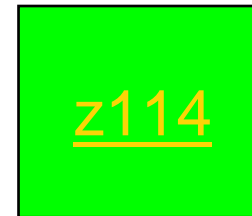


- Announced July 22 2010
- Generally Available September 2010
 - “Sold Out” in 4Q 2010
(best “z Quarter” in a decade)
- Replacement for the z10 EC family of servers
- Enhances and embellishes on trends started by the z10 EC
 - Cornerstone of the zEnterprise



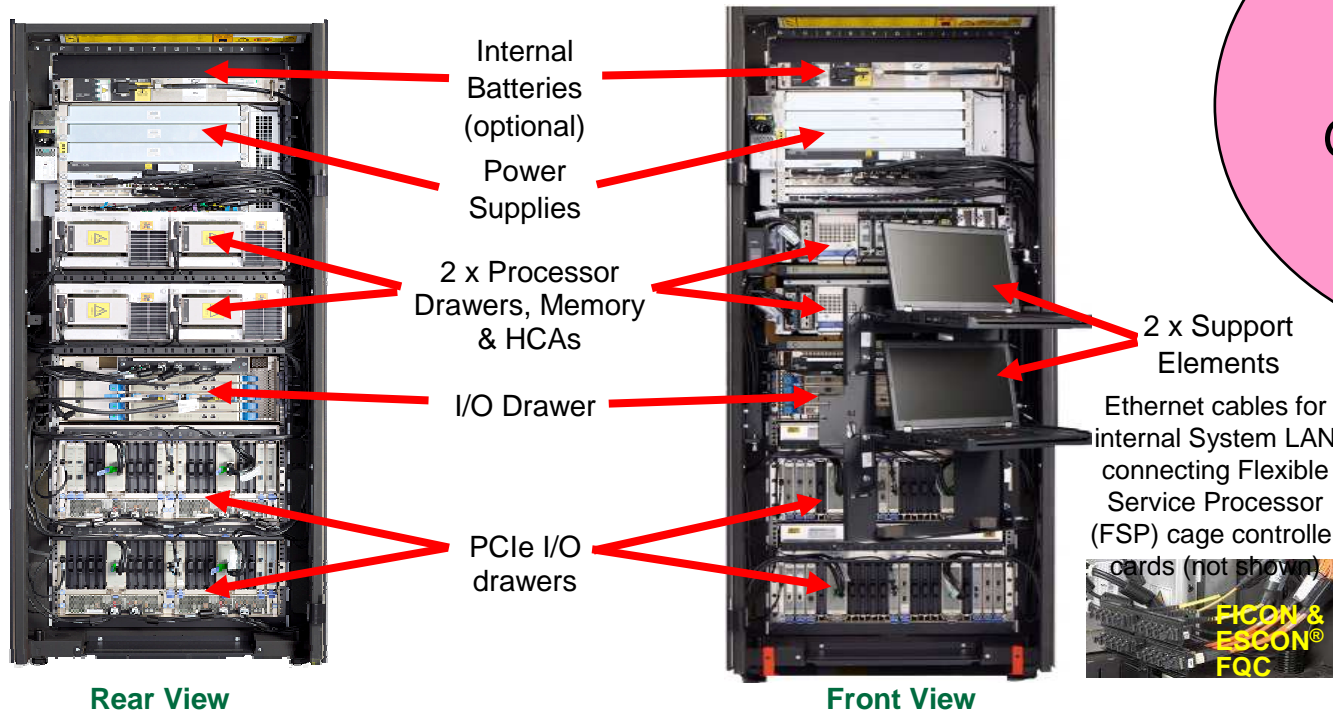
State of the Art
In High End
General Purpose
Computing

The z114



- Announced July 12 2011
- Generally Available Sept 09 2011
- Replacement for the z10 BC family of servers
- Cornerstone of the zEnterprise

State of the Art
in Mid Range
General Purpose
Computing



Topics



MICROPROCESSOR:

4.4GHz

Superscalar

In Order

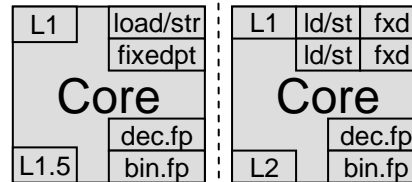
L1 (192KB) and

L1.5 (3MB) per core
(total:12.75MB per chip)

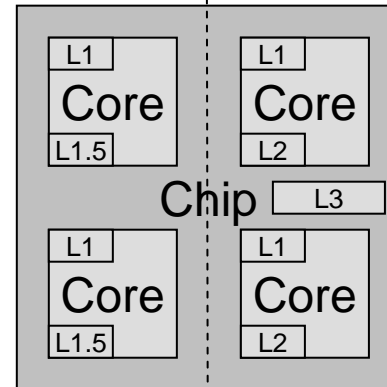
1B transistors

44 new instructions

z10 EC



z196



MICROPROCESSOR:

5.2GHz

Super-duper-scalar

Out of Order

L1 (192KB) and

L2 (1.5MB) per core
(new) L3 per chip (24MB)
(total:30.75MB per chip)

1.4B transistors

110 new instructions



MICROPROCESSOR:

4.4GHz

Superscalar

In Order

L1 (192KB) and

L1.5 (3MB) per core
(total:12.75MB per chip)

1B transistors

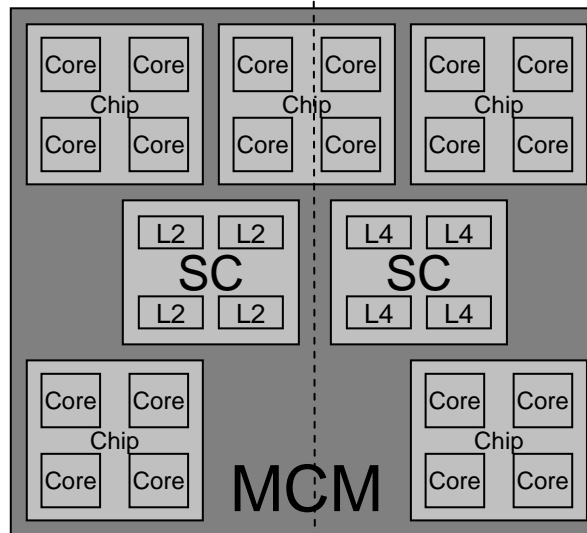
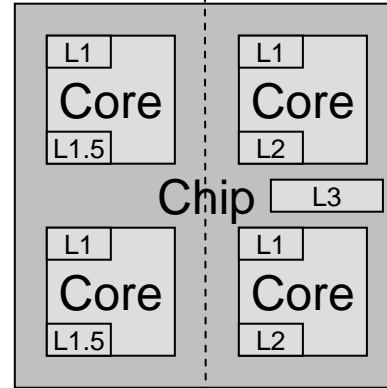
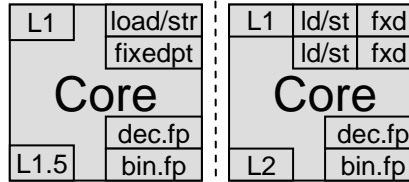
44 new instructions

MCM:

5 "z6" chips

L2 (48MB on 2SCchips)

z10 EC z196



MICROPROCESSOR:

5.2GHz

Super-duper-scalar

Out of Order

L1 (192KB) and

L2 (1.5MB) per core
(new) L3 per chip (24MB)
(total:30.75MB per chip)

1.4B transistors

110 new instructions

MCM:

6 "z7" chips

L4 (192MB on 2SC chips)



Moore's Law ... bent but not broken

MICROPROCESSOR:

4.4GHz

Superscalar

In Order

L1 (192KB) and

L1.5 (3MB) per core
(total:12.75MB per chip)

1B transistors

44 new instructions

MCM:

5 "z6" chips

L2 (48MB on 2SCchips)

CEC:

1 to 4 books

64/77 cores

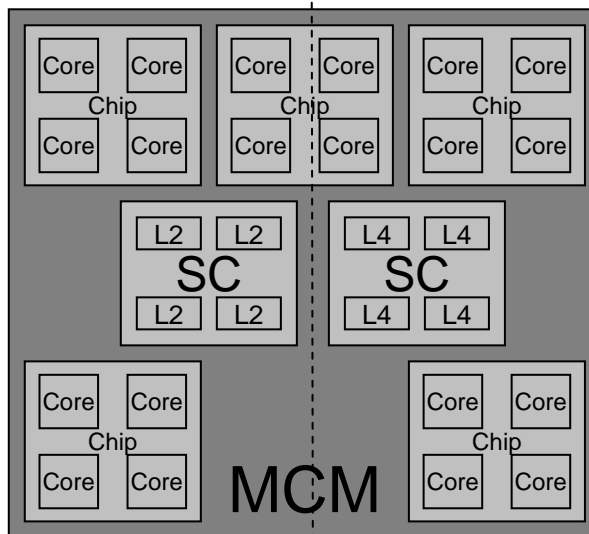
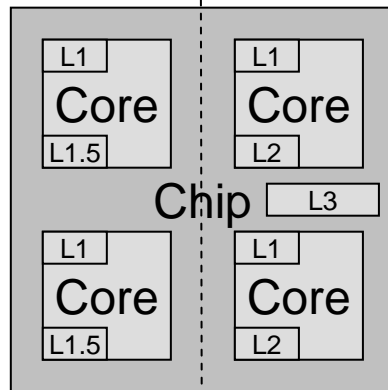
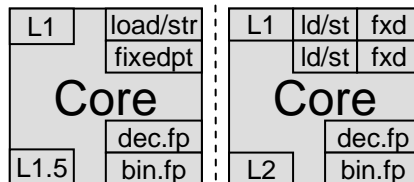
up to 1.5TB RAM

~1,000 to ~30,000MIPS

6GBps Infiniband

z10 EC

z196



Moore's Law ... bent but not broken

MICROPROCESSOR:

5.2GHz

SHARE
Technology - Connections - Results

Super-duper-scalar

Out of Order

L1 (192KB) and

L2 (1.5MB) per core

(new) L3 per chip (24MB)
(total:30.75MB per chip)

1.4B transistors

110 new instructions

MCM:

6 "z7" chips

L4 (192MB on 2SC chips)

CEC:

1 to 4 books

80/96 cores

up to 3.0TB **RAIM**

~1,500 to ~50,000MIPS

6GBps Infiniband

Hot plug I/O drawers

Optional Water Cooling

Optional DC Power

zEnterprise Server – z196 or z114



	Package	Hardware	Hypervisor	VM / OS	Integration
General Purpose Computing	Main Frame	z196 or z114			
Application Domain Computing					
High Performance Computing					

Topics



zEnterprise Server – z196



	Package	Hardware	Hypervisor	VM / OS	Integration
General Purpose Computing	Main Frame	z196 or z114	PR/SM		
Application Domain Computing					
High Performance Computing					

Topics



zEnterprise Server – z196



	Package	Hardware	Hypervisor	VM / OS	Integration
General Purpose Computing	Main Frame	z196 or z114	PR/SM	z/OS	
				z/VSE	
Application Domain Computing					
High Performance Computing					

Topics



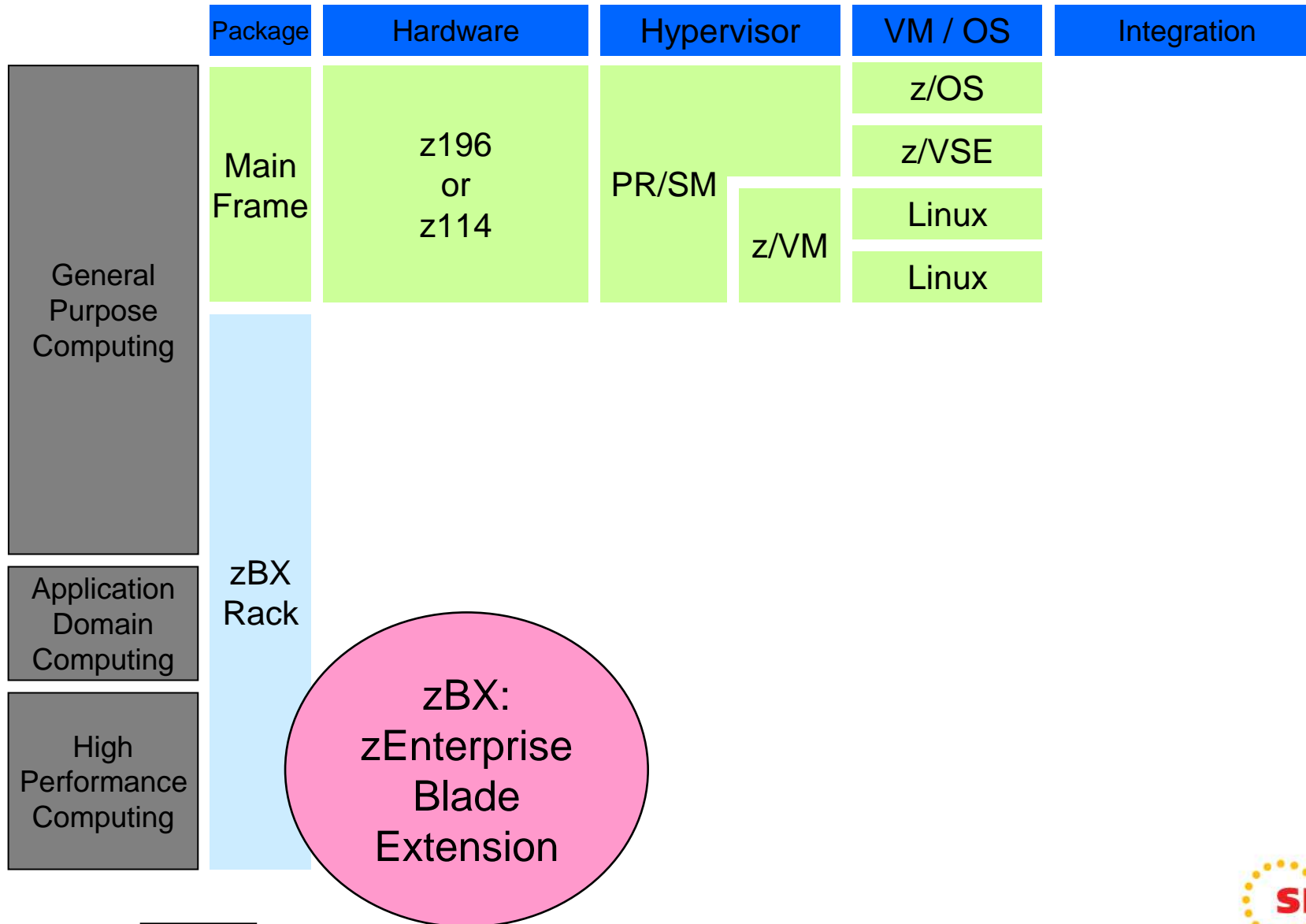
zEnterprise Server - z196



	Package	Hardware	Hypervisor	VM / OS	Integration
General Purpose Computing	Main Frame	z196 or z114	PR/SM	z/OS	
				z/VSE	
				Linux	
				Linux	
Application Domain Computing			z/VM		
High Performance Computing					

This much should look and feel like mainframe computing of today and the past decade (or so)...
IFLs/zIIPs/zAAPs etc.

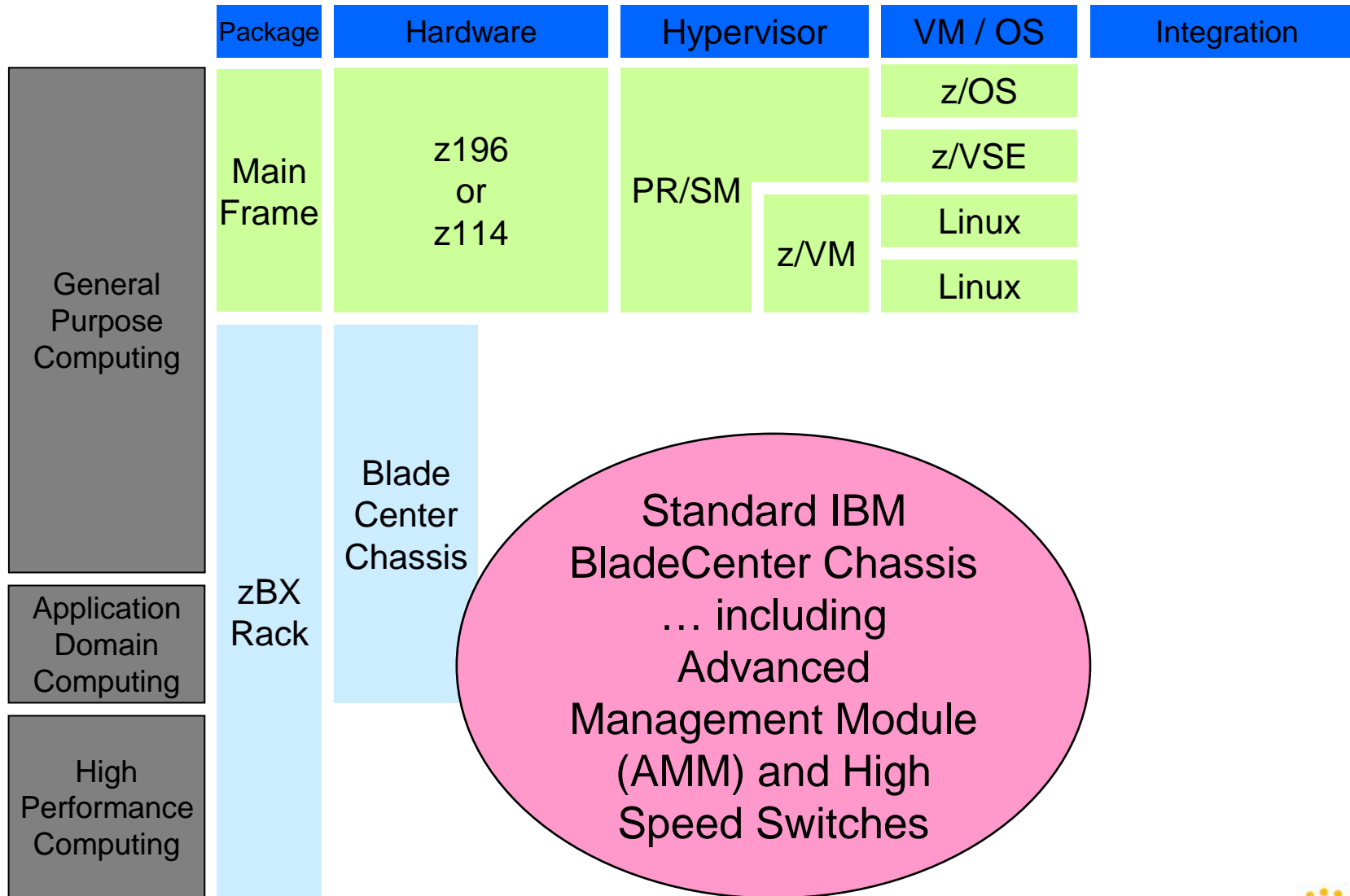
zEnterprise Server – zBX



Topics



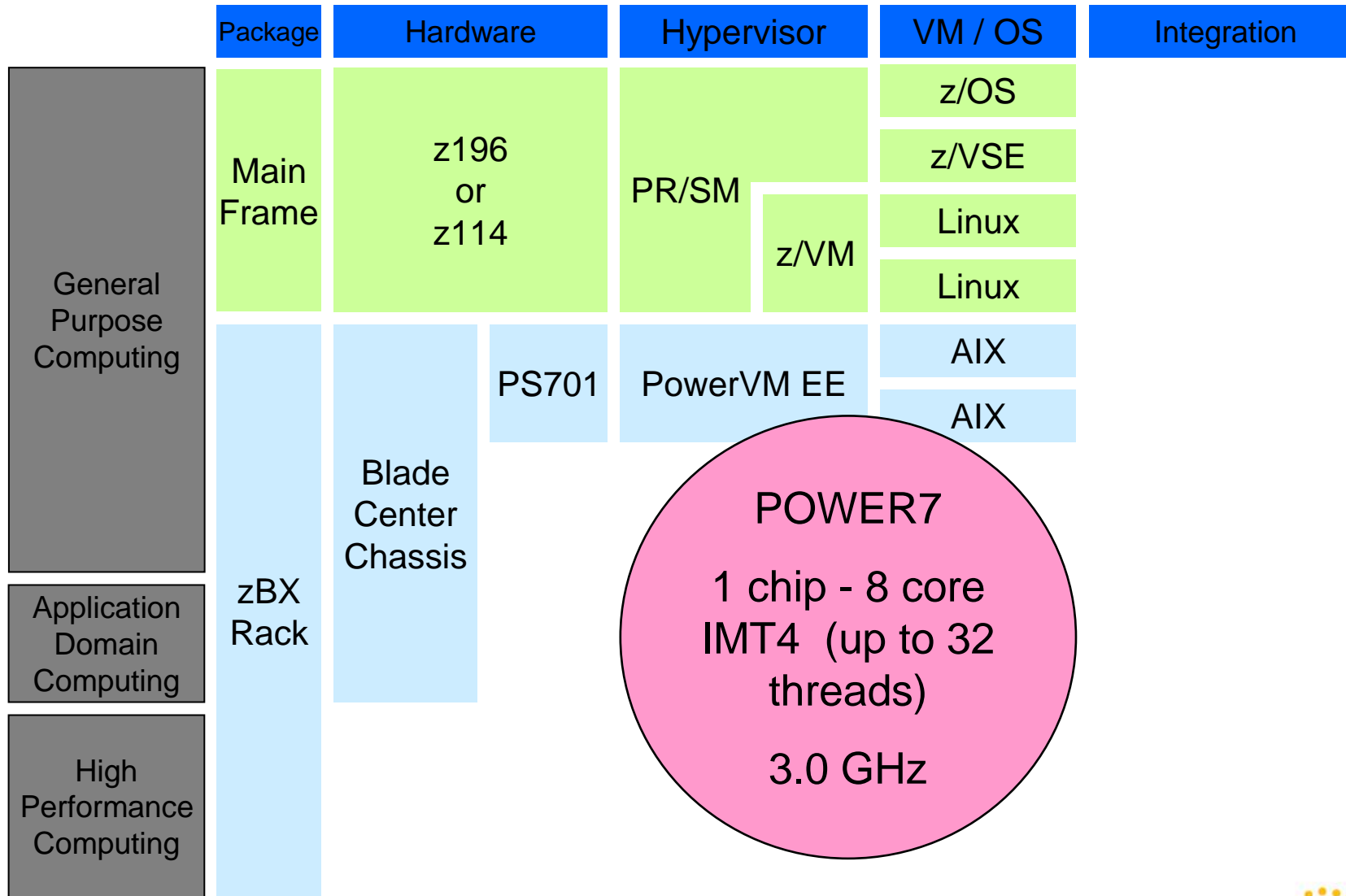
zEnterprise Server – BladeCenter Chassis



Topics



zEnterprise Server – POWER (PS701)



Topics



zEnterprise Server – x86 (HX5)



	Package	Hardware	Hypervisor	VM / OS	Integration
General Purpose Computing	Main Frame	z196 or z114	PR/SM	z/OS	
				z/VSE	
				Linux	
				Linux	
Application Domain Computing	zBX Rack	Blade Center Chassis	PS701	PowerVM EE	AIX
				PowerVM EE	AIX
		HX5	KVM	Linux	
				Windows	
High Performance Computing					

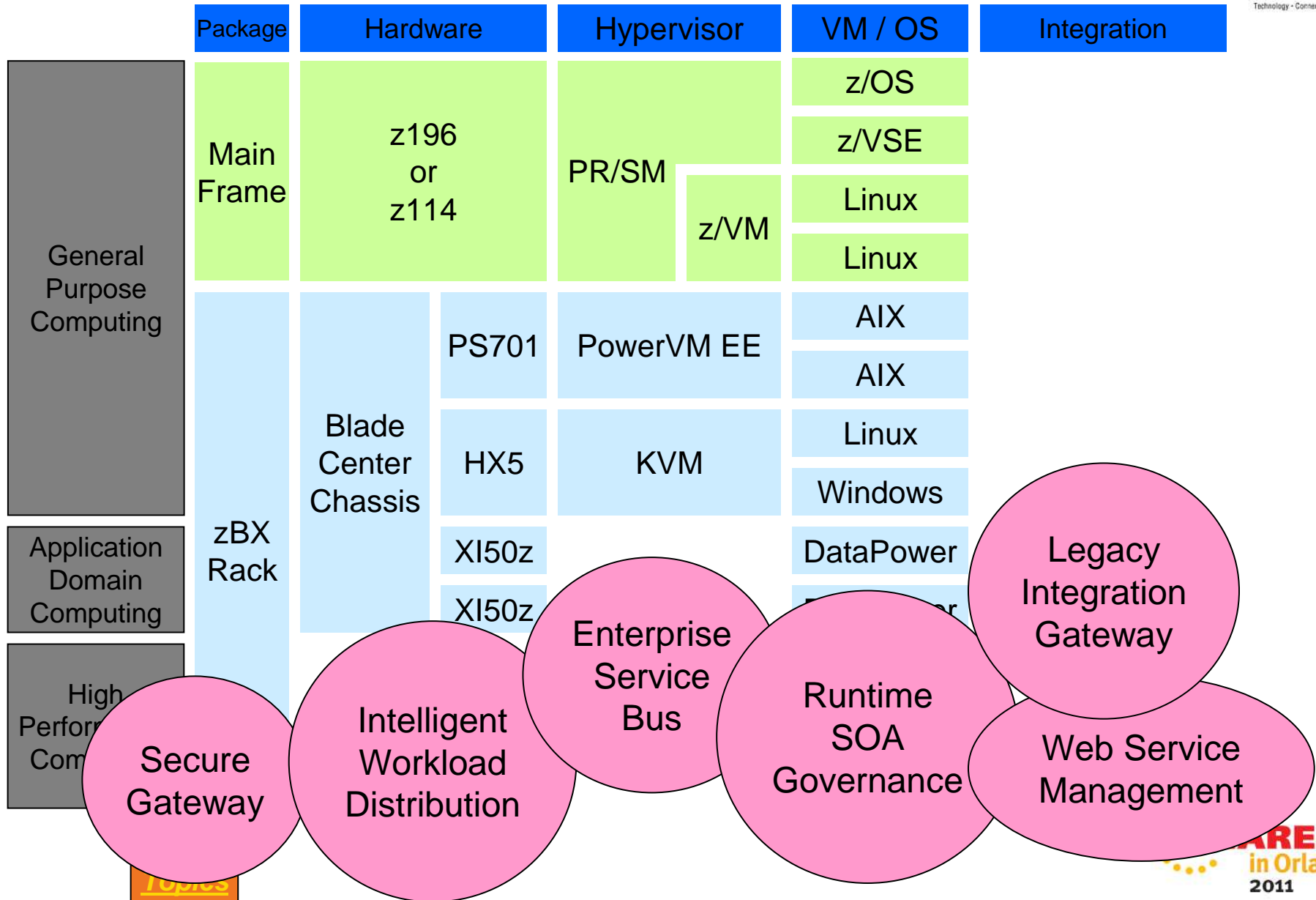
IBM x86 - Linux
HX5 Sept 26
2 chip - 16 core

Windows SOD
4Q2011

Topics



zEnterprise Server - DataPower



zEnterprise Server - ISAO

IBM Smart Analytics Optimizer



	Package	Hardware	Hypervisor	VM / OS	Integration	
General Purpose Computing	Main Frame	z196 or z114	PR/SM	z/OS	DB2 for z/OS "extension"	
				z/VSE		
				Linux		
				Linux		
App Domain Computing	Rack	PS701	P/EE	AIX	In-Memory Parallel Database	
				AIX		
High Performance Computing	Blade Center Chassis	50z	ISAO	Coordinator	Predictable Response Times	
				ISAO		Worker
				ISAO		Worker

DB2 for z/OS "extension"

In-Memory Parallel Database

Columnar Storage Schema

Predictable Response Times

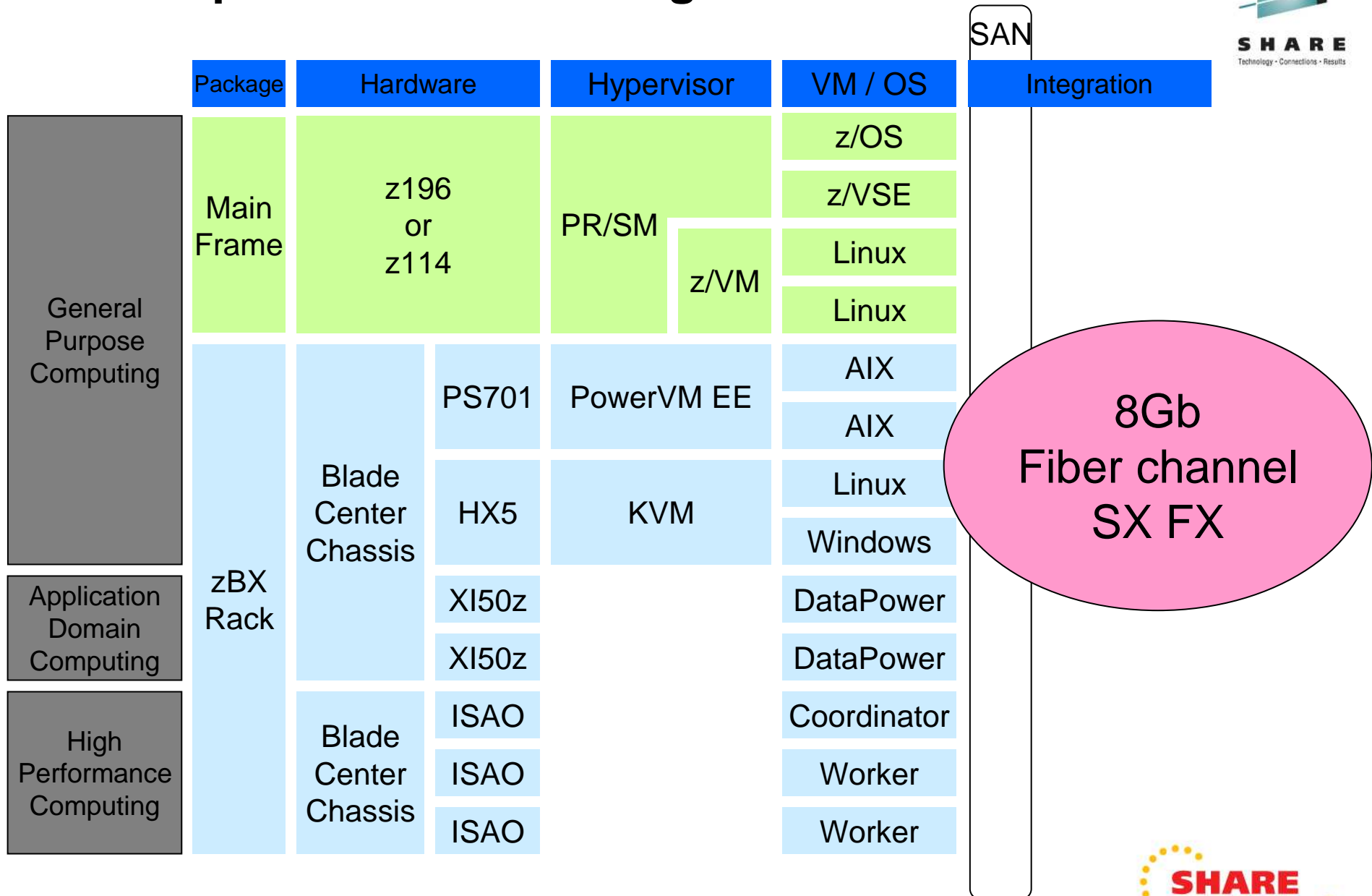
DB2 Eclipse Data Studio Dev Tool

Evaluation and Sizing Analyzers

Topics



zEnterprise Server – Storage Area Network

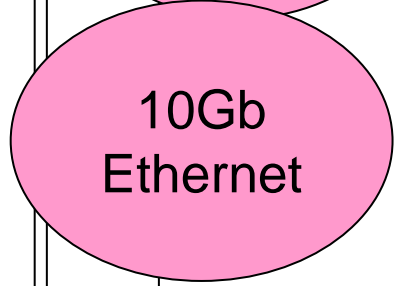
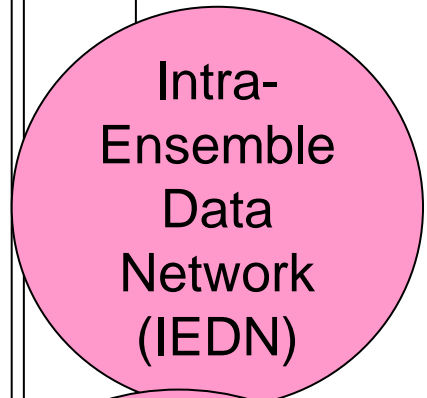
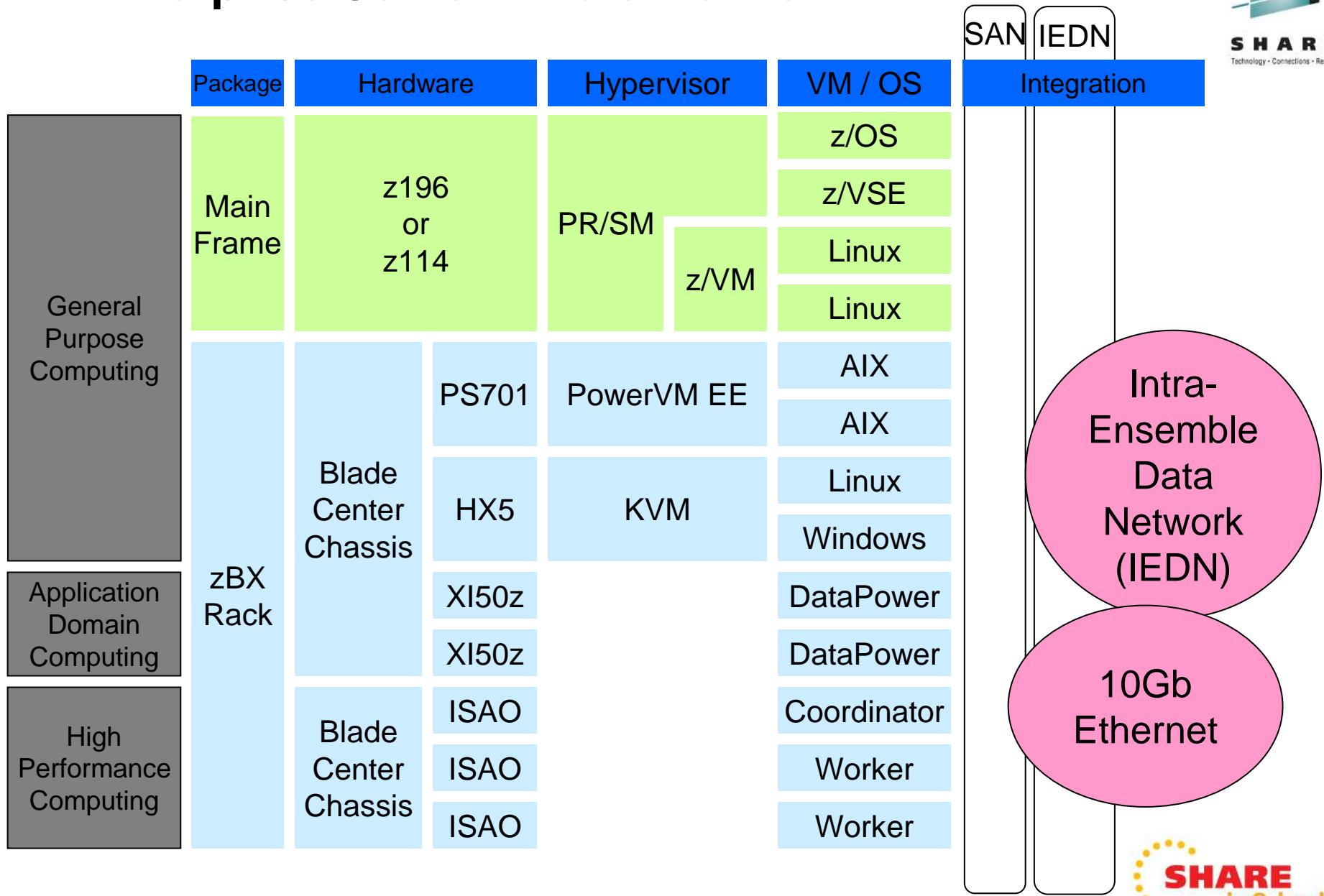


8Gb
Fiber channel
SX FX

Topics



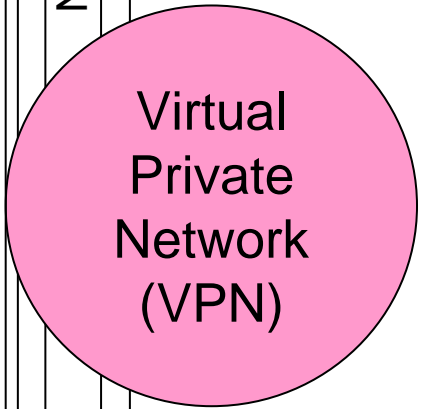
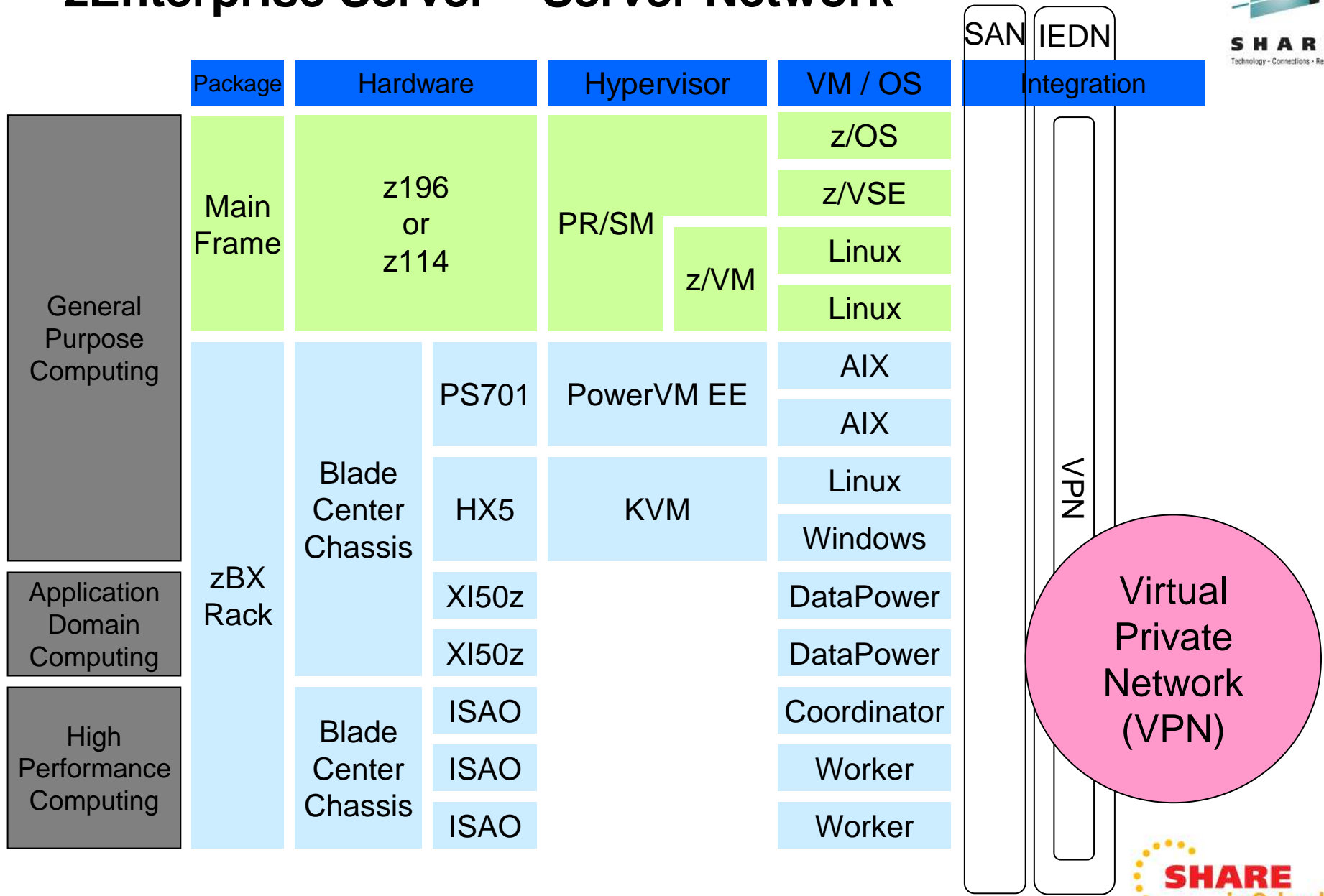
zEnterprise Server – Data Network



Topics



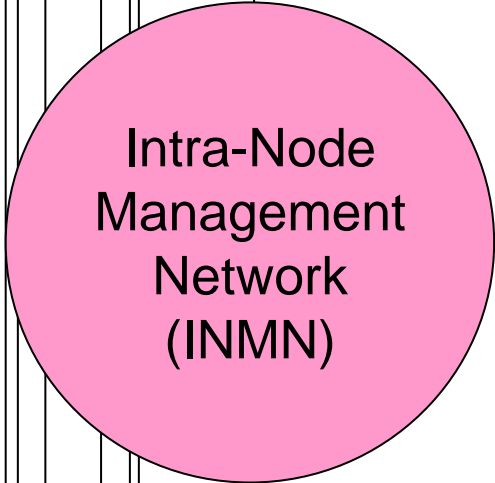
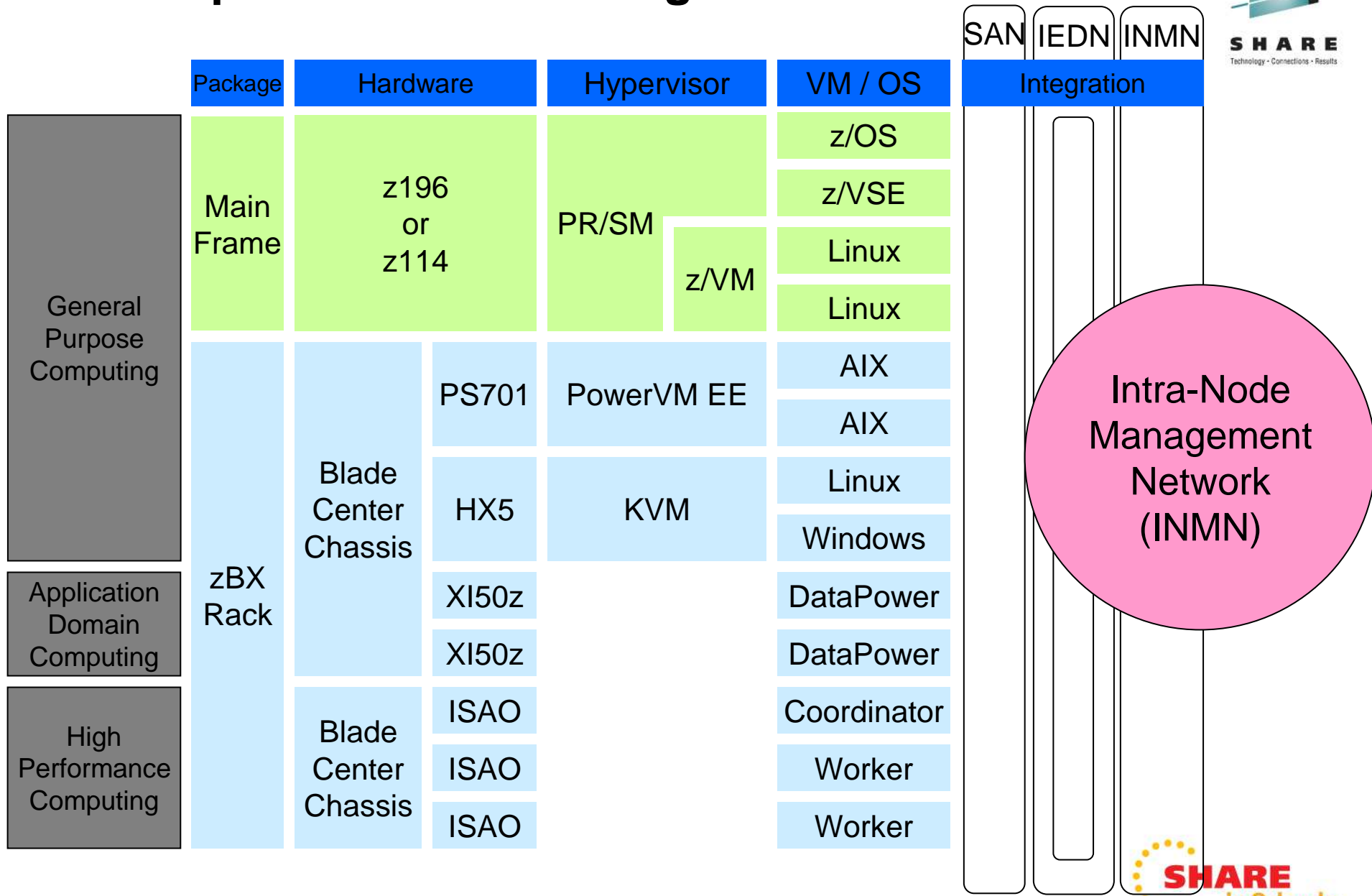
zEnterprise Server – Server Network



Topics



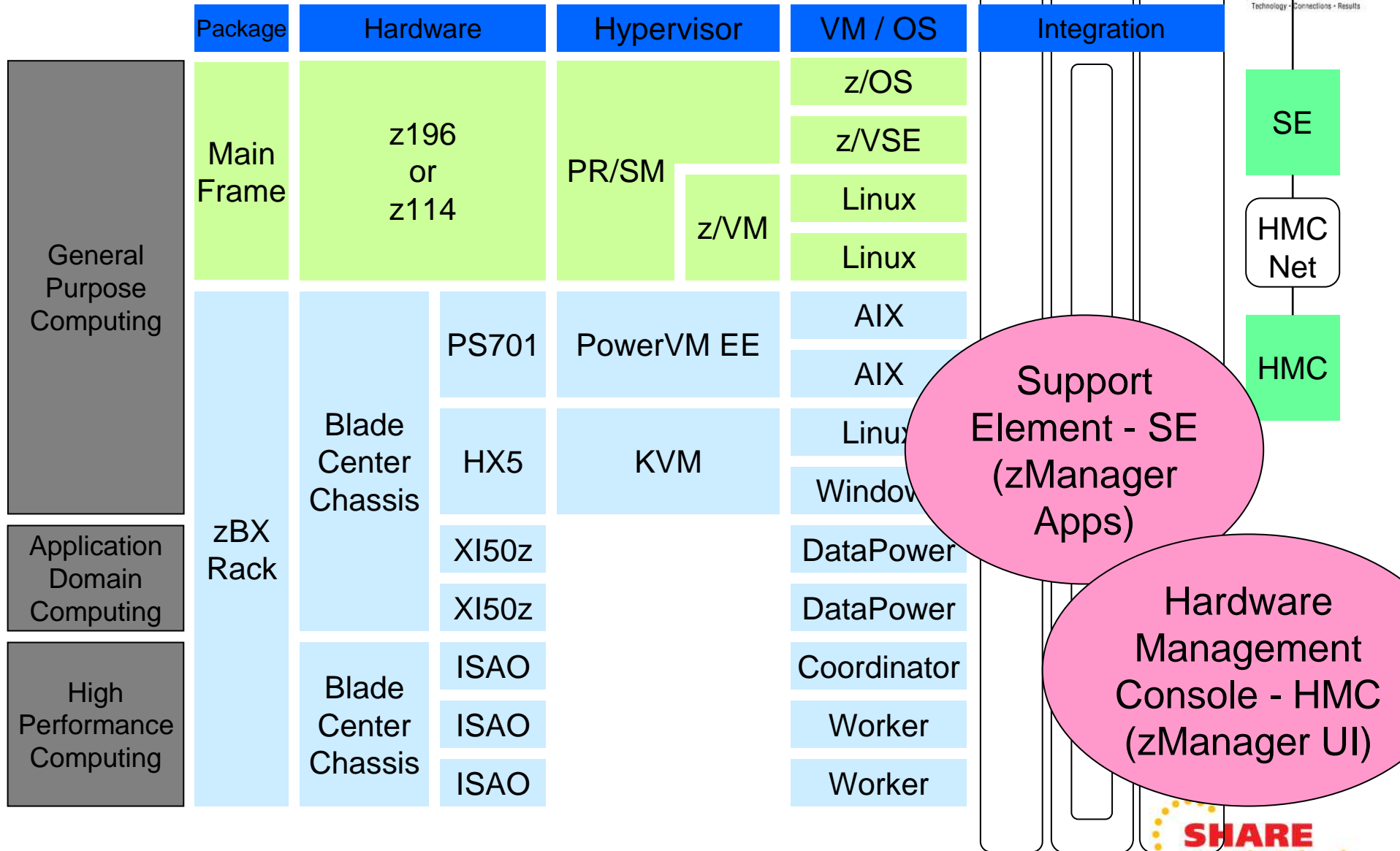
zEnterprise Server – Management Network



Topics

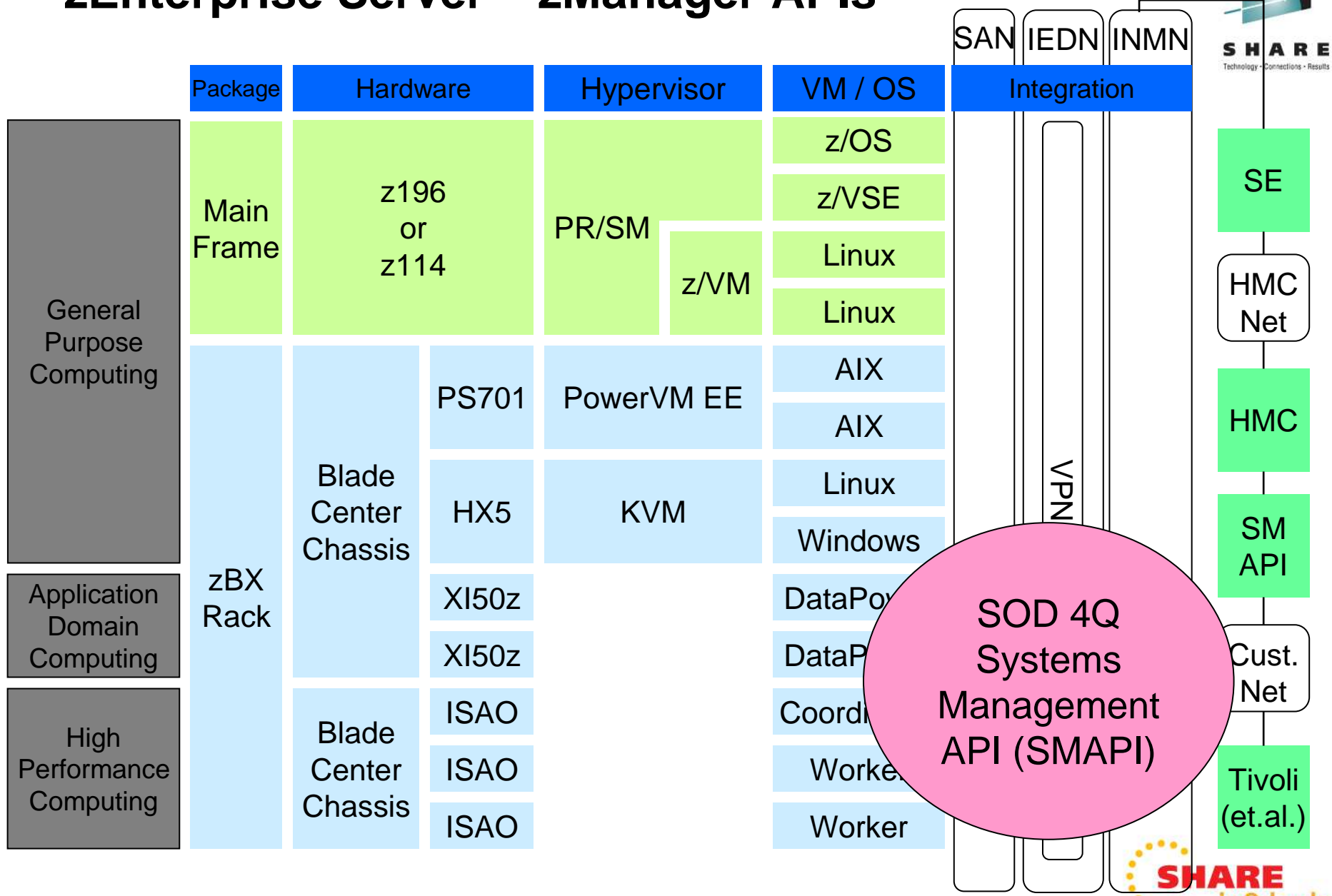


zEnterprise Server - zManager



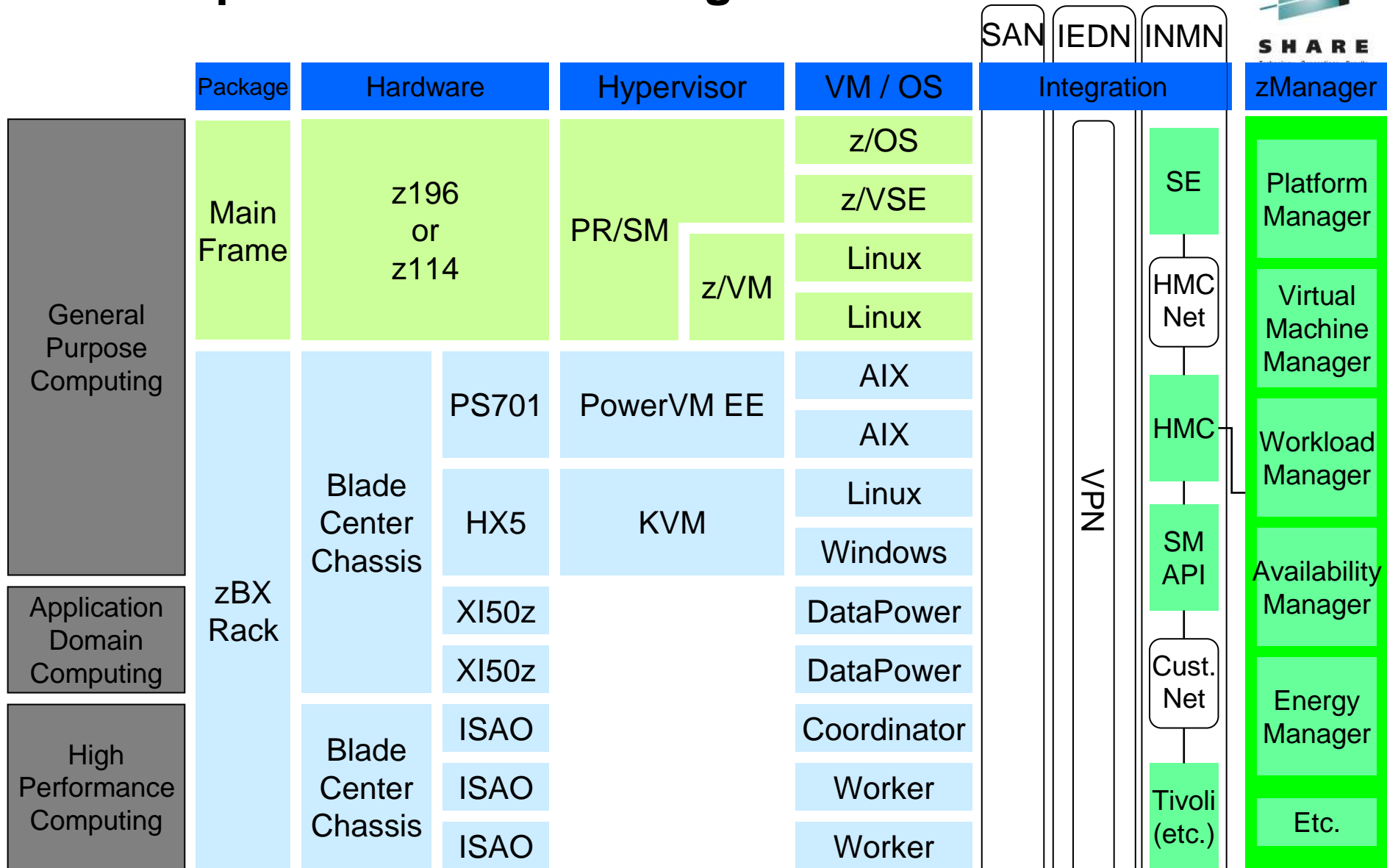
Topics

zEnterprise Server – zManager APIs



Topics

zEnterprise Server – zManager Functions

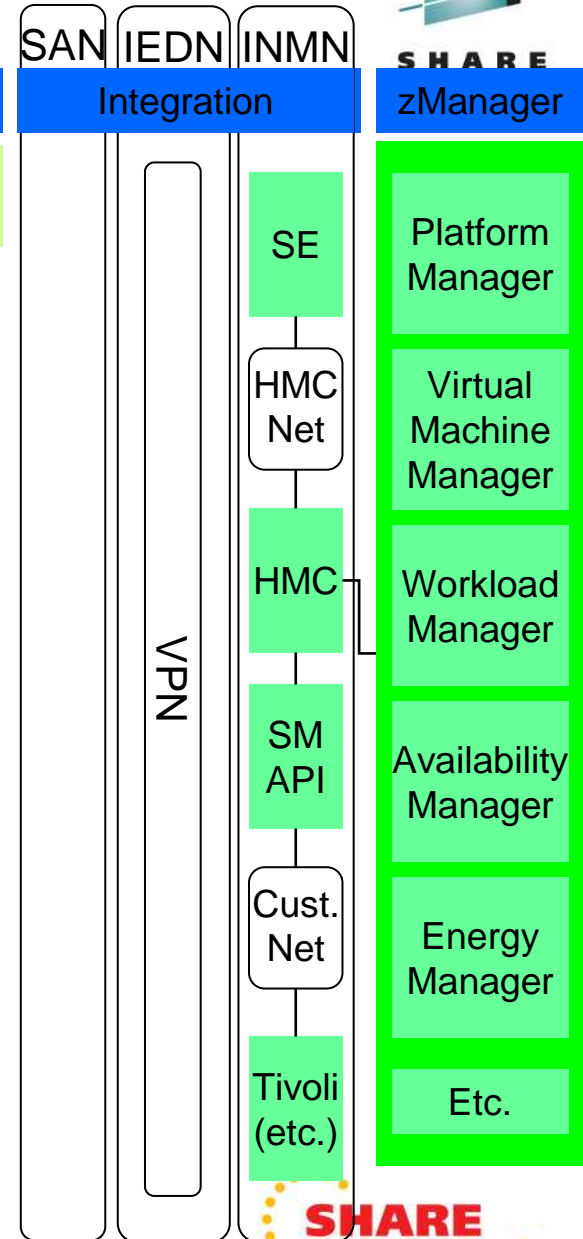


Topics

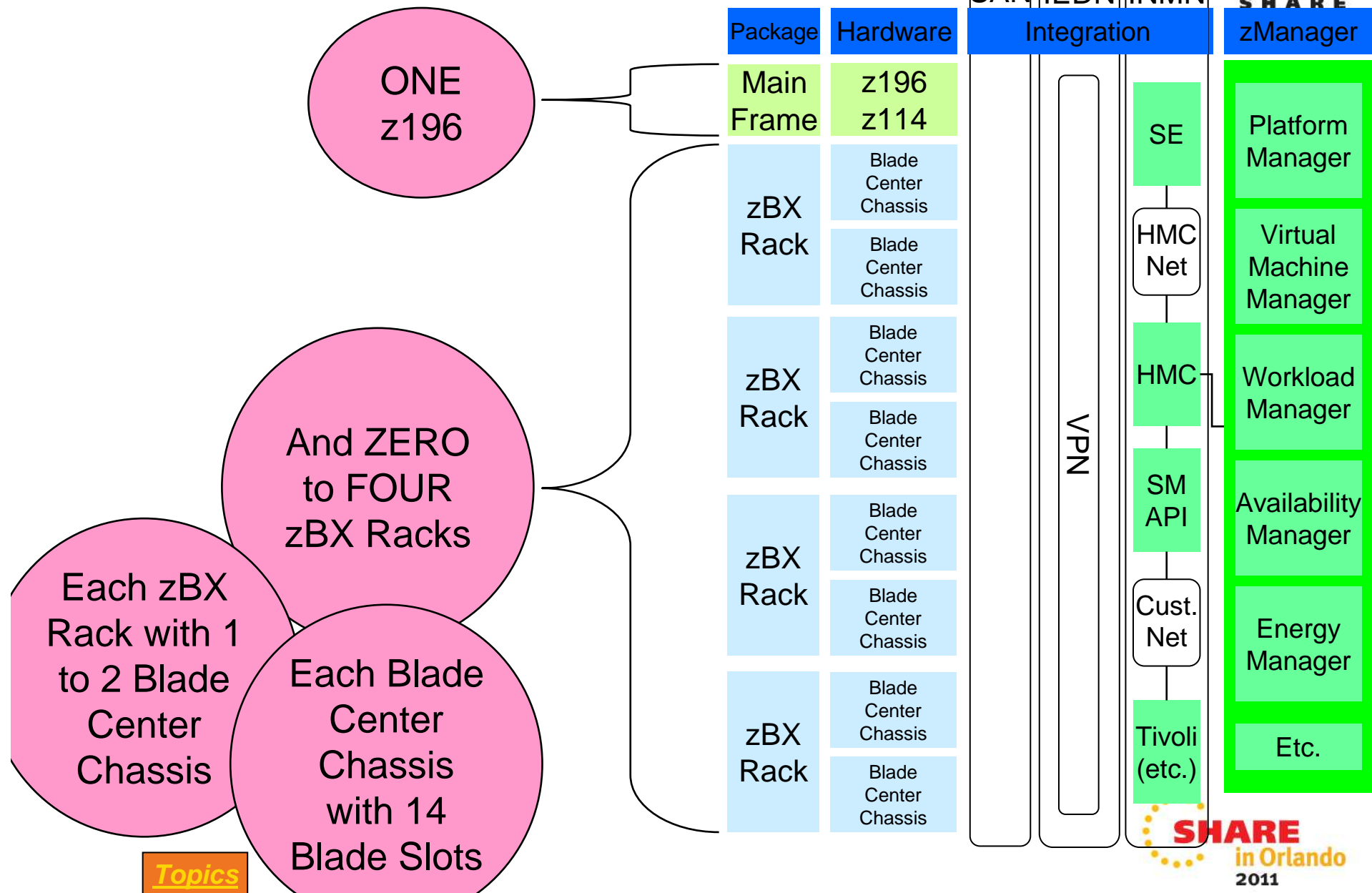
zEnterprise Node

ONE z196
(per node)

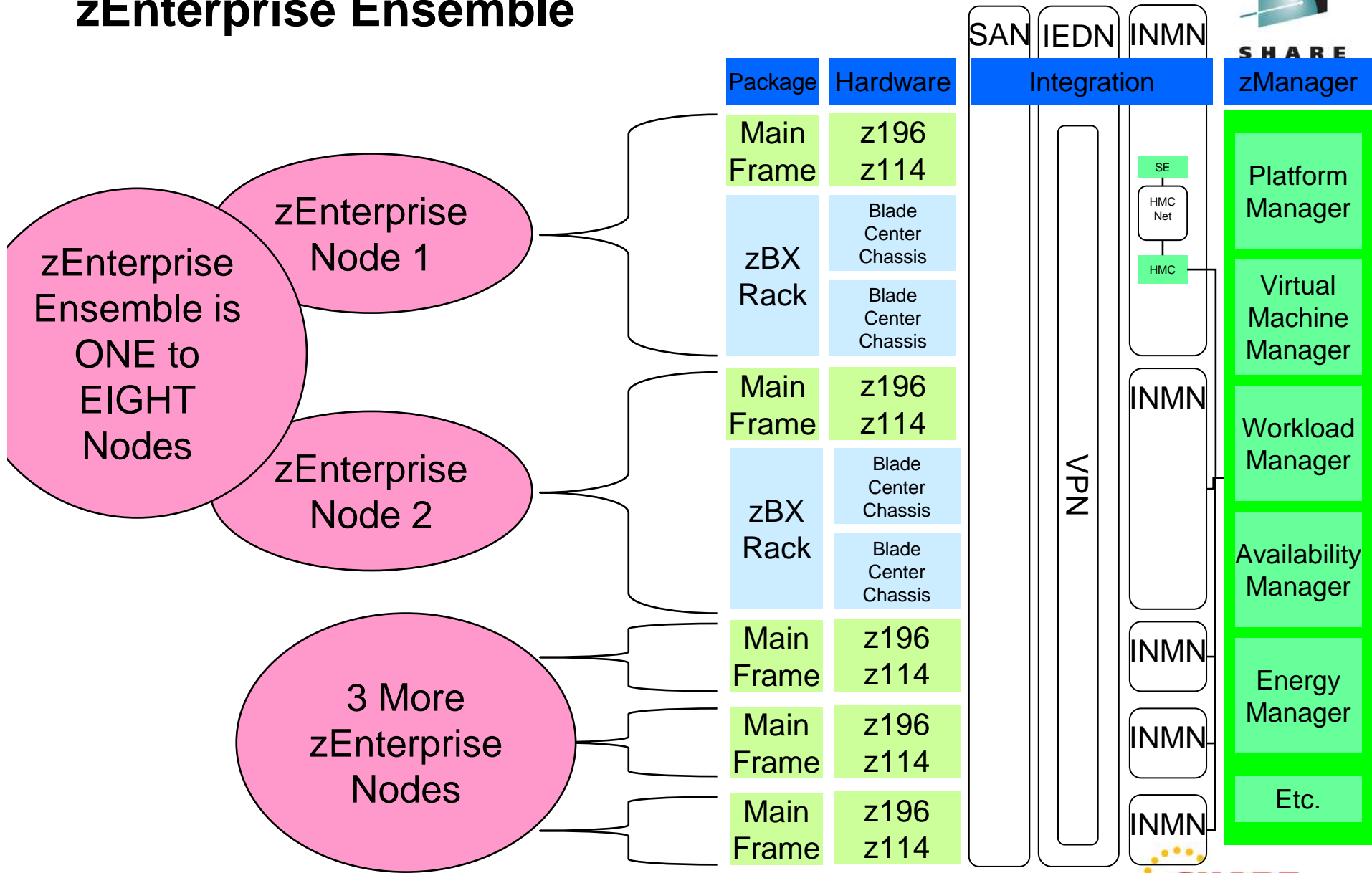
Package	Hardware
Main Frame	z196 z114



zEnterprise Node



zEnterprise Ensemble



End of Section



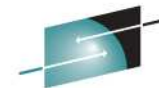
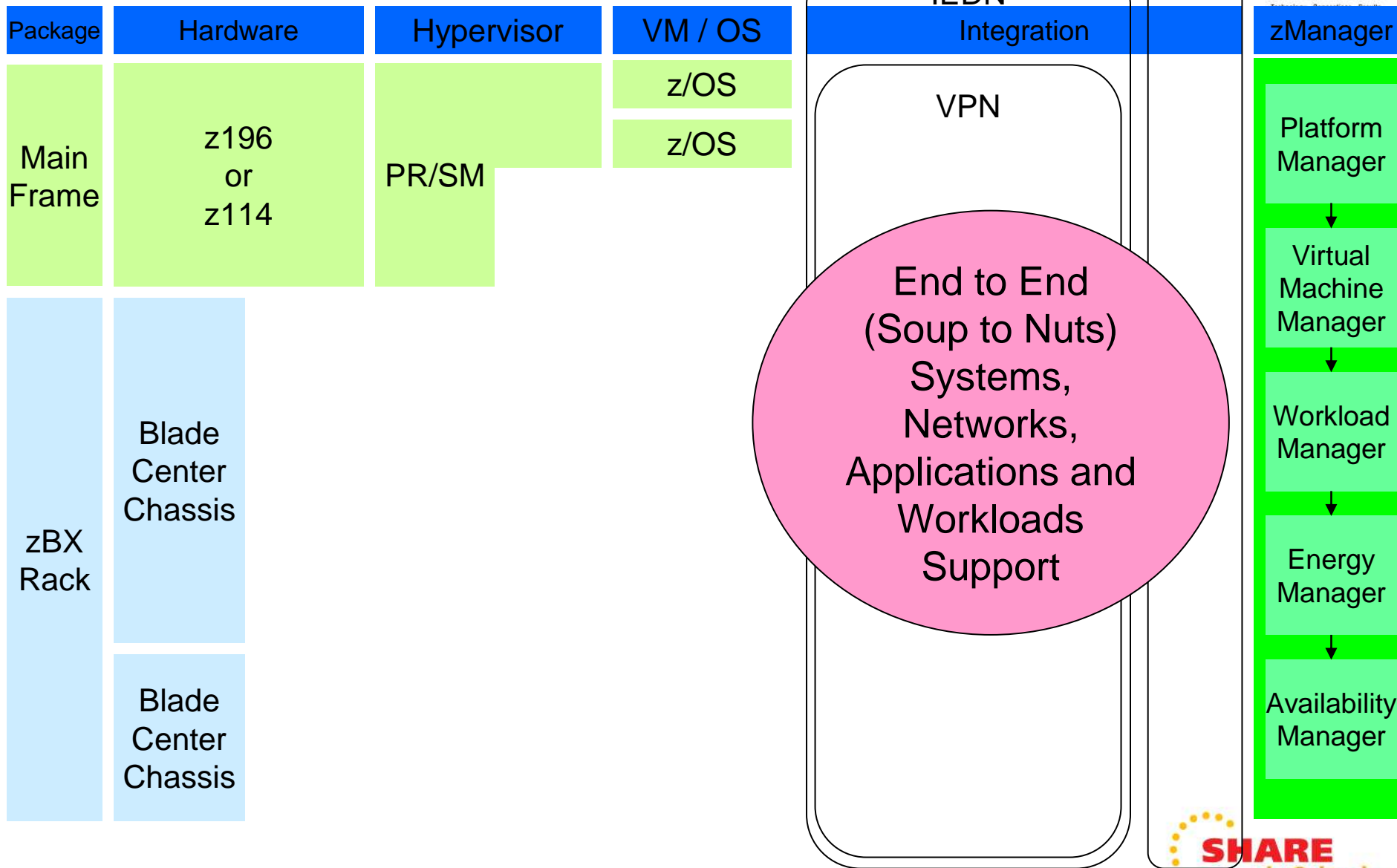
zManager



Topics



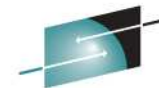
zManager



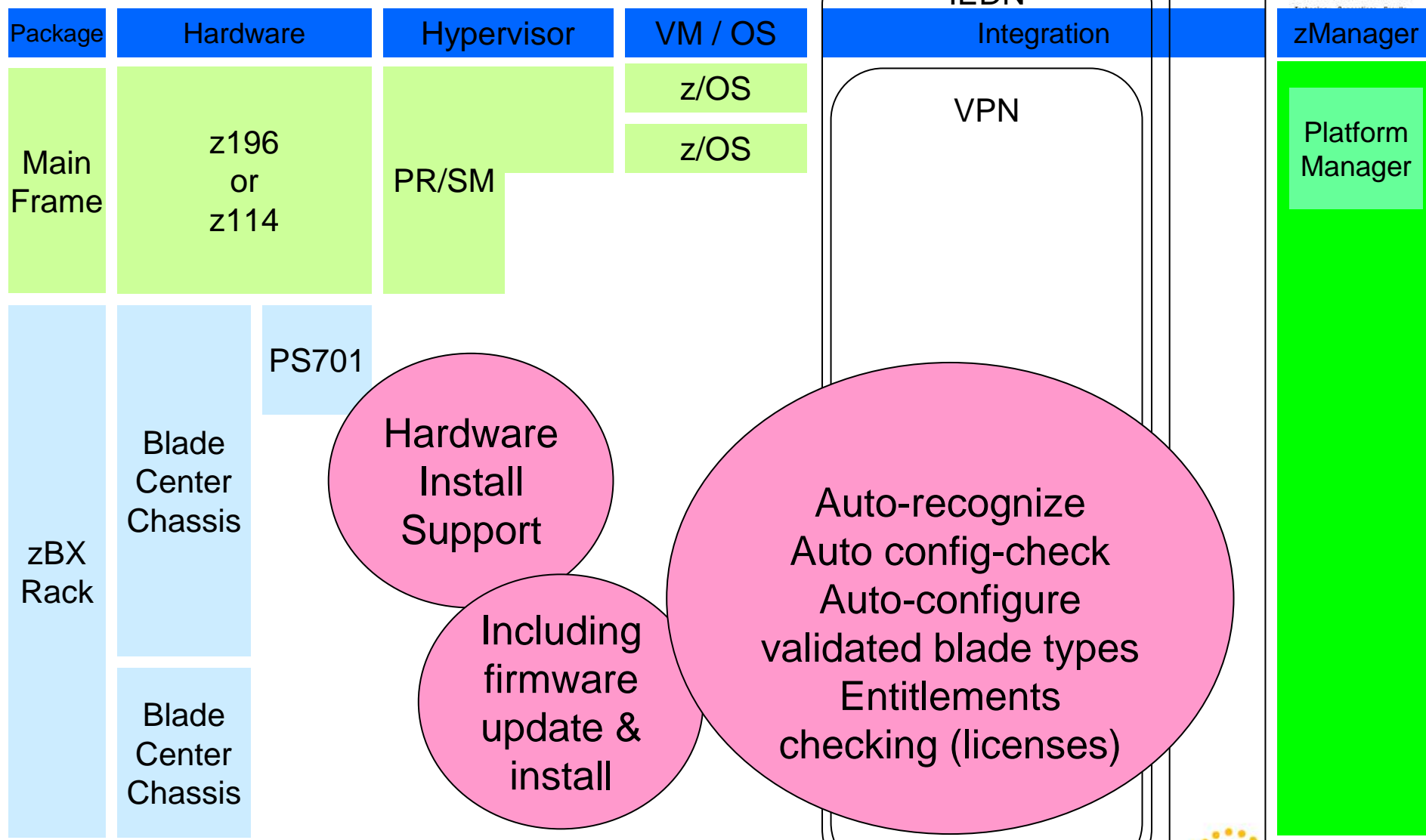
SHARE

Topics

zManager



SHARE



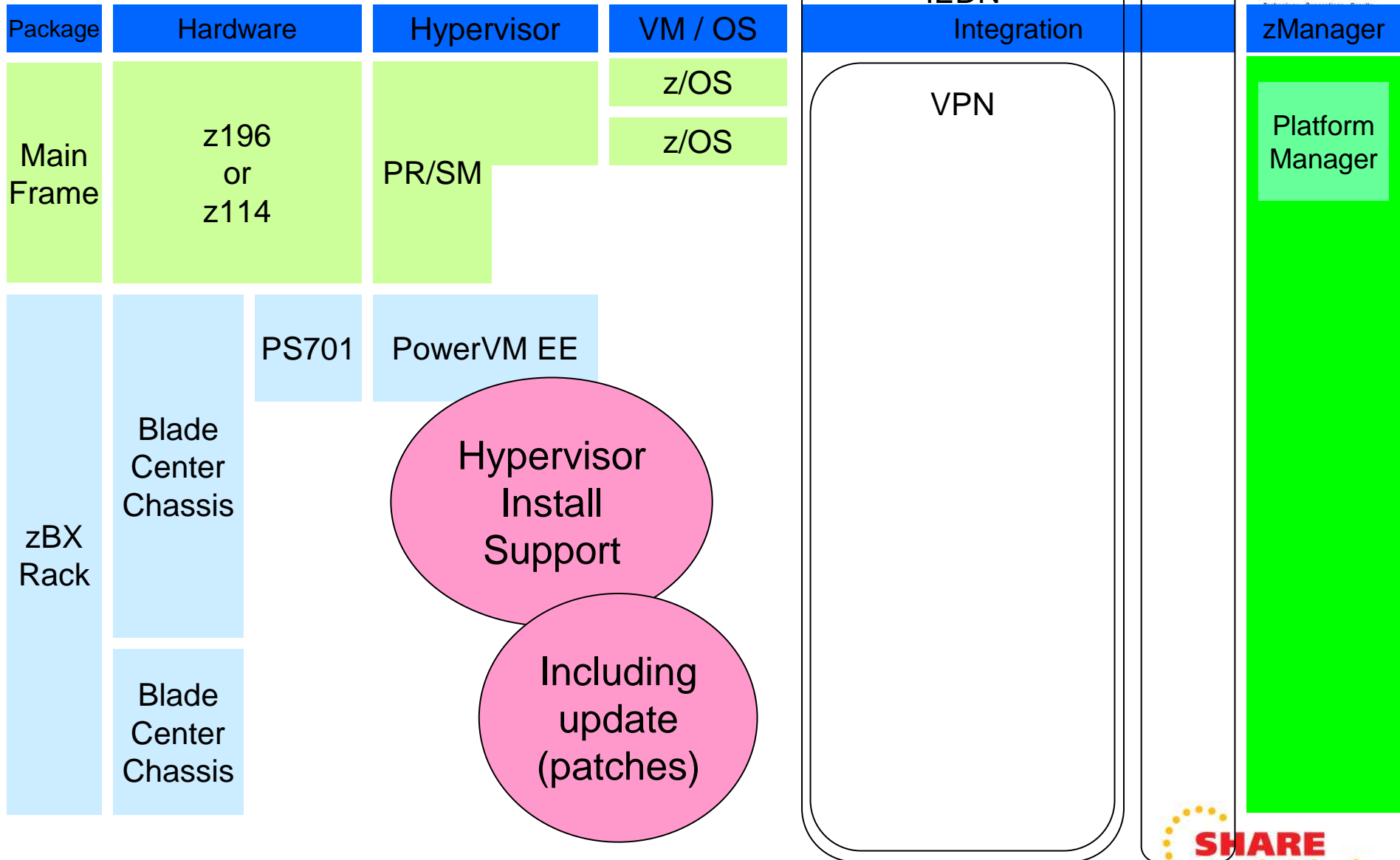
Platform Manager

Topics

zManager – Platform Management

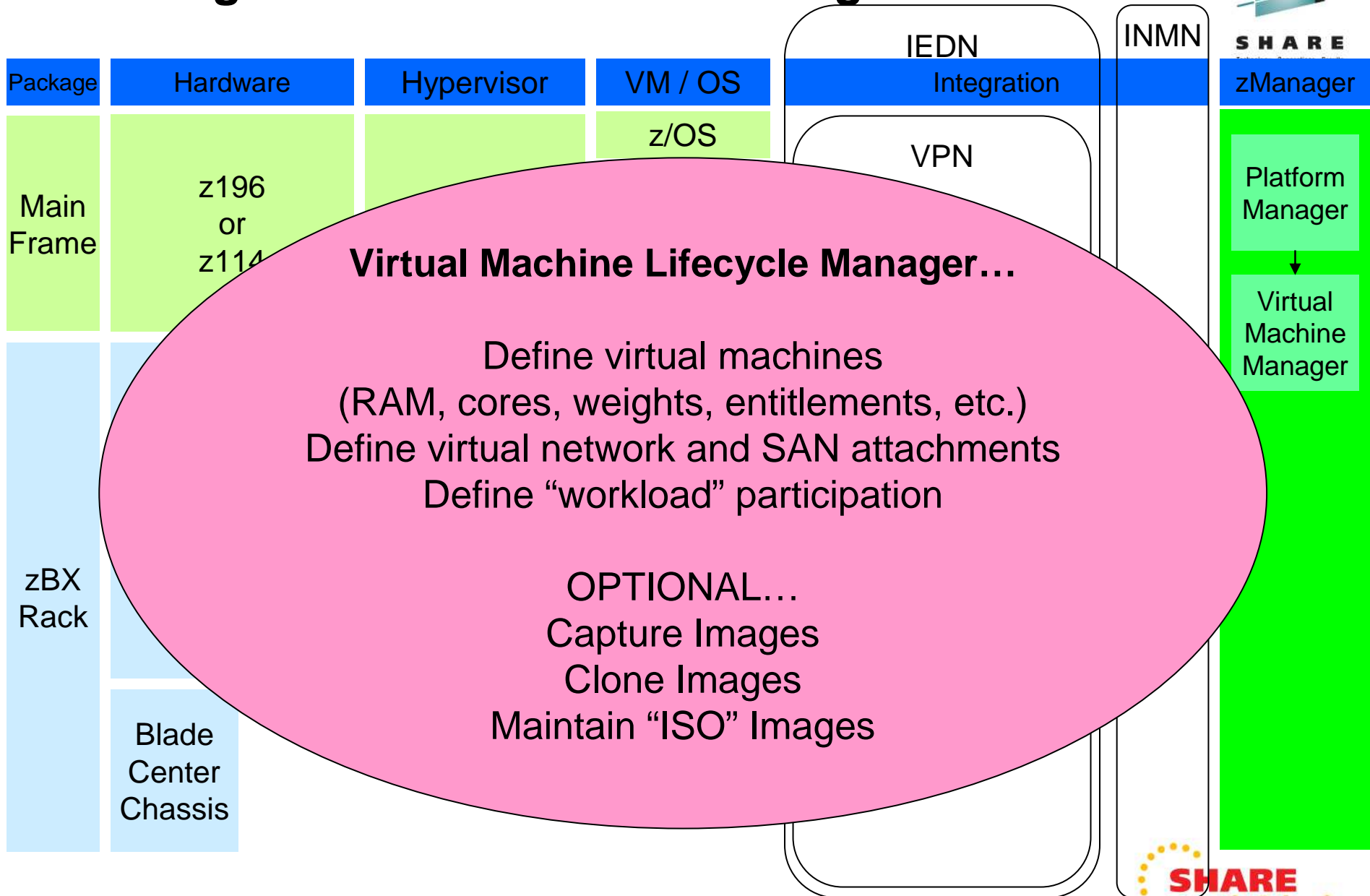
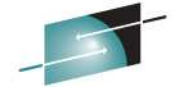


SHARE



Topics

zManager – Virtual Machine Management



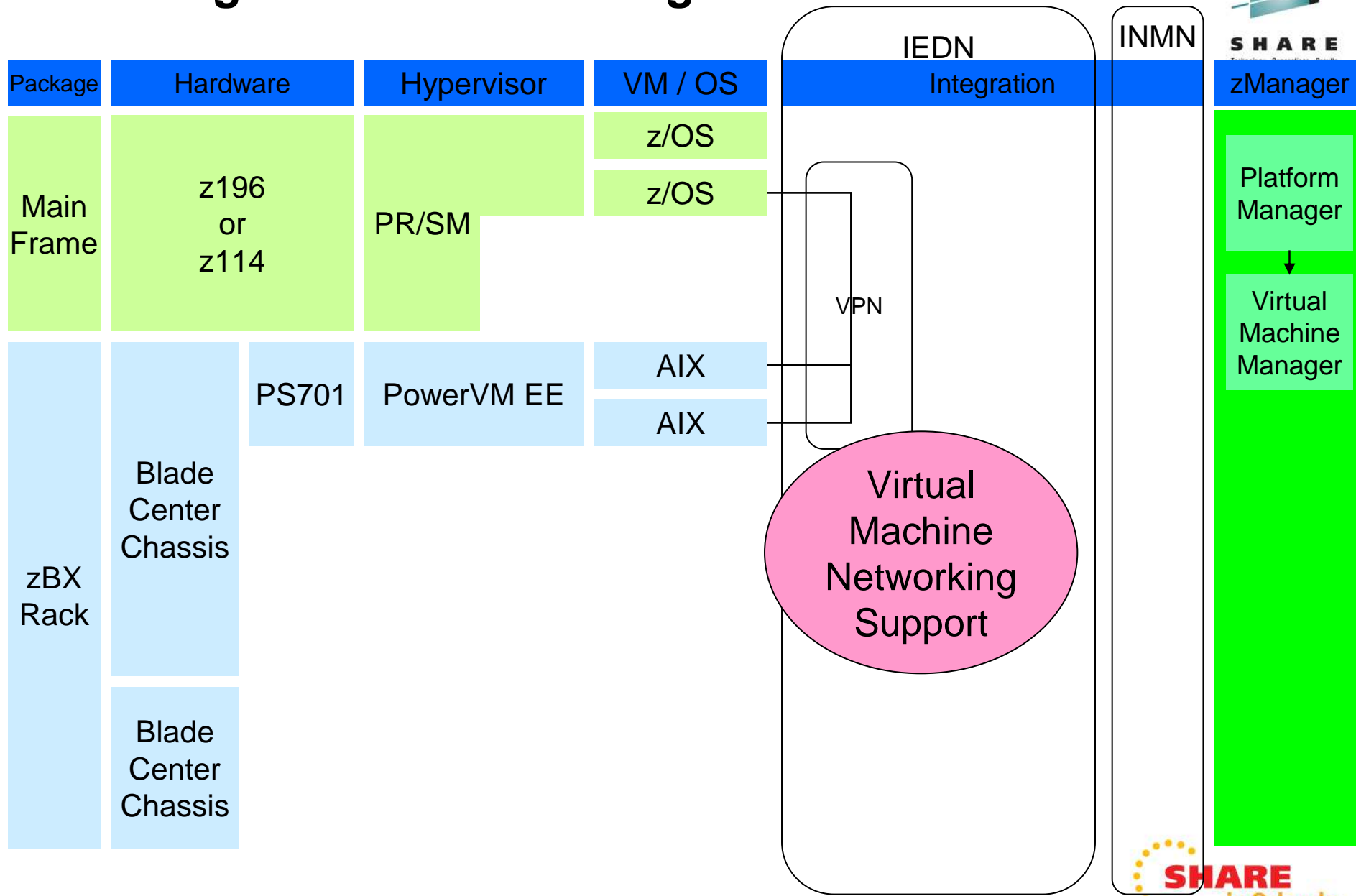
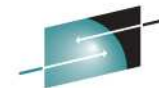
Virtual Machine Lifecycle Manager...

Define virtual machines
(RAM, cores, weights, entitlements, etc.)
Define virtual network and SAN attachments
Define “workload” participation

OPTIONAL...
Capture Images
Clone Images
Maintain “ISO” Images

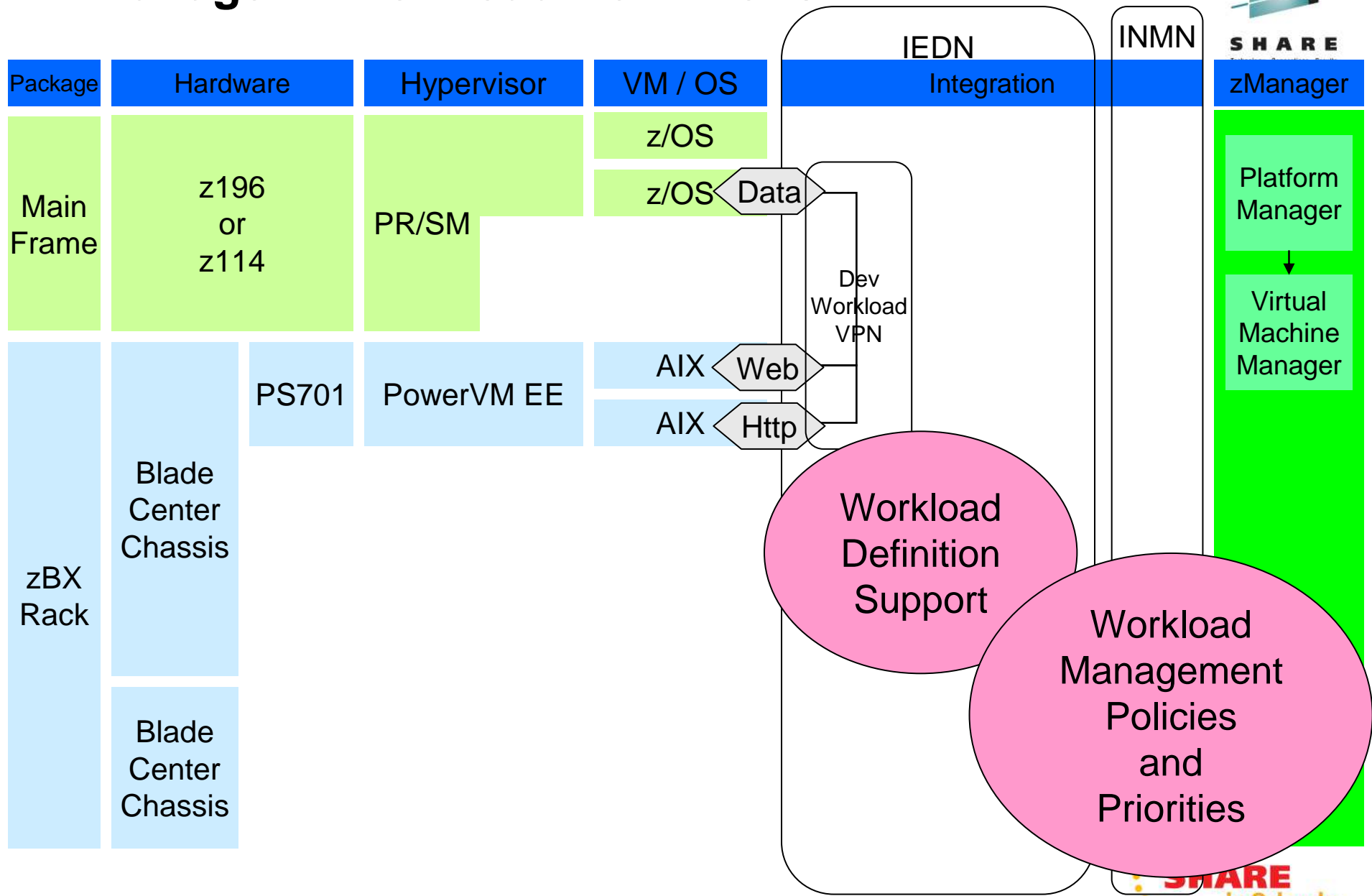
Topics

zManager – Network Management



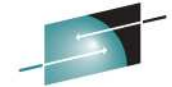
Topics

zManager – Workload Definitions

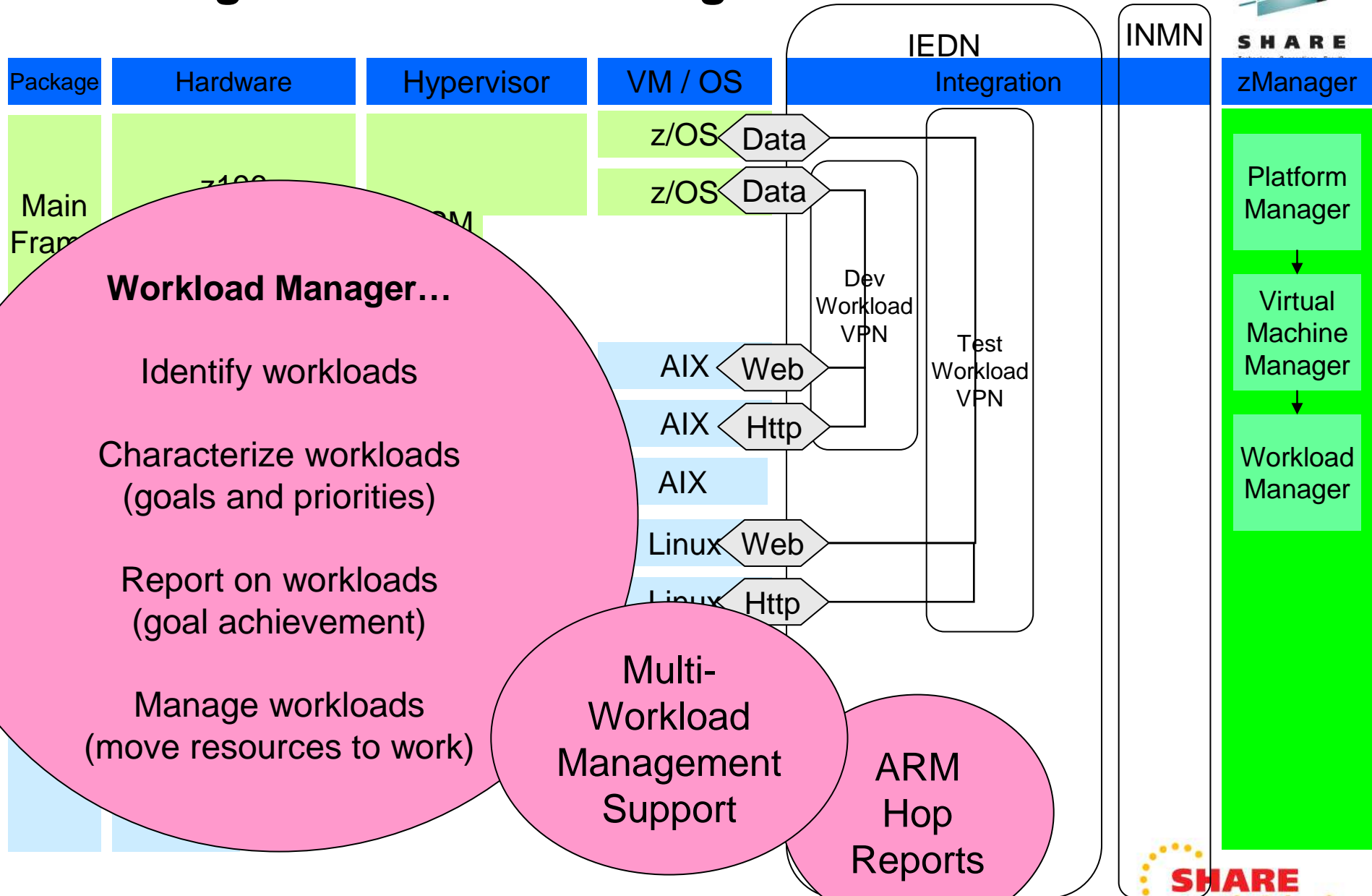


Topics

zManager – Workload Management

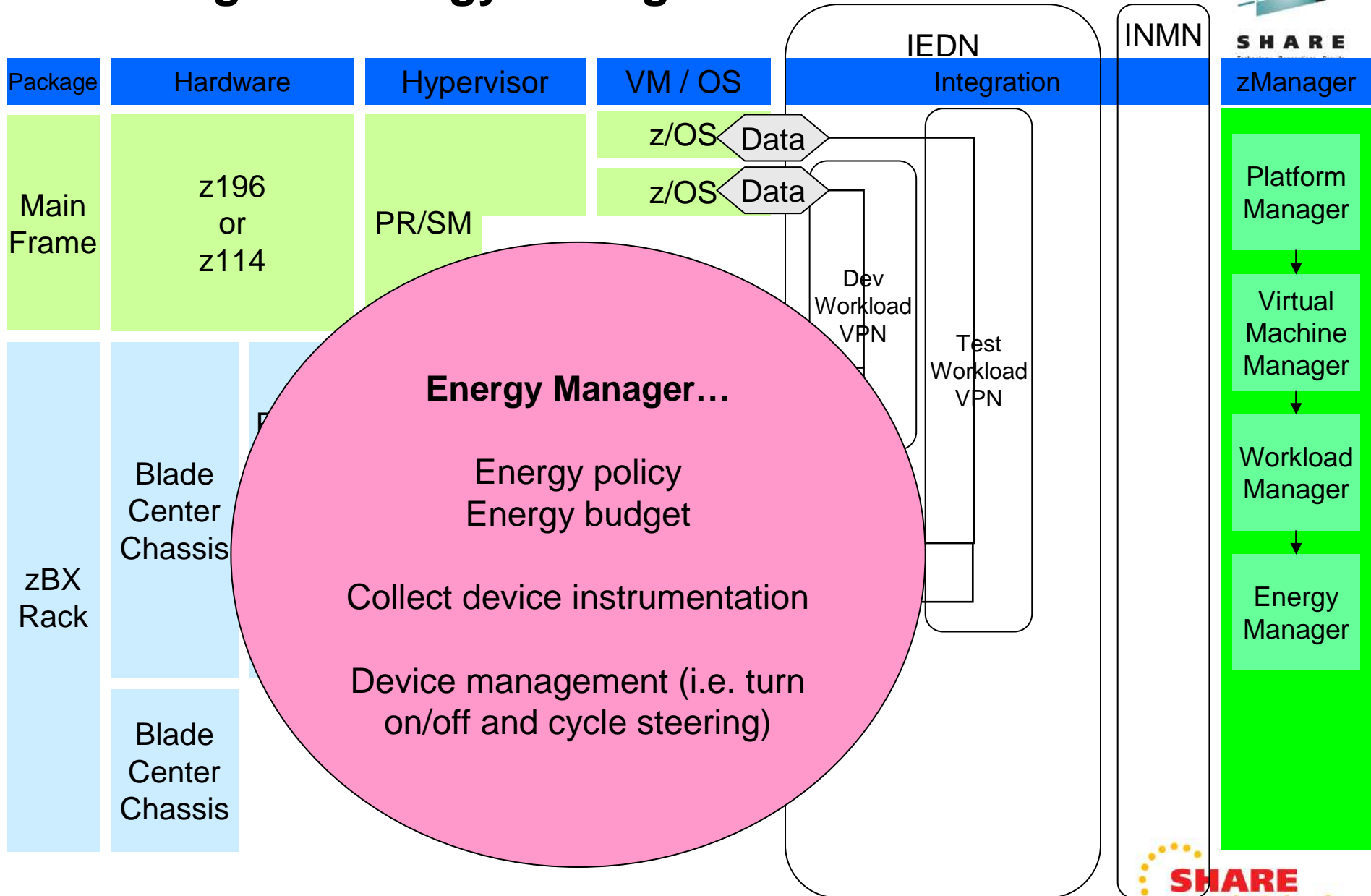


SHARE



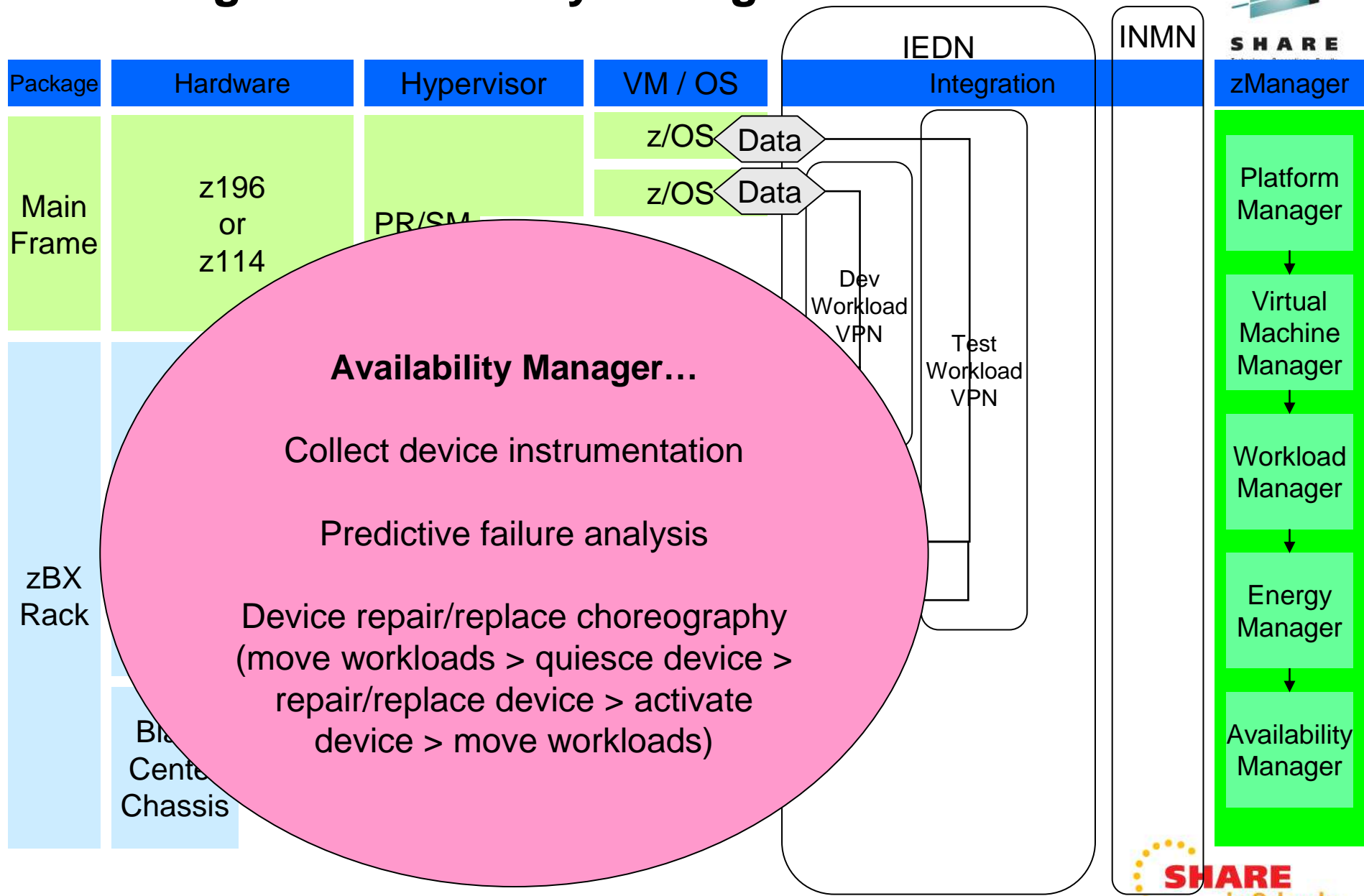
Topics

zManager – Energy Manager



Topics

zManager – Availability Manager



Availability Manager...

Collect device instrumentation

Predictive failure analysis

Device repair/replace choreography
(move workloads > quiesce device > repair/replace device > activate device > move workloads)

Topics

End of Section



Use Cases



Topics



Use Cases

1. Multi-Tier Application Serving with zEnterprise Linux on System z
2. Multi-Tier Application Serving with zEnterprise POWER/AIX and z/OS
3. Real-Time Analytics with zEnterprise and IBM Smart Analytics Optimizer (ISAO)
4. Hardware ESB with zEnterprise and DataPower XI50z
5. Storm Cloud
6. Parallel Batch
7. Combinatorial

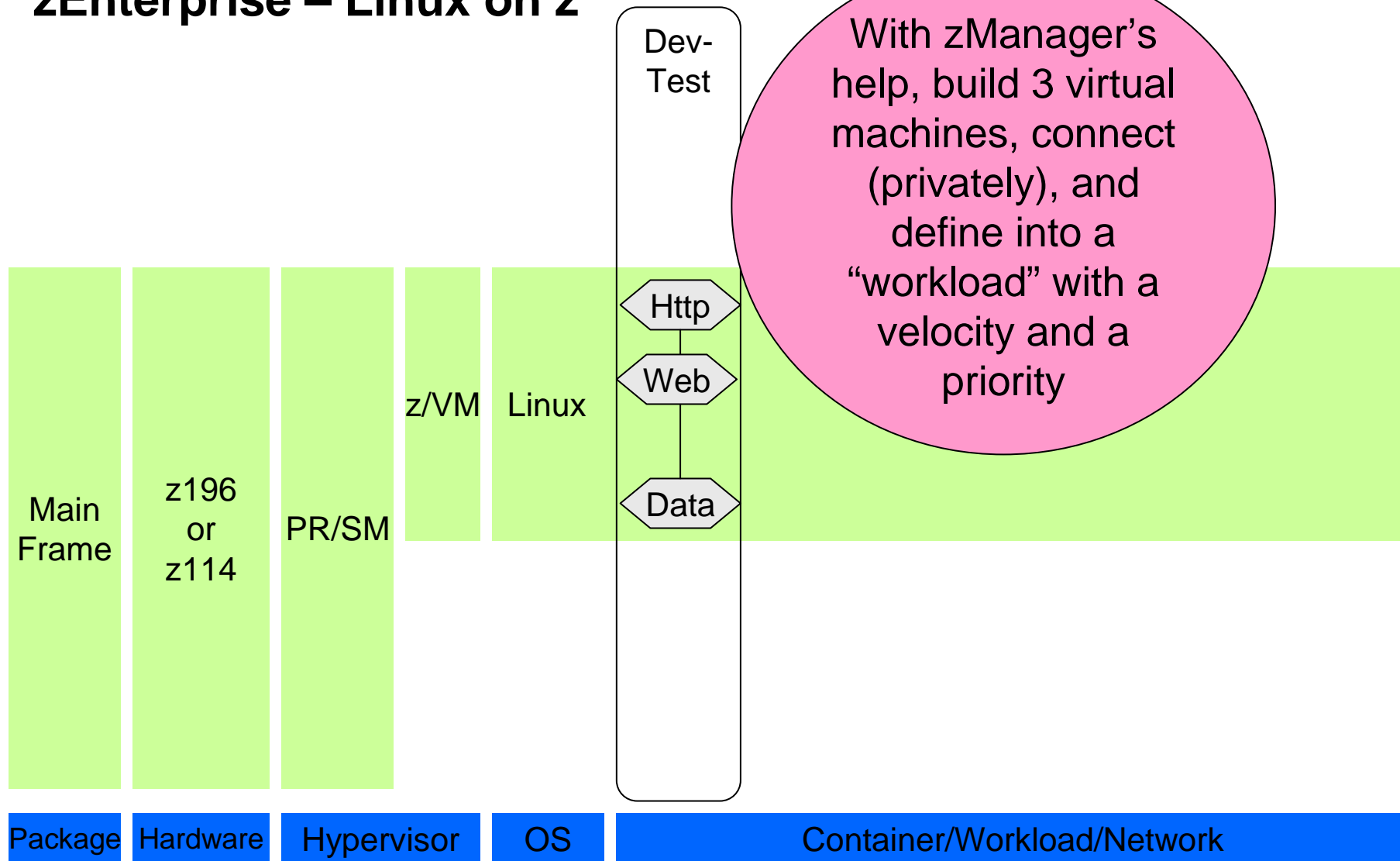
Use Case 1



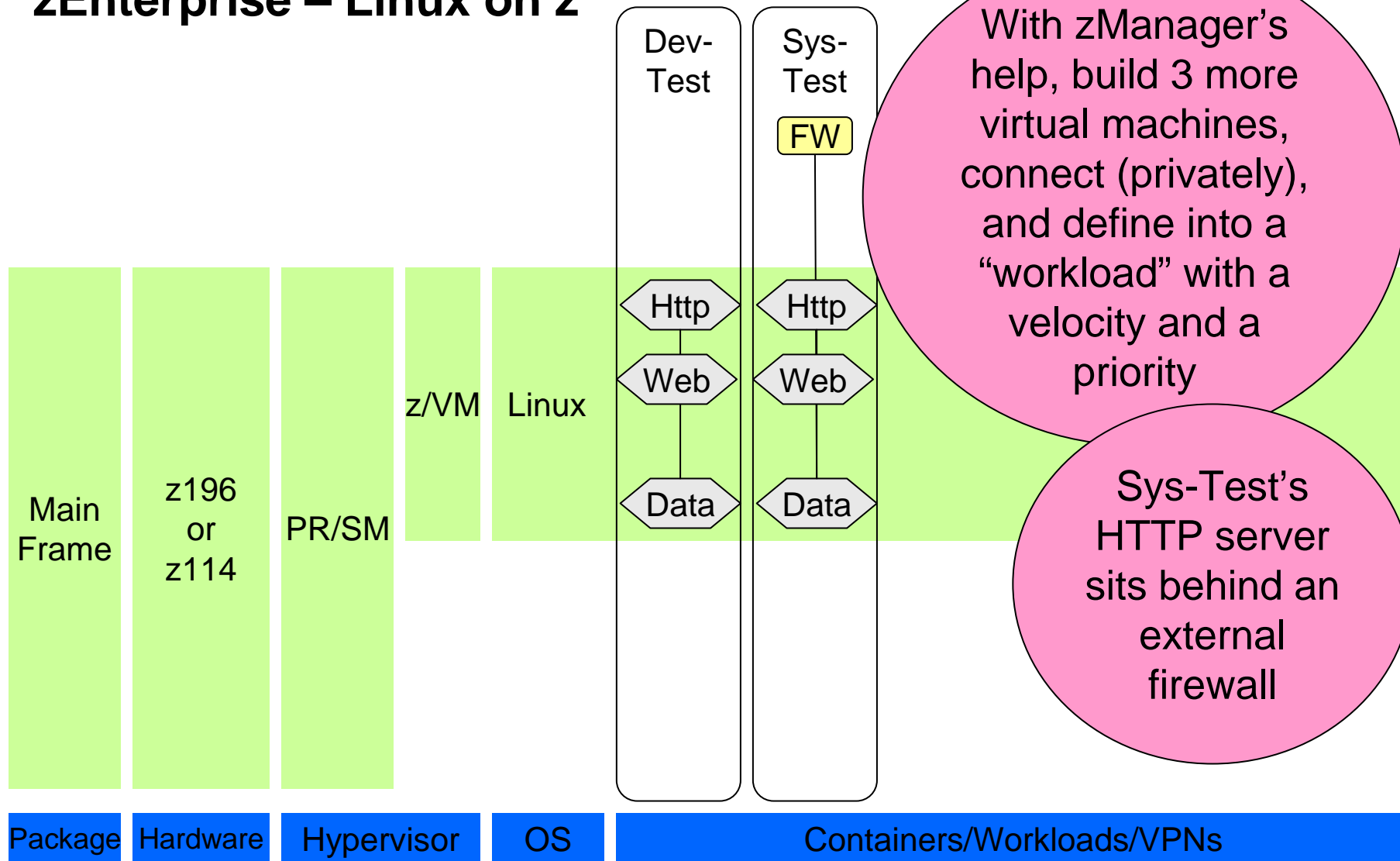
Topics



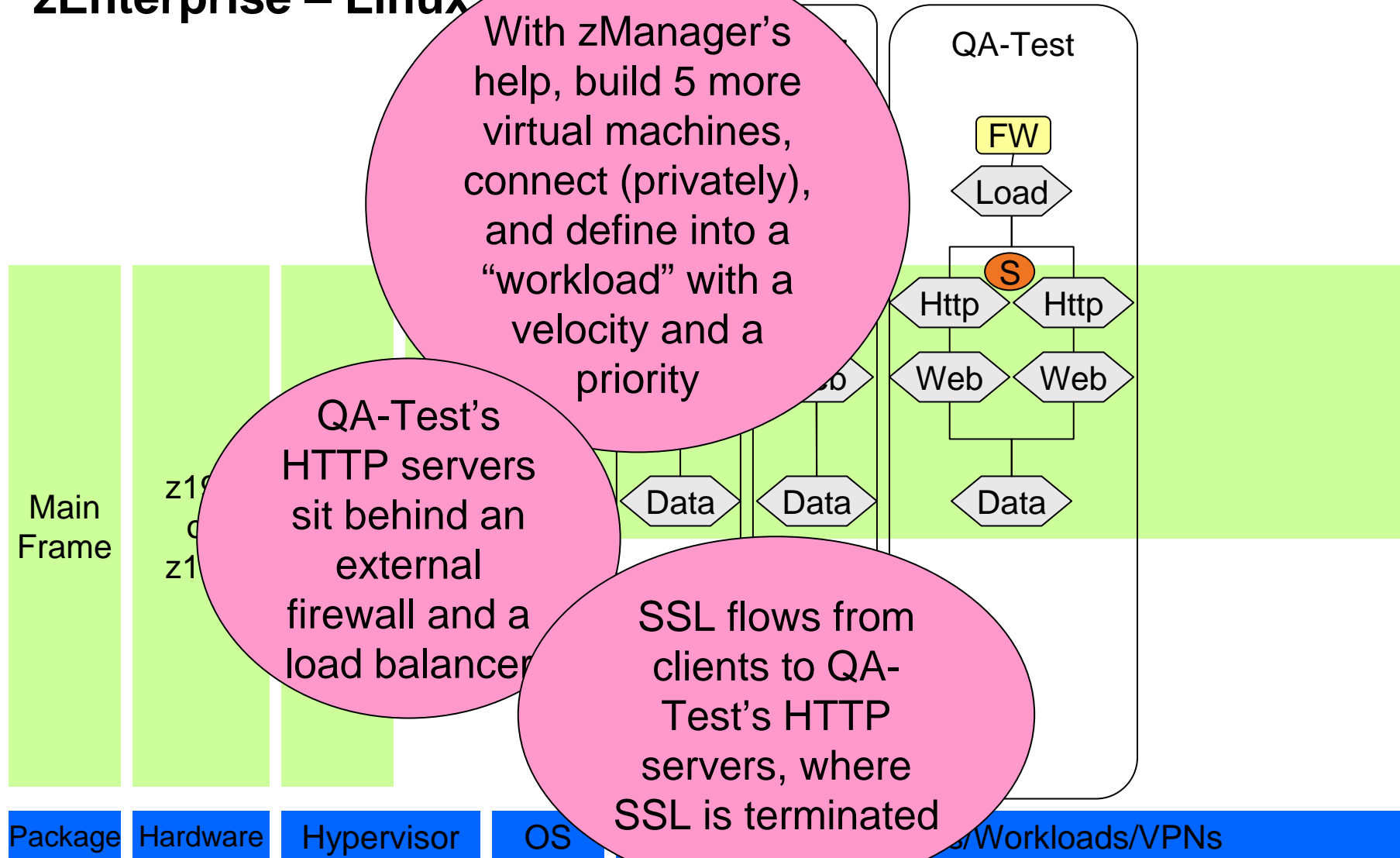
Use Case: Multi-Tier Application zEnterprise – Linux on z



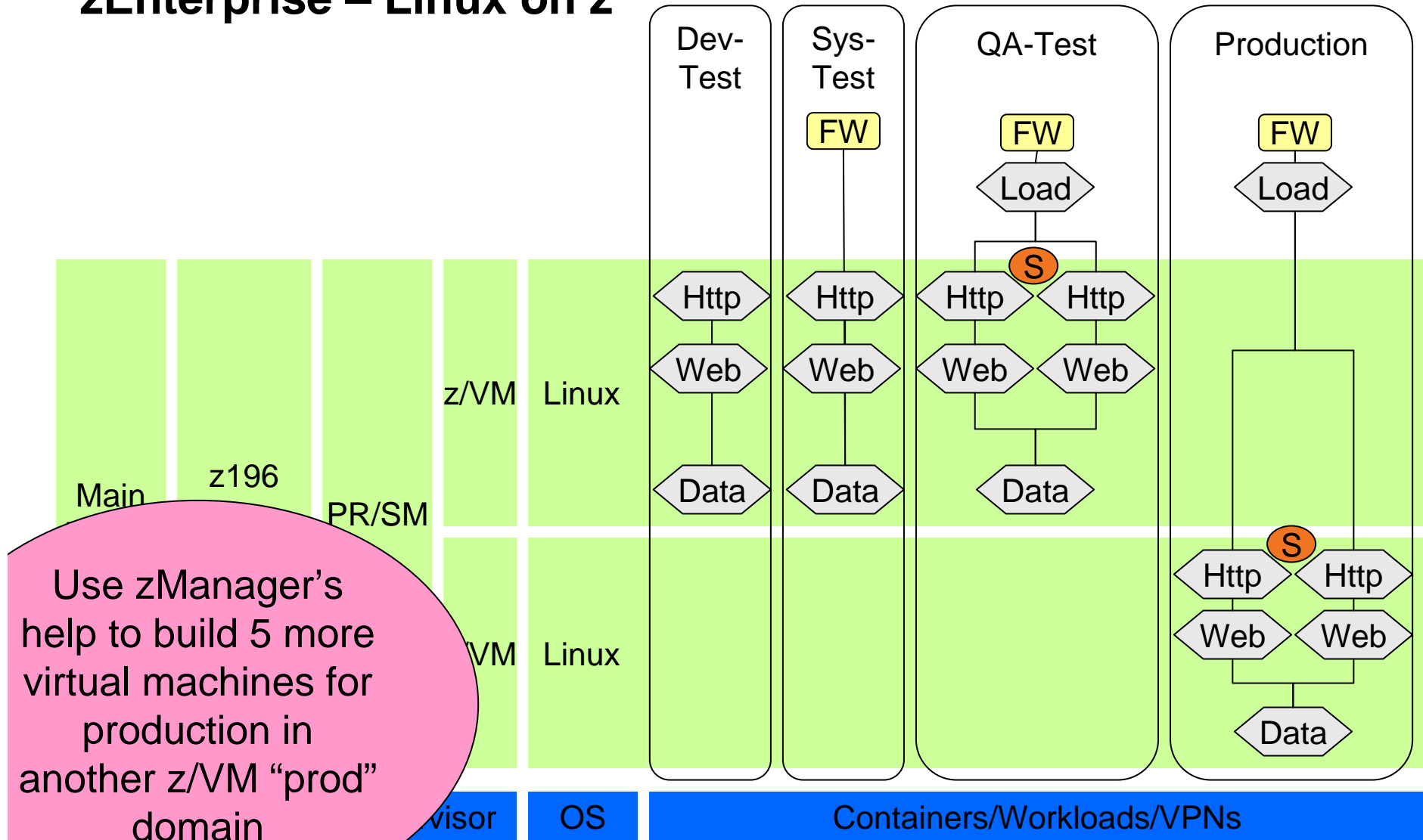
Use Case: Multi-Tier Application zEnterprise – Linux on z



Use Case: Multi-Tier Application zEnterprise – Linux



Use Case: Multi-Tier Application zEnterprise – Linux on z



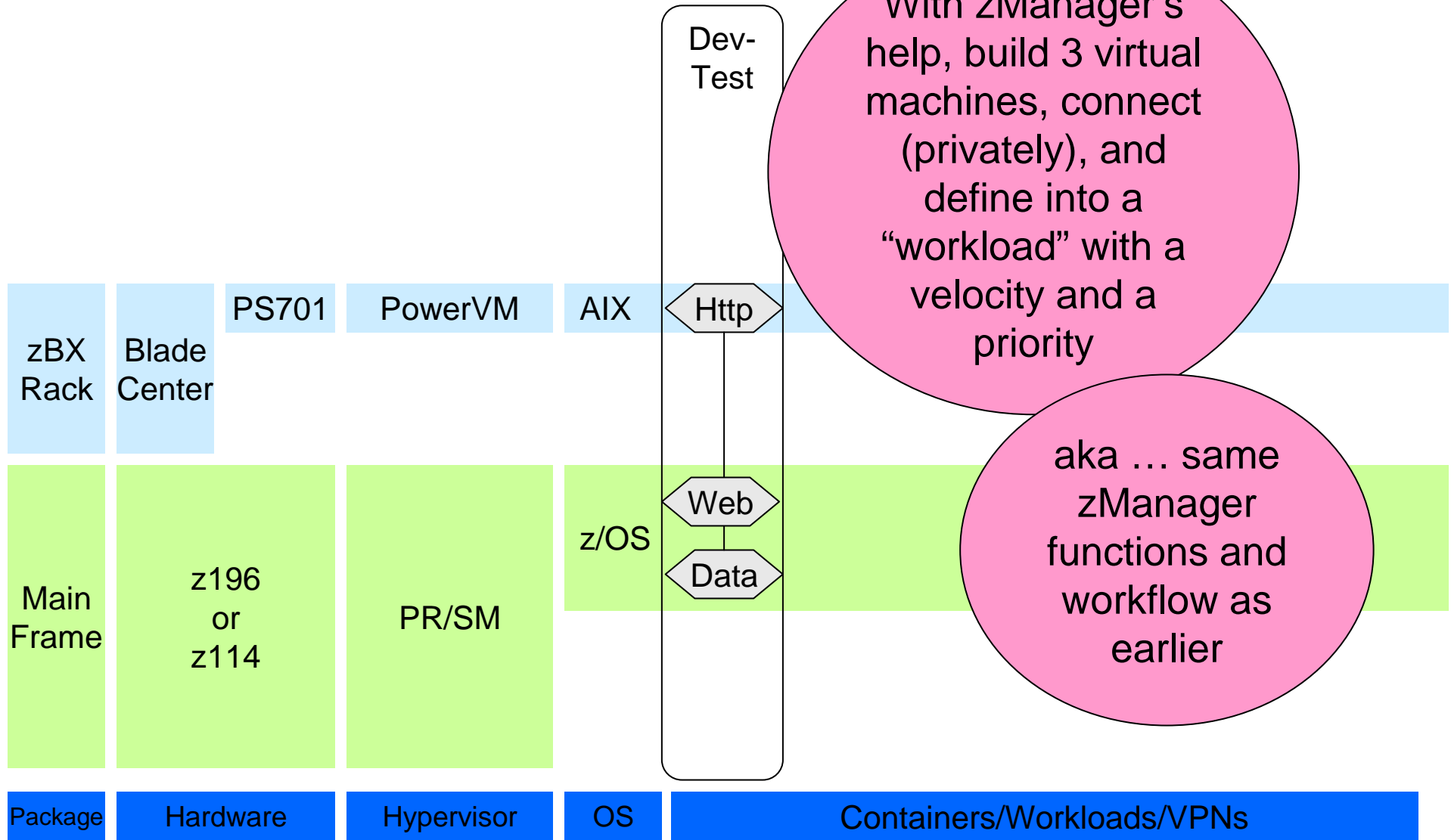
Use Case 2



Topics

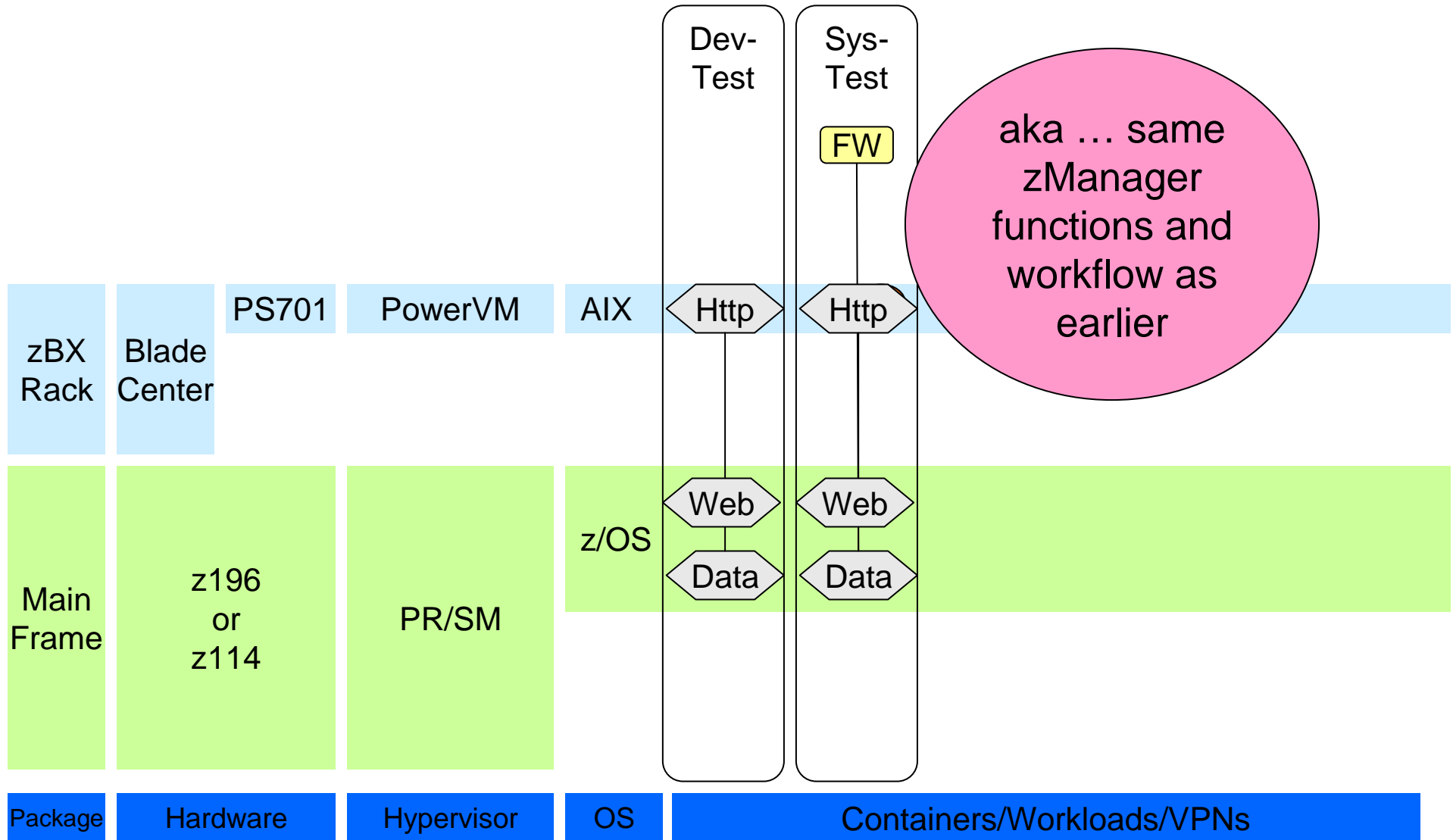


Use Case: Multi-Tier Application zEnterprise – PS701 & z/OS



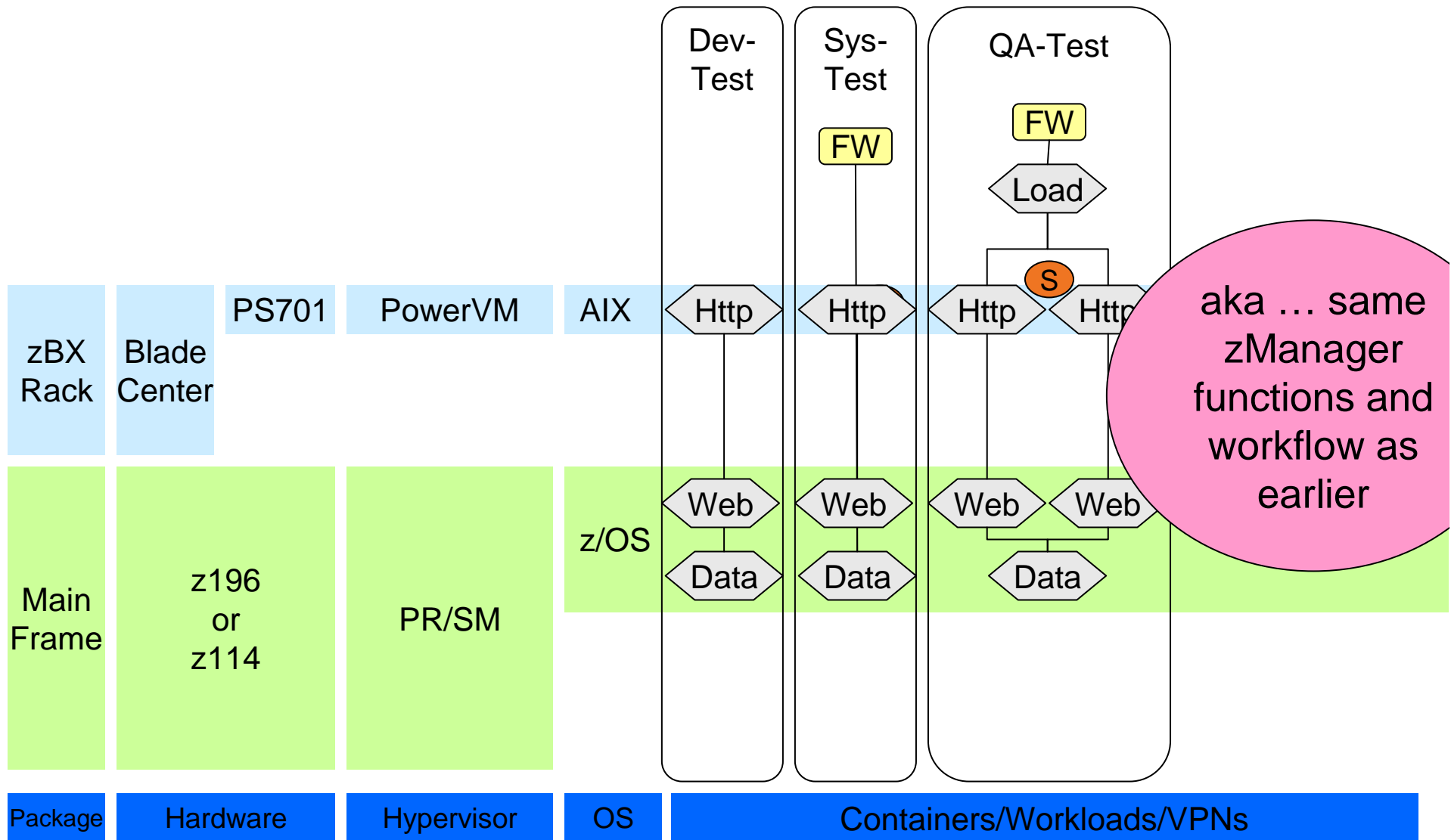
Topics

Use Case: Multi-Tier Application zEnterprise – PS701 & z/OS



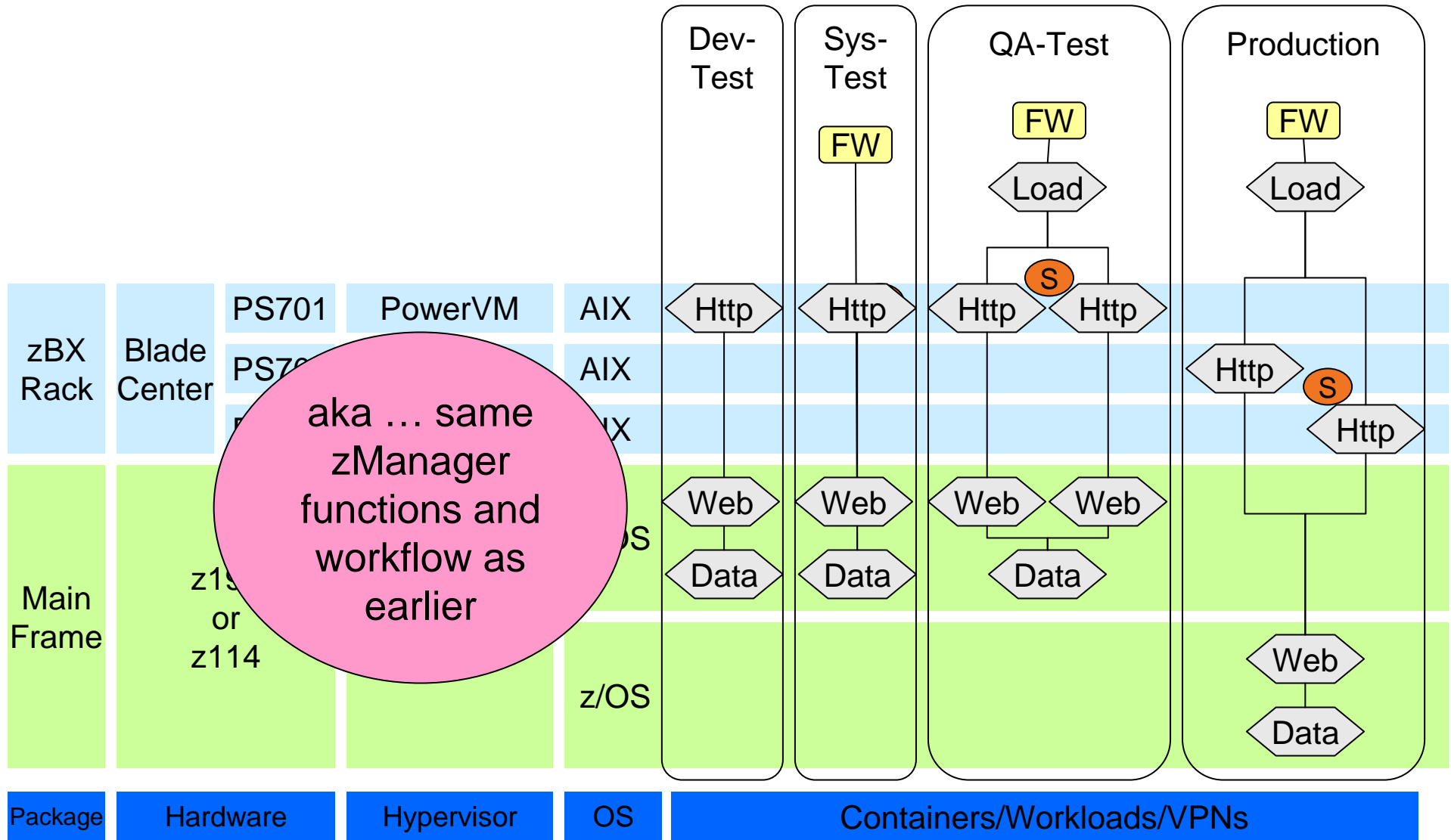
Topics

Use Case: Multi-Tier Application zEnterprise – PS701 & z/OS



Topics

Use Case: Multi-Tier Application zEnterprise – PS701 & z/OS



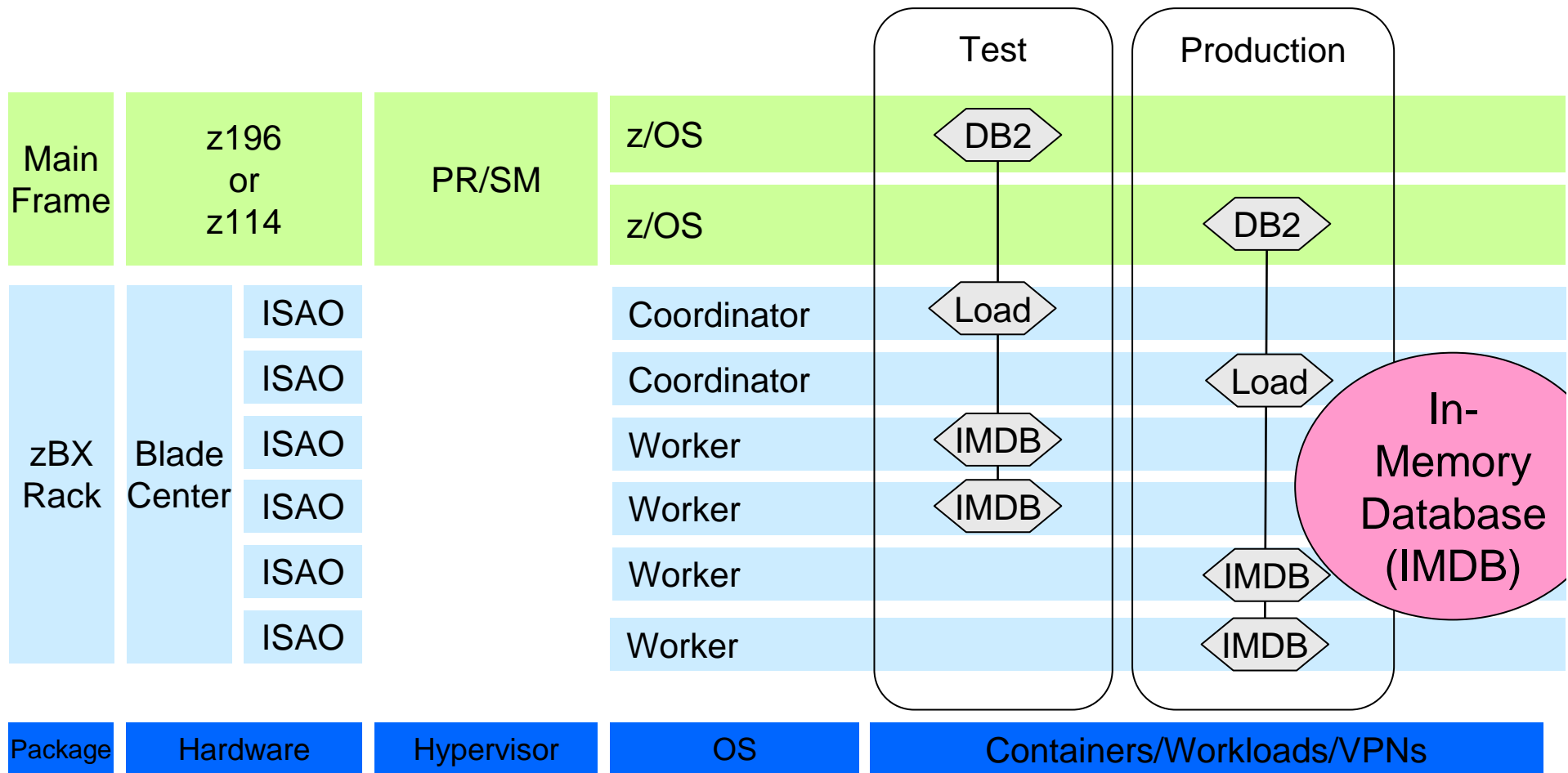
Use Case 3



Topics



Use Case: Real-Time Analytics zEnterprise – ISAO & z/OS



In-Memory Database (IMDB)

Package Hardware Hypervisor OS Containers/Workloads/VPNs

Topics

Use Case 4

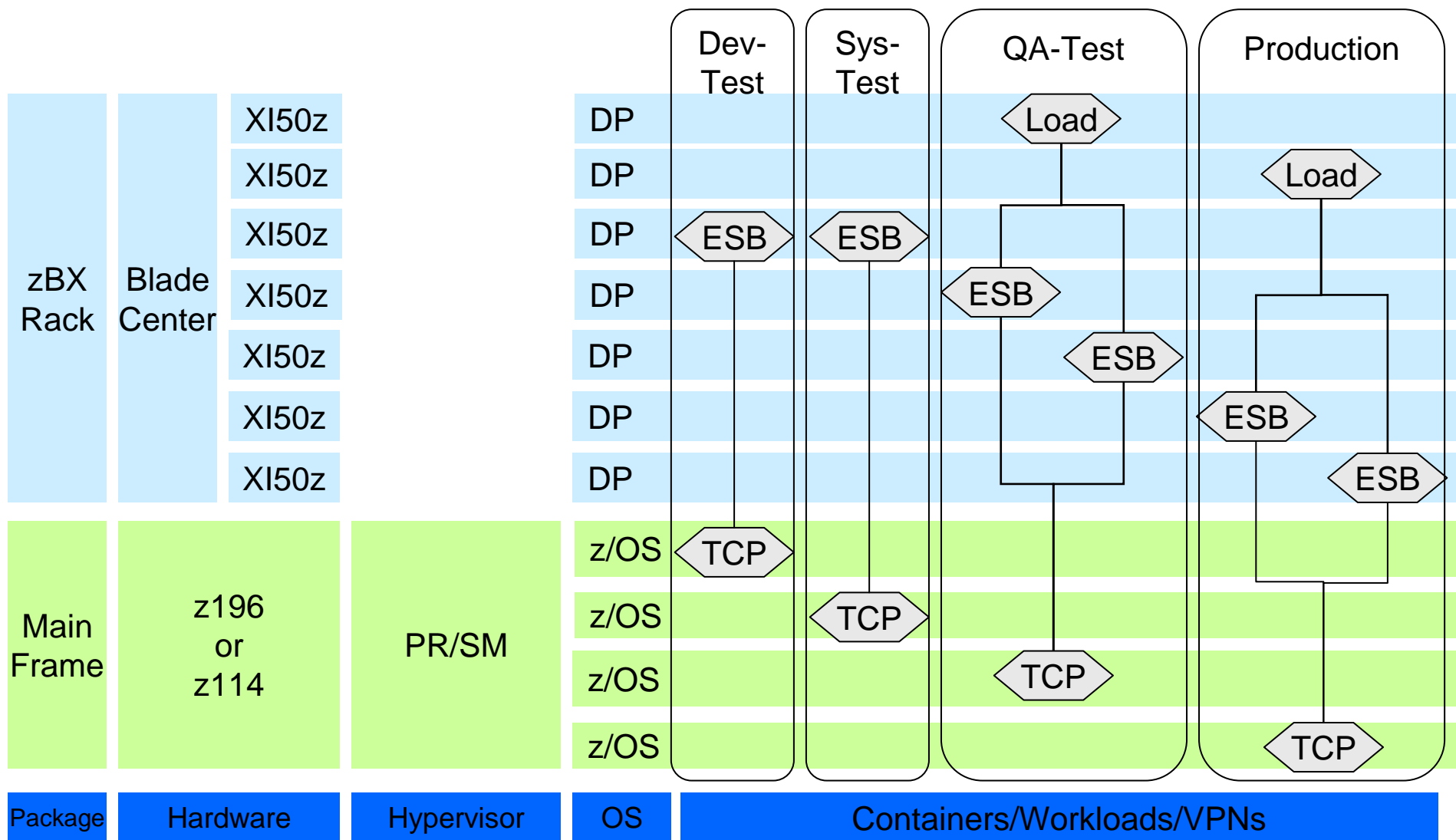


Topics



Use Case: Hardware ESB

zEnterprise – DataPower XI50z & z/OS



Topics

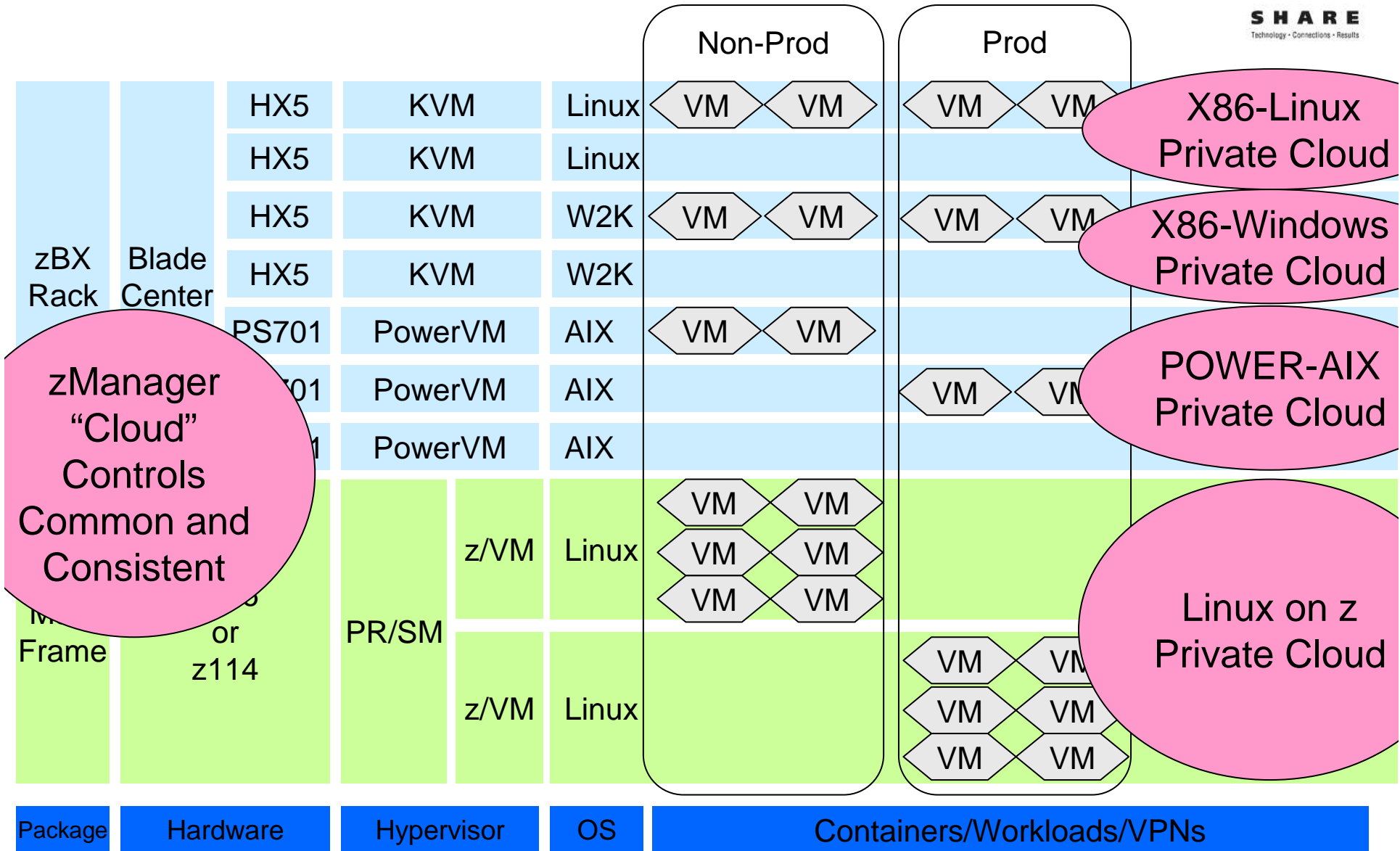
Use Case 5



Topics



Use Case: Storm Cloud



zManager
"Cloud"
Controls
Common and
Consistent

X86-Linux
Private Cloud

X86-Windows
Private Cloud

POWER-AIX
Private Cloud

Linux on z
Private Cloud

Topics

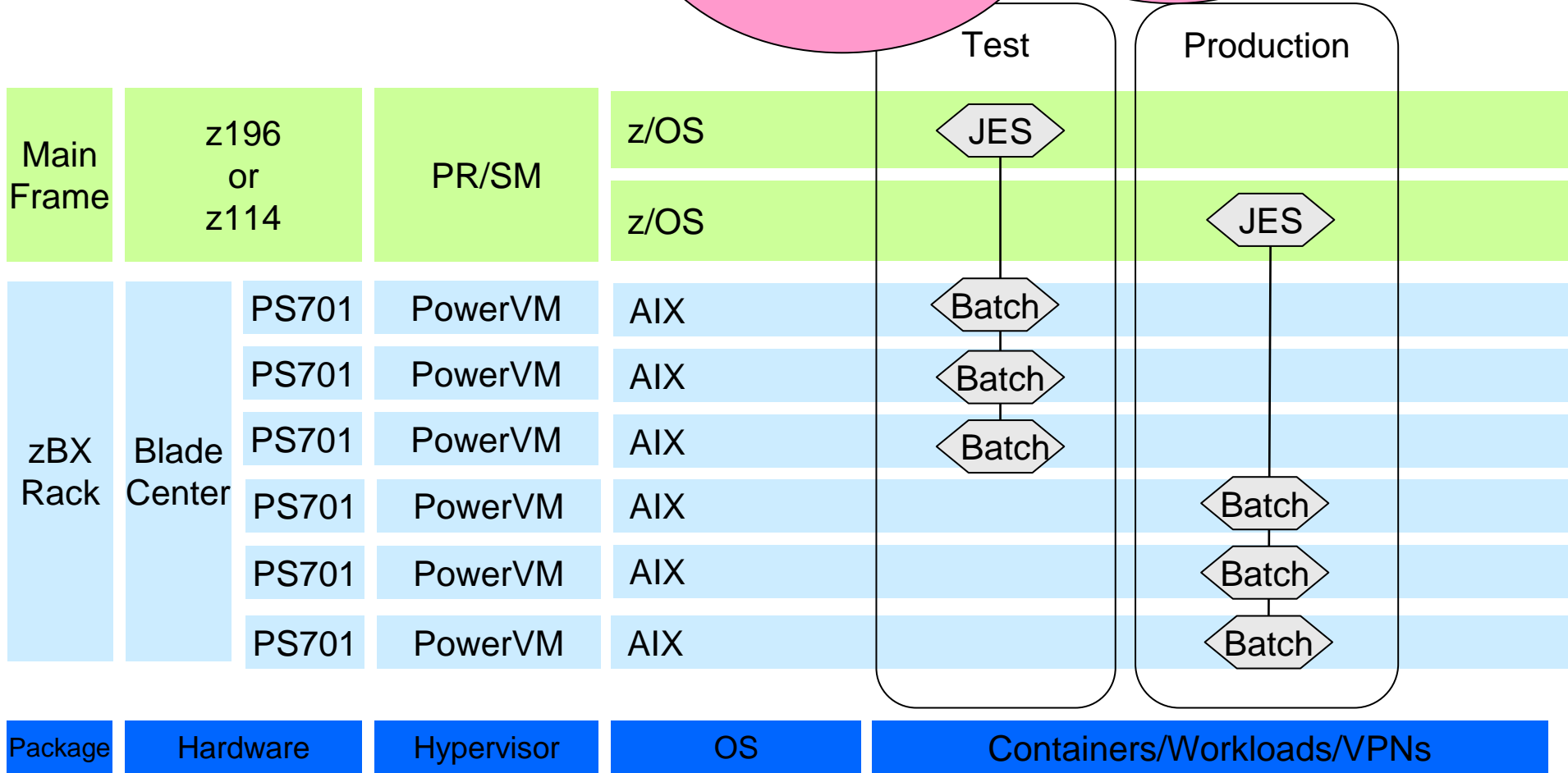
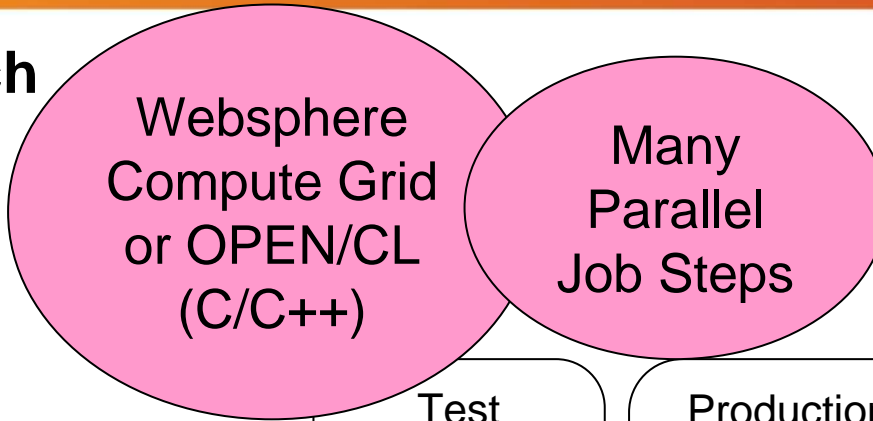
Use Case 6



Topics



Use Case: Parallel Batch



Topics

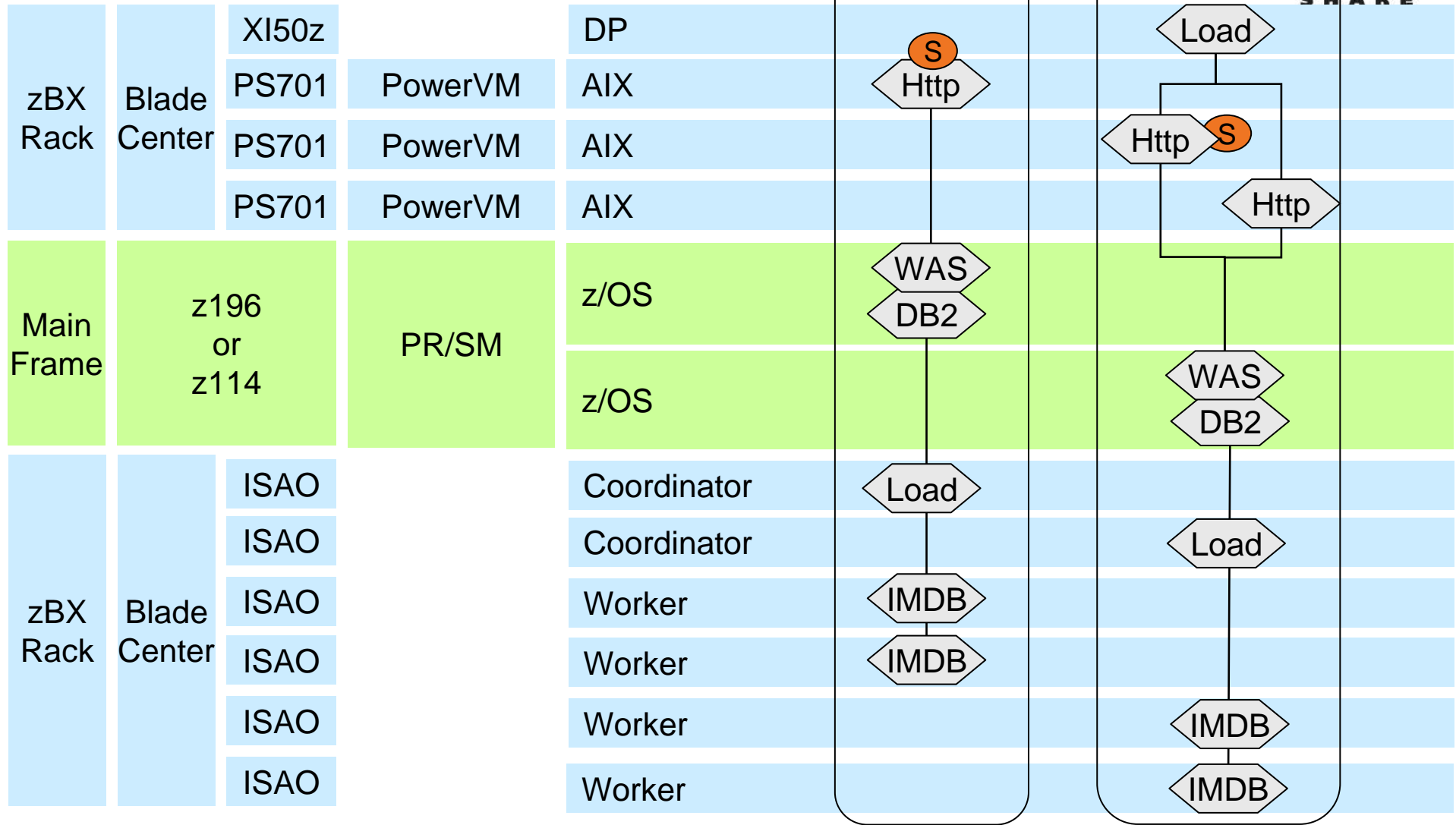
Use Case 7



Topics



Use Case: Combinatorial



Package Hardware Hypervisor OS Containers/Workloads/VPNs

Topics

End of Section



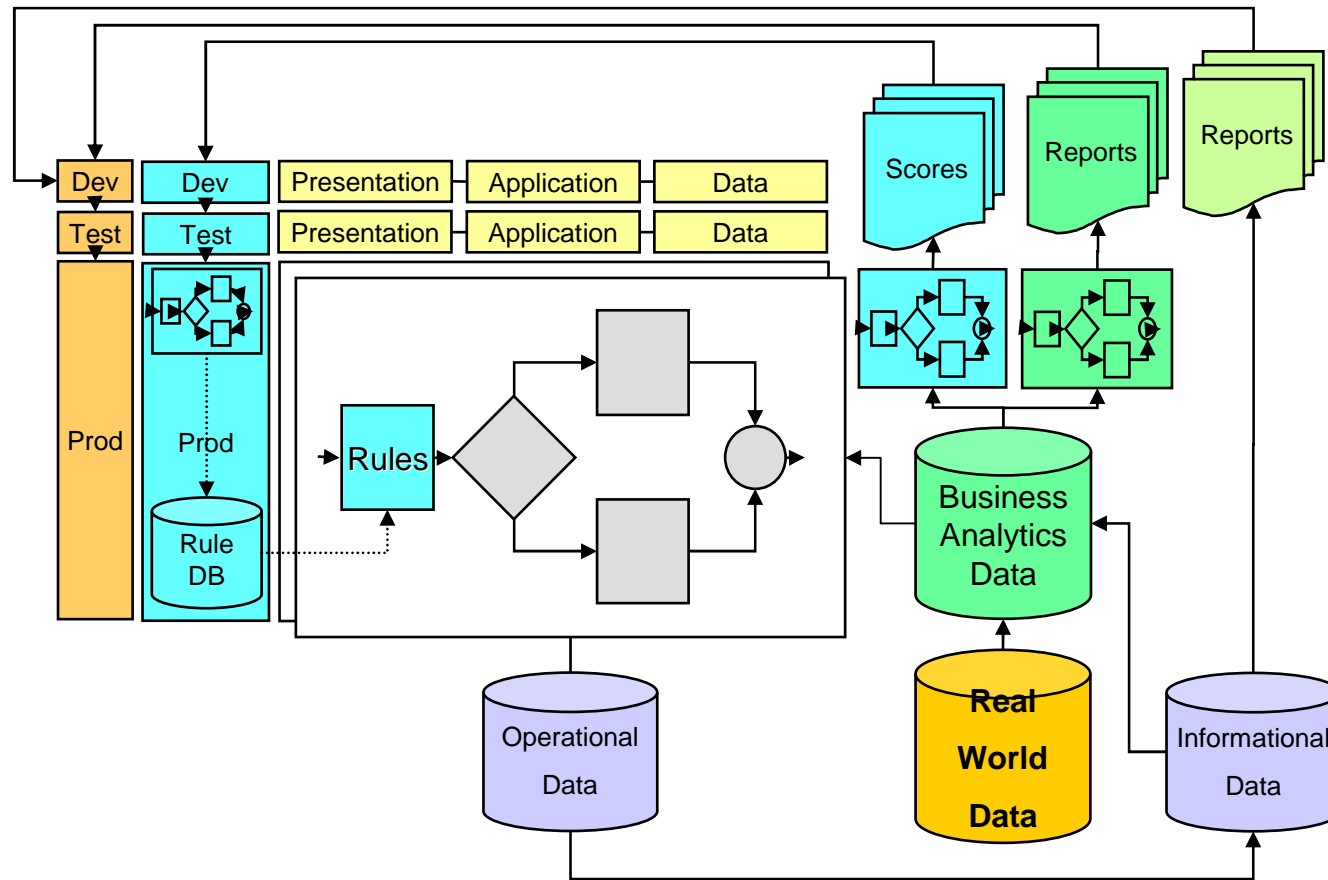
Topics



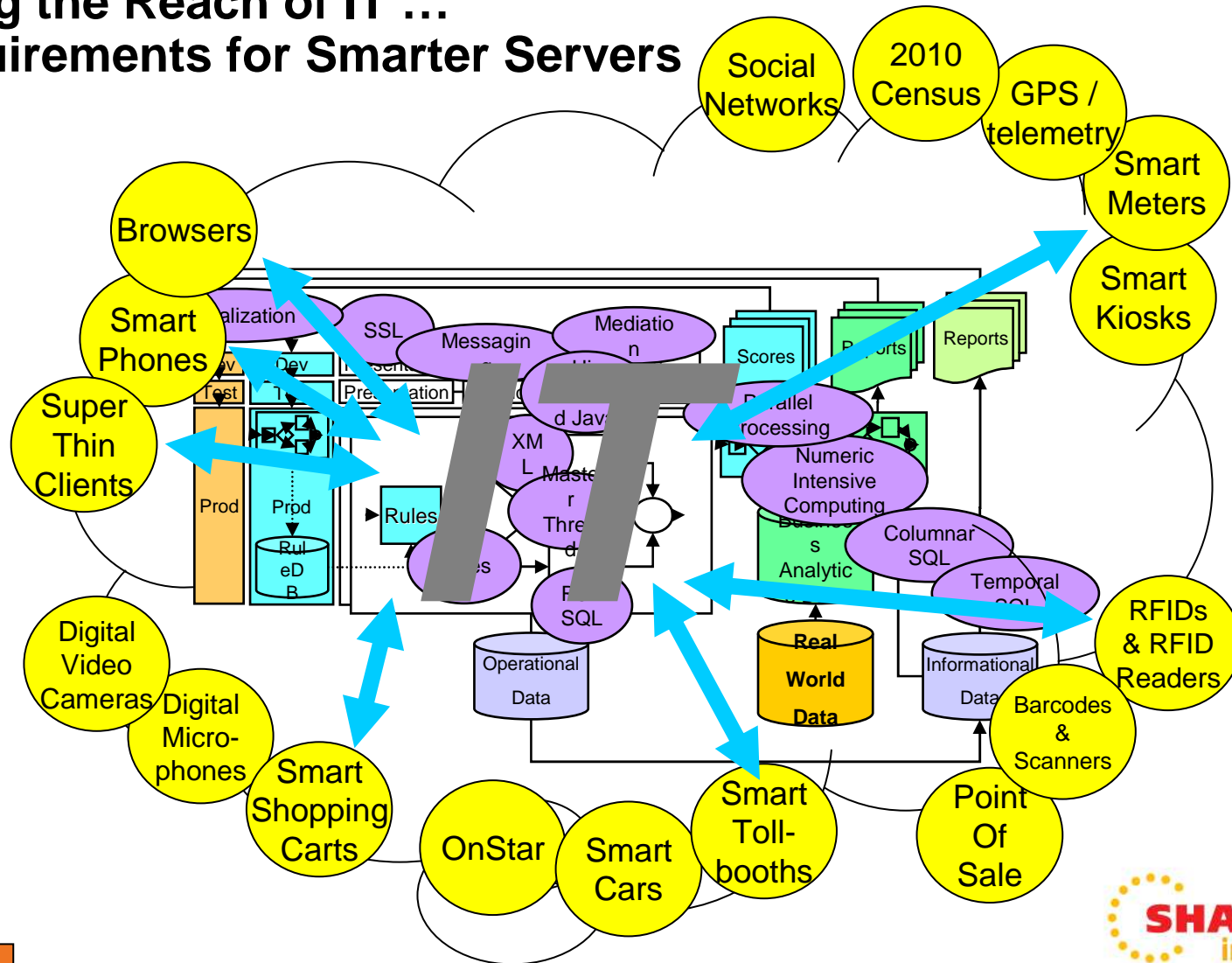
Conclusion



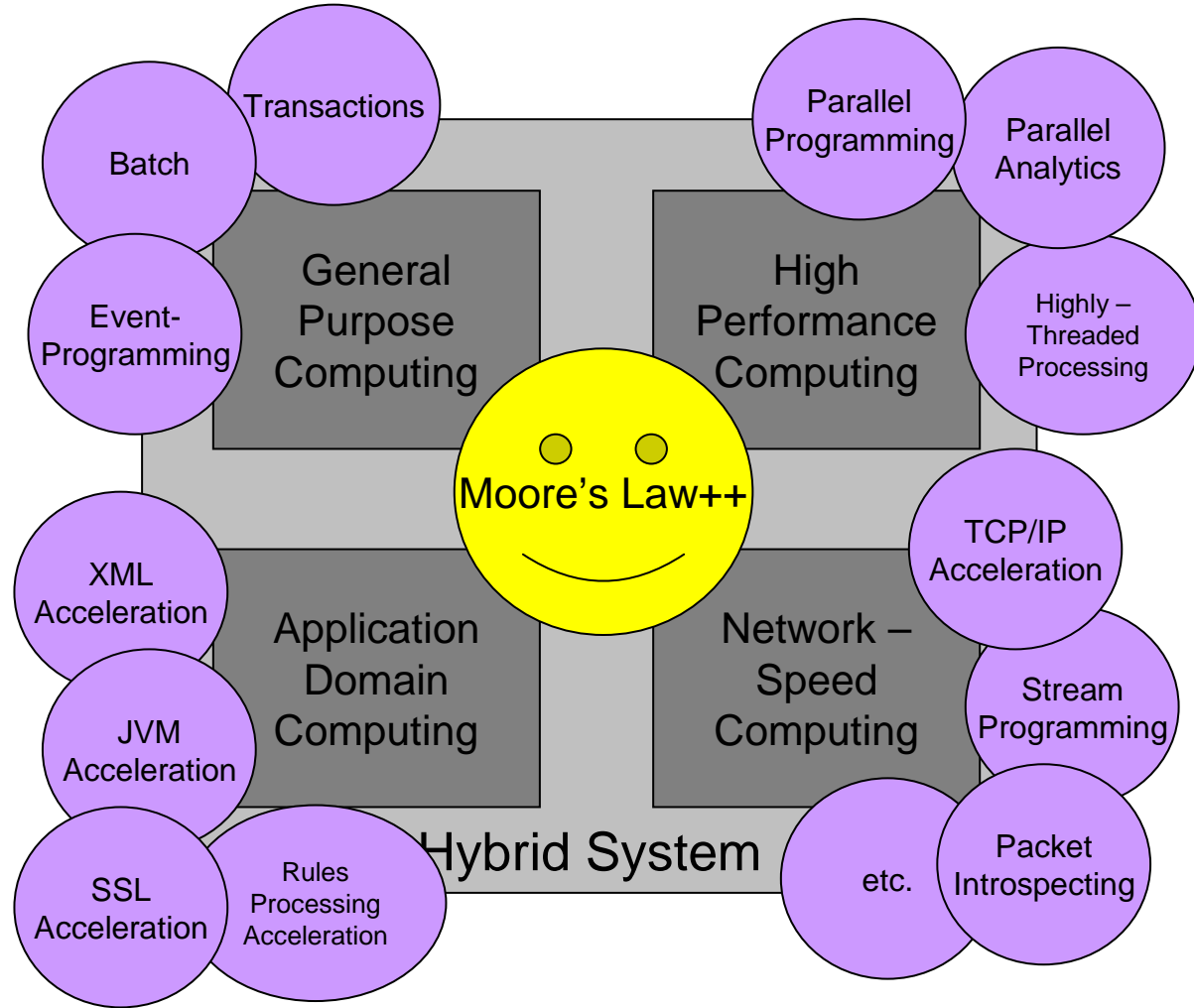
Conclusion – We Talked About... Smarter Applications... Smarter Transactions... and Smarter Business Process Improvement



**Conclusion – We Talked About...
 The Internet of Things ...
 Big Data ...
 Extending the Reach of IT ...
 and Requirements for Smarter Servers**



Conclusion – We Talked About... Hybrid Computing



Conclusion – We Talked About... State of the Art General Purpose Computing



MICROPROCESSOR:

4.4GHz
Superscalar
In Order
L1 (192KB) and
L1.5 (3MB) per core
(total:12.75MB per chip)
1B transistors
44 new instructions

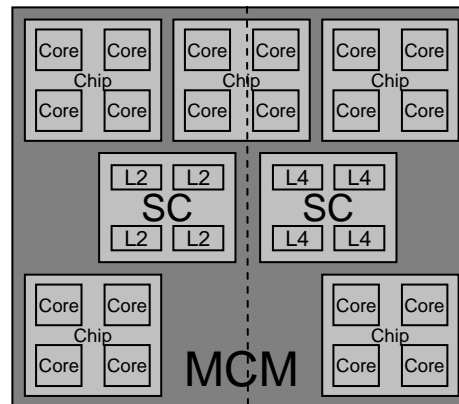
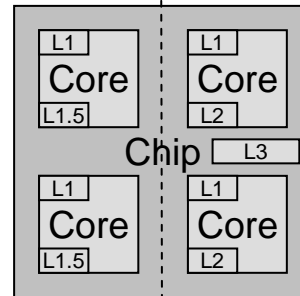
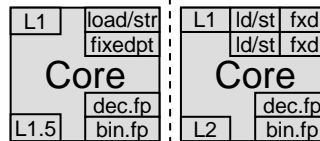
MCM:

5 “z6” chips
L2 (48MB on 2SCchips)

CEC:

1 to 4 books
64/77 cores
up to 1.5TB RAM
~1,000 to ~30,000MIPS
6GBps Infiniband

z10 EC z196



Moore's Law ... bent but not broken

MICROPROCESSOR:

5.2GHz
Super-duper-scalar
Out of Order
L1 (192KB) and
L2 (1.5MB) per core
(new) L3 per chip (24MB)
(total:30.75MB per chip)
1.4B transistors
110 new instructions

MCM:

6 “z7” chips
L4 (192MB on 2SC chips)

CEC:

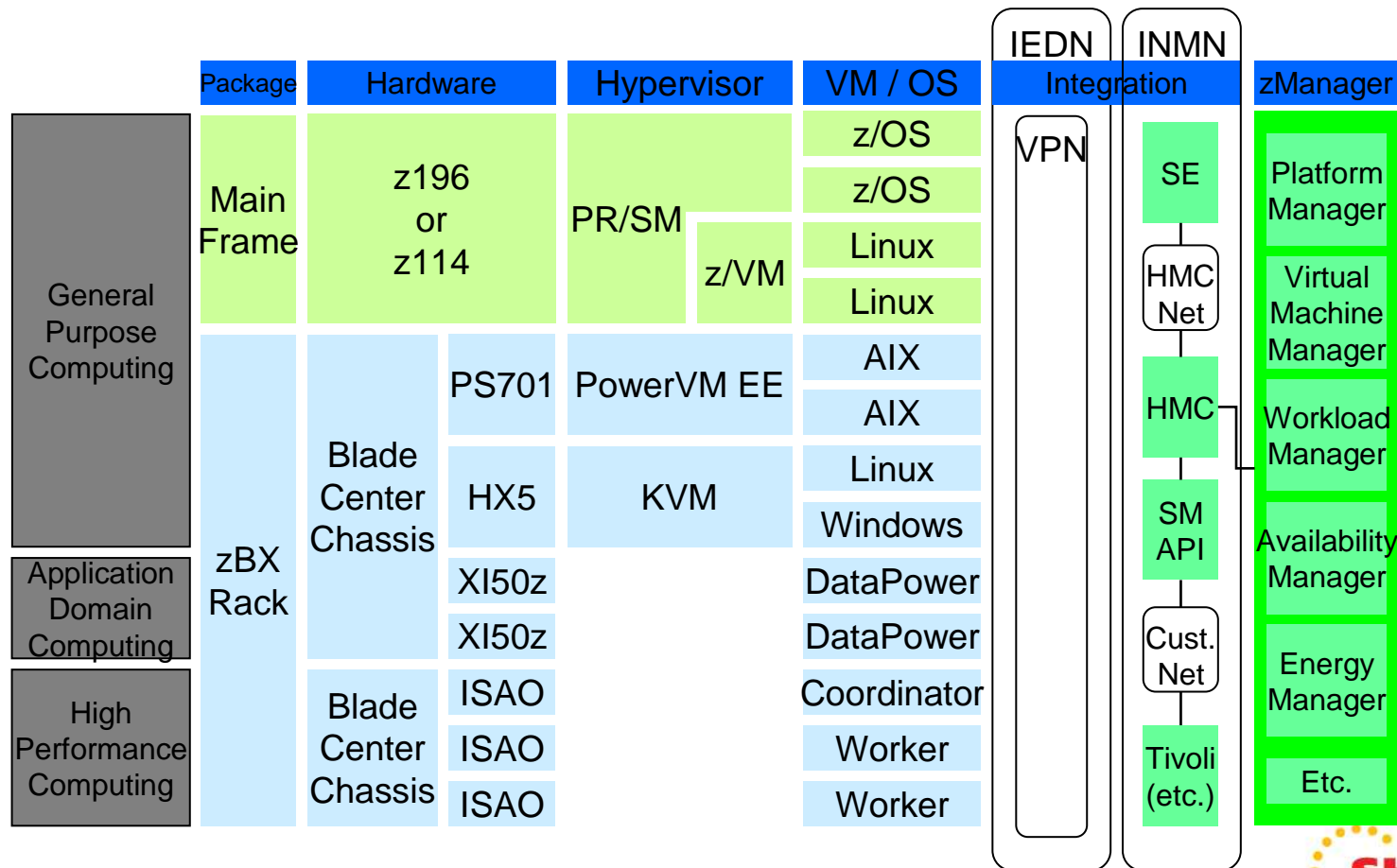
1 to 4 books
80/96 cores
up to 3.0TB **RAIM**
~1,500 to ~50,000MIPS
6GBps Infiniband
Hot plug I/O drawers
Optional Water Cooling
Optional DC Power

Conclusion – We Talked About...

zEnterprise: the World's First Hybrid Server



zEnterprise = **z196/z114** + **zBX** + **zManager**



Topics

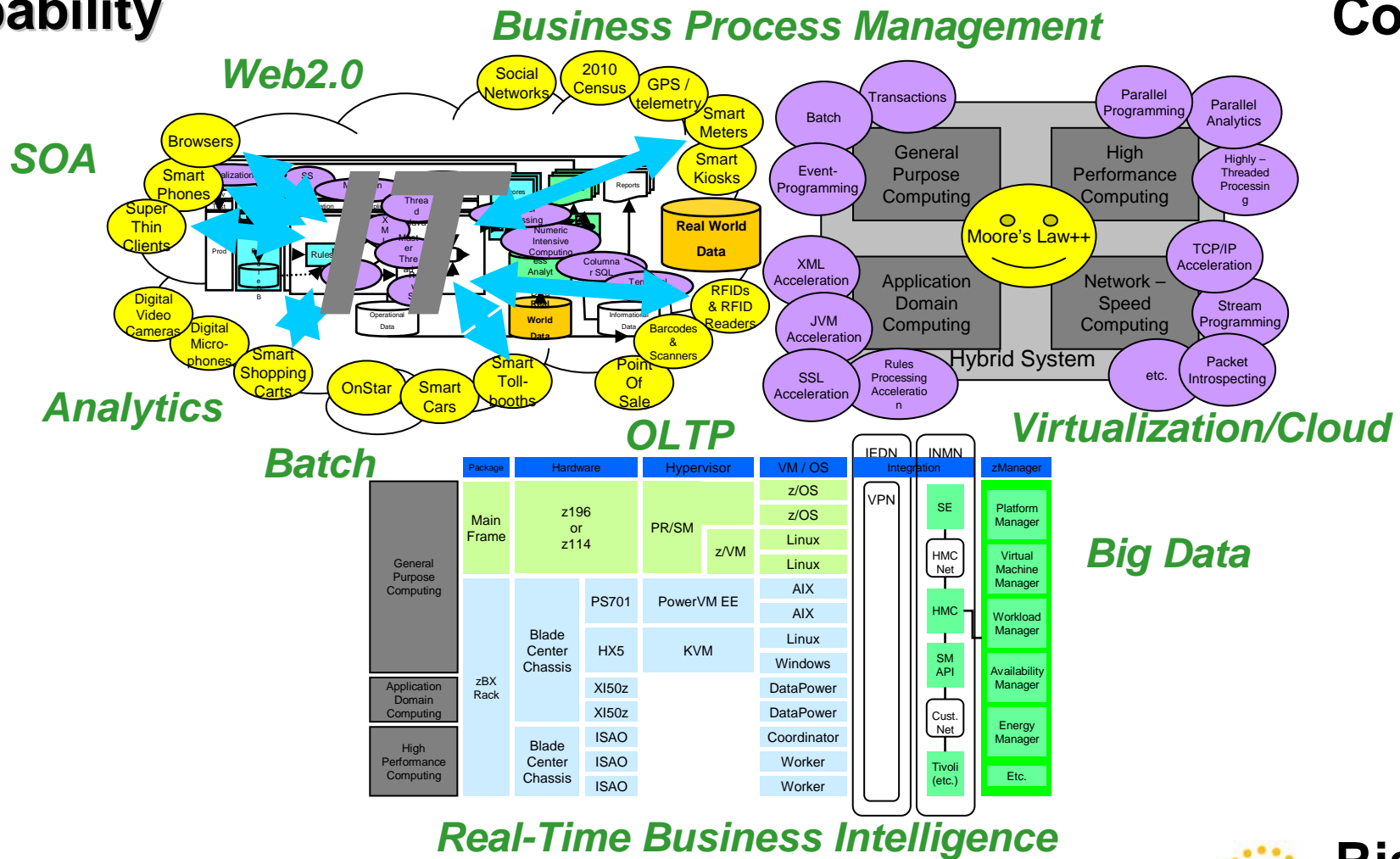


Conclusion: zEnterprise – Ready for Business



Capability

Cost



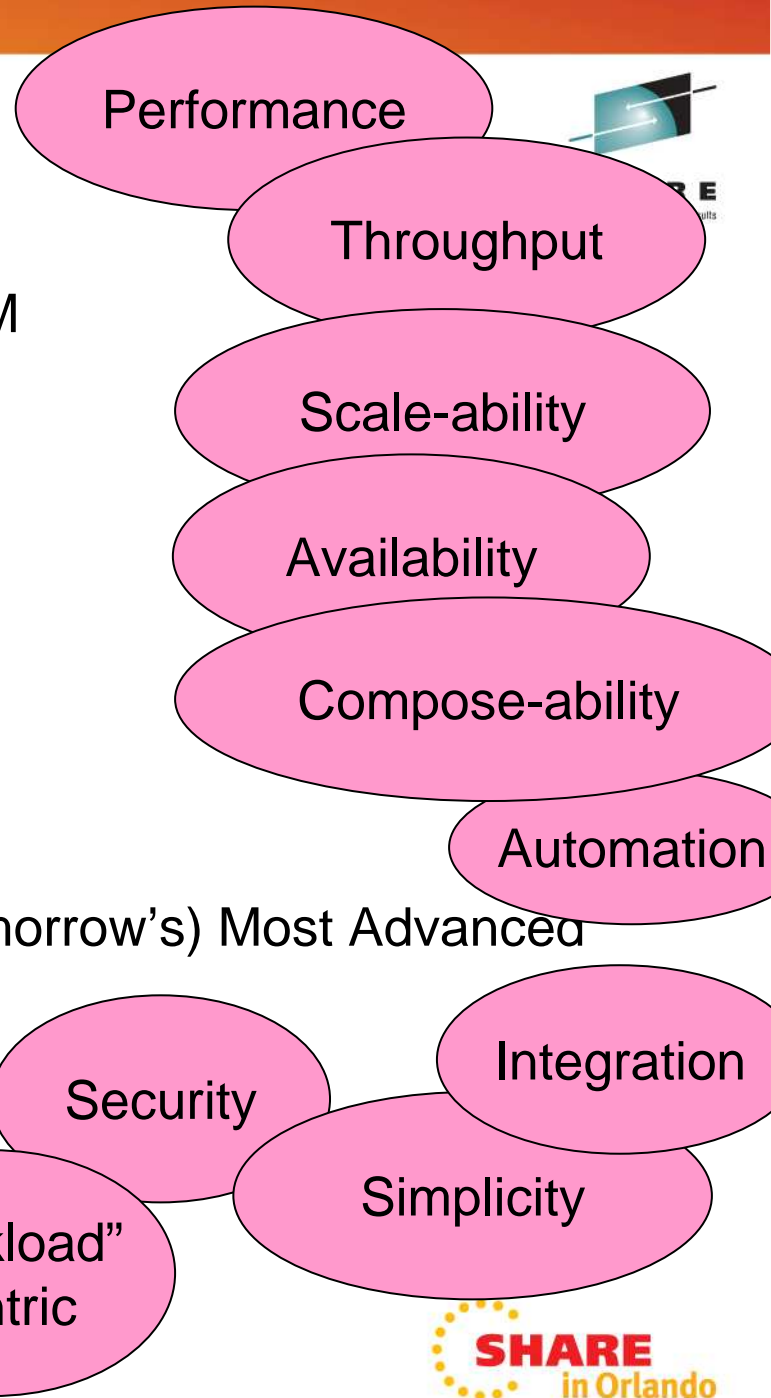
Time

Topics



Conclusion

- zEnterprise = (z196 or z114) + zBX + zURM
- Hybrid Computing
 - “optimized computing”
- The World’s Fastest “Master Thread”
 - and most highly resilient
- **Manageably** Harnessing Today’s (and Tomorrow’s) Most Advanced Computing Models
- Driving Moore’s Law to New Levels
 - Price AND Performance



End of Section



Topics



Next Steps



Topics



IBM “Next Step” Workshops and Services



- For the Business Analysts
 - Process Improvement Workshop
- For the Architects
 - Architecture and Integration Workshop
 - Right-Fit Workshop
- For Platform Decision-Making
 - Fit for Purpose Workshop
- For Financial Analysis (TCO)
 - Right-fitting Applications into Consolidated Environments (RACE) Workshop
 - EAGLE Engagement
 - Scorpion Study*
 - Portfolio Review Analysis
- For Missourians (or the Missouri-like)
 - Proof of Concept / Proof of Technology / Benchmarking Centers
 - Design Center
- For the Truly Adventurous
 - IBM Research First of a Kind Joint Development Projects

* Typically provided as a “service” at cost-recovery rates

**Thank
You**

Questions?

Comments?

Critiques?

Requests??!?!?

Monte Bauman

IBM Columbus

mbauman@us.ibm.com

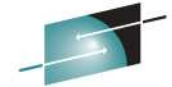
Slide Title (Type Size=28) (no more than two lines)



- First Major Topic (Type Size=24)
 - Subtopic One (Type Size=22)
 - Subtopic Two (Type Size=22)
 - Sub-subtopic (Type Size=20)
- Second Major Topic (Type Size=24)
- Third Major Topic (Type Size=24)
- Fourth Major Topic (Type Size=24)

Two-Column Slide (Type Size=28)

- Topic A (Type Size=24)
 - Subtopic 1 (Type Size=22)
 - Subtopic 2 (Type Size=22)
 - Subtopic 3 (Type Size=22)
 - Subtopic 4 (Type Size=22)
- Topic B (Type Size=24)
- Topic C (Type Size=24)
 - Subtopic 1 (Type Size=22)
 - Subtopic 2 (Type Size=22)
 - Subtopic 3 (Type Size=22)
 - Sub-subtopic 1 (Type Size=20)
 - Sub-subtopic 2 (Type Size=20)
- Topic D (Type Size=20)

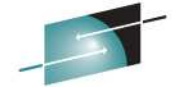


SHARE
Technology • Connections • Results

Slide with Table

Topics

SHARE
in Orlando
2011



SHARE
Technology • Connections • Results

Slide with Text & Graphic

Topics

SHARE
in Orlando
2011